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Communication and Architectural Support for Network-Based Parallel Computing

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Preface

As the performance gap between commodity microprocessors and exotic high-end processors continues to close, microprocessor-based massively parallel processors (MPPs) are becoming commonplace for achieving supercomputer performance levels. Similarly, clusters of workstations connected by local area networks (LANs) are increasingly being employed as cost-effective parallel processing systems. Such configurations are often termed Networks of Workstations (NOWs) [1], Clusters of Workstations (COWs), or simply clusters [2].

Efficient and scalable parallel processing implies efficient communication and synchronization. Although the use of workstation technology can be relatively inexpensive, commodity workstation hardware and software components have not typically provided low latency, high bandwidth inter-node communication. Strategies for improving communication and synchronization fall into several categories, some of which are:

- Better interfaces between the processor and the network
- More efficient implementations of existing end-to-end protocols (e.g., TCP/IP)
- Light-weight end-to-end communication protocols
- High-performance interconnect technology and protocols
- Operating system and architectural support for communication and synchronization
- Architectural support for distributed shared memory
- Load balancing techniques
- Collective communication support

Unlike most MPP systems, NOW systems may operate in a "shared" environment and might consist of heterogeneous workstations and networks. Such systems may be also integrated with an existing computing environment like a department or a lab, all of which makes it more difficult to achieve optimal performance.

CANPC '97—the Workshop on Communication and Architectural Support for Network-based Parallel Computing—addresses these and other issues which have an impact on the effectiveness of clusters used as parallel systems. Potential authors submitted 10-page extended abstracts which were typically reviewed by 4 referees, including at least two program committee members. We were able to accept 19 papers out of a total of 36 submissions. We believe that the resulting selections comprise an important compilation of state-of-the-art solutions for network-based parallel computing systems. This CANPC workshop was sponsored by the IEEE Computer Society, and was held in conjunction with HPCA-3, the 3rd International Symposium on High-Performance Computer Architecture, held in San Antonio on Feb. 1-5, 1997. The workshop itself took place on Feb. 1-2.

We would like to thank all of the authors who submitted papers to this workshop. Special thanks go to the program committee and the other referees

for providing us with high-quality reviews under tight deadlines. We thank Lionel Ni for his support of this workshop, including the use of his web-based review software which made our jobs considerably easier. Thanks to Rajeev Sivaram for porting and installing this software to our web server at Ohio State and maintaining it. Lastly, we thank Springer-Verlag for agreeing to an extremely tight publication schedule in order to provide the workshop attendees with these proceedings as they registered.

February 1997   Dhabaleswar K. Panda and Craig B. Stunkel

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