

# Design and Testing of Digital Microfluidic Biochips

Yang Zhao · Krishnendu Chakrabarty

# Design and Testing of Digital Microfluidic Biochips

Yang Zhao  
Advanced Micro Devices  
Nashua, NH 03062-5737  
USA

Krishnendu Chakrabarty  
Duke University ECE  
130 Hudson Hall Box 90291  
Durham, NC 27708  
USA

ISBN 978-1-4614-0369-2      ISBN 978-1-4614-0370-8 (eBook)  
DOI 10.1007/978-1-4614-0370-8  
Springer New York Heidelberg Dordrecht London

Library of Congress Control Number: 2012935796

© Springer Science+Business Media New York 2013

This work is subject to copyright. All rights are reserved by the Publisher, whether the whole or part of the material is concerned, specifically the rights of translation, reprinting, reuse of illustrations, recitation, broadcasting, reproduction on microfilms or in any other physical way, and transmission or information storage and retrieval, electronic adaptation, computer software, or by similar or dissimilar methodology now known or hereafter developed. Exempted from this legal reservation are brief excerpts in connection with reviews or scholarly analysis or material supplied specifically for the purpose of being entered and executed on a computer system, for exclusive use by the purchaser of the work. Duplication of this publication or parts thereof is permitted only under the provisions of the Copyright Law of the Publisher's location, in its current version, and permission for use must always be obtained from Springer. Permissions for use may be obtained through RightsLink at the Copyright Clearance Center. Violations are liable to prosecution under the respective Copyright Law.

The use of general descriptive names, registered names, trademarks, service marks, etc. in this publication does not imply, even in the absence of a specific statement, that such names are exempt from the relevant protective laws and regulations and therefore free for general use.

While the advice and information in this book are believed to be true and accurate at the date of publication, neither the authors nor the editors nor the publisher can accept any legal responsibility for any errors or omissions that may be made. The publisher makes no warranty, express or implied, with respect to the material contained herein.

Printed on acid-free paper

Springer is part of Springer Science+Business Media ([www.springer.com](http://www.springer.com))

*To my wife Xinying Li and to my parents  
Ning Zhao and Huaixiu Yan*

By Yang Zhao

*To my students over the years and to the  
tradition of excellence that they have  
established*

By Krishnendu Chakrabarty

# Preface

Digital microfluidics is an emerging technology that provides fluid-handling capability on a chip. Biochips based on digital microfluidics have therefore enabled the automation of laboratory procedures in biochemistry. By reducing the rate of sample and reagent consumption, digital microfluidic biochips allow continuous sampling and analysis for realtime biochemical analysis, with application to clinical diagnostics, immunoassays, and DNA sequencing. Recent advances in technology and applications serve as a powerful driver for research on computer-aided design (CAD) tools for biochips.

This book is focused on a design automation framework that addresses chip synthesis, droplet routing, control-pin mapping, testing and diagnosis, and error recovery. In contrast to prior work on automated design techniques for digital microfluidics, the emphasis here is on practical CAD optimization methods that can target different design problems in a unified manner. Constraints arising from the underlying technology and the application domain are directly incorporated in the optimization framework.

The avoidance of cross-contamination during droplet routing is a key design challenge for biochips. A droplet-routing method has been proposed to avoid cross-contamination in the optimization of droplet flow paths. The proposed approach targets disjoint droplet routes and synchronizes wash-droplet routing with functional droplet routing, in order to reduce the duration of droplet routing while avoiding the cross-contamination between different droplet routes. In order to avoid cross-contamination between successive routing steps, an optimization technique is used to minimize the number of wash operations that must be used between successive routing steps.

In pin-constrained digital microfluidic biochips, concurrently implemented fluidic operations may involve pin-actuation conflicts if they are not carefully synchronized. A two-phase optimization method has been proposed to identify and synchronize these fluidic operations. The goal is to implement these fluidic operations without pin-actuation conflict, and minimize the duration of implementing the outcome sequence after synchronization.

Due to the interdependence between droplet routing and pin-count reduction, this book presents two optimization methods to concurrently solve the droplet-routing and the pin-mapping design problems. First, an integer linear programming (ILP)-based optimization method has been developed to minimize the number of control pins. Next, an efficient heuristic approach has been developed to tackle the co-optimization problem.

Dependability is an important system attribute for microfluidic biochips. Robust testing methods are therefore needed to ensure correct results. This book presents a built-in self-test (BIST) method for digital microfluidic biochips. This method utilizes digital microfluidic logic gates to implement the BIST architecture. A cost-effective fault diagnosis method has also been proposed to locate a single defective cell, multiple rows/columns with defective cells, as well as an unknown number of rows/columns-under-test with defective cells. A BIST method for online testing of digital microfluidic biochips has been proposed. An automatic test pattern generation (ATPG) method has been proposed for non-regular digital microfluidic chips. A pin-count-aware online testing method has been developed for pin-constrained designs to support the execution of both fault testing and the target bioassay protocol.

To better monitor and manage the execution of bioassays, control flow has been incorporated in the design and optimization framework. A synthesis method has been developed to incorporate control paths and an error-recovery mechanism during chip design. This method addresses the problem of recovering from fluidic errors that occur during on-chip bioassay execution.

In summary, this book presents a set of unified design tools for digital microfluidics. This work is expected to reduce human effort during biochip design and biochip usage, and enable low-cost manufacture and more widespread adoption for laboratory procedures.

The authors acknowledge the financial support received from the National Science Foundation. In particular, the authors thank Program Directors Dr. Sankar Basu and Dr. Dmitry Maslov for supporting this work.

# Contents

<b>1</b>	<b>Introduction</b>	1
1.1	Overview of Digital Microfluidics	5
1.2	Automated Chip Design and Testing	7
1.2.1	Synthesis Methods	7
1.2.2	Droplet-Routing Methods	10
1.2.3	Pin-Constrained Design Methods	11
1.2.4	Testing and Diagnosis Methods	13
1.3	Outline of Book	19
References		21
<b>2</b>	<b>Cross-Contamination Avoidance for Droplet Routing</b>	27
2.1	Related Prior Work	27
2.2	Disjoint Routes for Cross-Contamination Avoidance	29
2.2.1	Problem Formulation and Constraints	29
2.2.2	Routing Method	31
2.3	Synchronization of Washing Operations with Droplet Routing	34
2.3.1	One Cross-Contamination Site	34
2.3.2	Multiple Cross-Contamination Sites	37
2.4	Unification of Disjoint Routing and Wash-Operation Synchronization	40
2.5	Cross-Contamination Avoidance Across Successive Routing Subproblems	41
2.6	Evaluation	44
2.6.1	Baseline Approaches	44
2.6.2	Example 1: Multiplexed In Vitro Diagnostics	45
2.6.3	Example 2: Protein Assay	50
2.6.4	Comparison with [1]	53
2.7	Chapter Summary and Conclusions	53
References		54

<b>3 Synchronization of Concurrently-Implemented Fluidic Operations in Pin-Constrained Biochips . . . . .</b>	57
3.1 Synchronization Problem . . . . .	57
3.2 Merging of Sequences (Phase 1) . . . . .	60
3.3 Parallelization of Sequences (Phase 2) . . . . .	61
3.3.1 Parallelization Based on Integer Linear Programming . . . . .	62
3.3.2 Parallelization with Incubation Vectors . . . . .	65
3.3.3 Parallelization Based on a Heuristic Method . . . . .	67
3.4 Synchronization with the Addition of Control Pins . . . . .	69
3.5 Results . . . . .	72
3.5.1 Evaluation using a Biochip for $n$ -Plex Bioassay (Commercial Prototype) . . . . .	72
3.5.2 Evaluation for an Experimental University Chip . . . . .	77
3.6 Chapter Summary and Conclusions . . . . .	80
References . . . . .	81
<b>4 Optimization of Droplet Routing and Control-Pin Mapping to Electrodes . . . . .</b>	83
4.1 Problem Formulation . . . . .	83
4.2 ILP-Based Method for Co-Optimization . . . . .	85
4.2.1 Objective Function . . . . .	85
4.2.2 Constraints . . . . .	86
4.3 Heuristic Method for Co-Optimization . . . . .	92
4.3.1 ILP Model for Single Subproblem . . . . .	92
4.3.2 Concatenation of Electrode-Actuation Sequences with Broadcast Addressing for Multiple Subproblems . . . . .	94
4.4 Experimental Results . . . . .	95
4.4.1 Commercial Biochips . . . . .	96
4.4.2 Experimental Biochip for Multiplexed In vitro Diagnostics and Protein Assay . . . . .	103
4.5 Chapter Summary and Conclusions . . . . .	106
References . . . . .	107
<b>5 Built-In Self Test and Diagnosis . . . . .</b>	109
5.1 Digital Microfluidic Logic Gates . . . . .	109
5.1.1 Definitions and Experimental Setup . . . . .	109
5.1.2 Microfluidic OR Gate . . . . .	111
5.1.3 Microfluidic AND Gate . . . . .	113
5.1.4 Microfluidic Inverter and XOR Gate . . . . .	115
5.2 Droplet Compactor for BIST . . . . .	116
5.2.1 BIST for Parallel Scan-like Test . . . . .	118
5.2.2 BIST for Functional Testing . . . . .	124
5.2.3 Application to Pin-Constrained Chip . . . . .	129

5.3	Fault Diagnosis . . . . .	134
5.3.1	Single-Fault Diagnosis . . . . .	135
5.3.2	Multiple-Fault Diagnosis . . . . .	140
5.3.3	Fault Diagnosis for an Unknown Number of Faults . . . . .	141
5.3.4	Evaluation . . . . .	144
5.4	Summary and Conclusions . . . . .	147
	References . . . . .	147
<b>6</b>	<b>On-Line Testing and Test Generation . . . . .</b>	<b>149</b>
6.1	On-Line Testing Using Reconfigurable Digital Microfluidic Compactors . . . . .	149
6.1.1	On-Line Testing and ILP Model . . . . .	150
6.1.2	Evaluation . . . . .	156
6.2	Automatic Test Pattern Generation Method . . . . .	158
6.2.1	Non-Regular Digital Microfluidic Array . . . . .	158
6.2.2	ATPG Method . . . . .	160
6.2.3	An ILP Model for Test-Pattern Compaction . . . . .	165
6.2.4	Evaluation . . . . .	166
6.3	Pin-Count-Aware Online Testing . . . . .	168
6.3.1	On-Line Testing Based on Array Partitioning . . . . .	168
6.3.2	Pin-Count-Aware Test Scheduling . . . . .	170
6.3.3	Evaluation . . . . .	172
6.4	Chapter Summary and Conclusions . . . . .	177
	References . . . . .	177
<b>7</b>	<b>Integrated Control-Path Design and Error Recovery . . . . .</b>	<b>179</b>
7.1	Control-Path Design and Rollback-Recovery Mechanism . . . . .	179
7.1.1	Checkpointing and Re-Execution Subroutine . . . . .	180
7.1.2	Droplet Preparation for the Re-Execution Subroutine . . . . .	181
7.2	Error-Propagation Estimates for Checkpoint Insertion . . . . .	184
7.3	Control-Path Synthesis . . . . .	185
7.4	Software Programs and Implementation for Rollback Recovery . . . . .	187
7.5	Evaluation . . . . .	189
7.5.1	Protein Assay . . . . .	189
7.5.2	Interpolating Mixing Architecture . . . . .	195
7.6	Chapter Summary and Conclusions . . . . .	199
	References . . . . .	199
<b>8</b>	<b>Conclusions . . . . .</b>	<b>201</b>