

Secure Generators of q -valued Pseudo-Random Sequences on Arithmetic Polynomials

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Abstract. A technique for controlling errors in the functioning of nodes for the formation of q -valued pseudo-random sequences (PRS) operating under both random errors and errors generated through intentional attack by an attacker is provided, in which systems of characteristic equations are realized by arithmetic polynomials that allow the calculation process to be parallelized and, in turn, allow the use of redundant modular codes device.

Keywords: q -valued pseudo-random sequences · Secure generators of q -valued pseudo-random sequences · Primitive polynomials · Galois fields · Linear recurrent shift registers · Modular arithmetic · Parallel logical calculations by arithmetic polynomials · Error control of operation · Redundant modular codes

1 Introduction

In the theory and practice of cryptographic information protection, one of the key tasks is the formation of PRS which width, length and characteristics meet modern requirements [1]. Many existing solutions in this area aim to obtain a binary PRS of maximum memory length with acceptable statistical characteristics [2]. However, recently it is considered that one of the further directions in the development of means of information security (MIS) is the use of multi-valued functions of the algebra of logic (MFAL), in particular, using the PRS over the Galois field $\text{GF}(q)$ ($q > 2$), which have a wider spectrum of unique properties comparing to binary PRS [3].

The nodes of the formation of the q -valued PRS, like the others, are prone to failures and malfunction, which leads to the occurrence of errors in their functioning. In addition to random errors occurrence in the generation of PRS related to “unintentional” failures and malfunctions caused by various causes: aging of the element base, environmental influences, severe operating conditions, etc. (reasons typical for reliability theory), there are deliberate actions of an attacker aimed to create massive failures of electronic components of the formation

nodes of PRS due to the hardware errors generation (one of the types of information security threats) [4].

Many methods have been developed to provide the necessary level of reliability of the digital devices functioning; the most common are backup methods and methods of noise-immune coding. However, backup methods do not provide the necessary levels of operation reliability with limitations on hardware costs, and methods of noise-immune coding are not fully adapted to the specifics of the construction and operation of MIS, in particular, generators of q -valued PRS.

The work [5] offers a solution that overcomes the complexity of using code control for the nodes of the binary PRS generation, based on the “arithmetic” of logical count and the application of the redundant modular code device, which provides the necessary level of security for their functioning. However, the solution obtained is limited to exclusive applicability in the formation of binary PRS. At the same time, work [6], is known where by means of “arithmetic” of logical count the task of parallelizing the nodes of forming of binary PRS is solved, but without monitoring their functioning. As a result, it becomes necessary to generalize the solutions obtained to ensure the security of the functioning of the nodes of q -valued PRS formation.

2 General Principles of Building Generators of q -valued PRS

The most common and tested methods for PRS are algorithms and devices of PRS generation — linear recurrent shift registers (q -LFSR) with feedback — based on the use of recurrent logical expressions [2].

The construction of the q -LFSR over the field $GF(q)$ is carried out from the given generating polynomial:

$$K(x) = \sum_{i=0}^m k_{m-i} x^{m-i}, \quad (1)$$

where m — is the polynomial degree $K(x)$, $m \in N$; $k_i \in GF(q)$, $k_m = 1$, $k_0 \neq 0$.

Thus, the q -LFSR element is formed in accordance with the following characteristic equation [7]:

$$a_{p+m} = -k_{m-1}a_{p+m-1} - k_{m-2}a_{p+m-2} - \dots - k_1a_{p+1} - k_0a_p. \quad (2)$$

The Eq. (2) is a recursion which describes an infinite q -valued PRS with period $q^m - 1$ (with nonzero initial state, as well as under condition that the polynomial (1) is primitive over the field $GF(q)$), each nonzero state appears once per period.

A homogeneous recurrent Eq. (2) can be presented in the following form:

$$a_{p+m} = k_{m-1}a_{p+m-1} \oplus k_{m-2}a_{p+m-2} \oplus \dots \oplus k_1a_{p+1} \oplus k_0a_p$$

or

$$a_{p+m} = \bigoplus_{i=1}^m k_{i-1} a_{p+i-1}, \quad (3)$$

where \oplus — is the symbol of addition on module q .

The q -LFSR corresponding to the polynomial (3) is shown in Fig. 1, whose cells contain field $\text{GF}(q)$ elements: a_p, \dots, a_{p+m-1} .

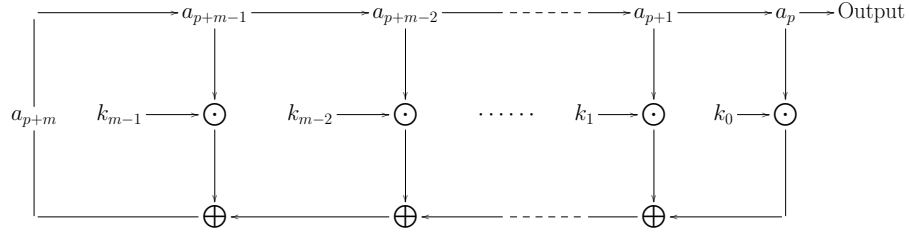


Fig. 1. Structural diagram of the operation of the sequential q -LFSR in accordance with formula (3) (\oplus and \odot — according to transaction of addition and multiplication of the $\text{mod } q$)

3 Analysis of Possible Modifications q -valued PRS Caused by the Error Occurred

It is known that the consequences of accidental errors that occur during the PRS generation associated with “unintentional” failures, as well as the consequences of intentional actions by an attacker based on the use of thermal, high-frequency, ionizing or other external influences in order to obtain mass malfunctions of the equipment by initiation of calculation errors, lead to similar types of PRS modification.

Fig. 2 shows main types of modification of PRS over the $\text{GF}(q)$ field. The attacker’s actions based on error generation are highly effective for most of the known and currently used algorithms for generating q -valued PRS [8–10]. It is known [11] that the probability of error generation is proportional to the irradiation time of the respective registers in a favorable state for the error occurrence and to the number of bits within which an error is expected. This type of impact has not been sufficiently studied and therefore represents a threat to the information security of modern and promising MIS functioning.

One of the ways to solve this problem is to develop a technique for improving the safety of the operation of the MIS nodes most susceptible to these effects, in particular, the nodes of q -valued PRS formation.

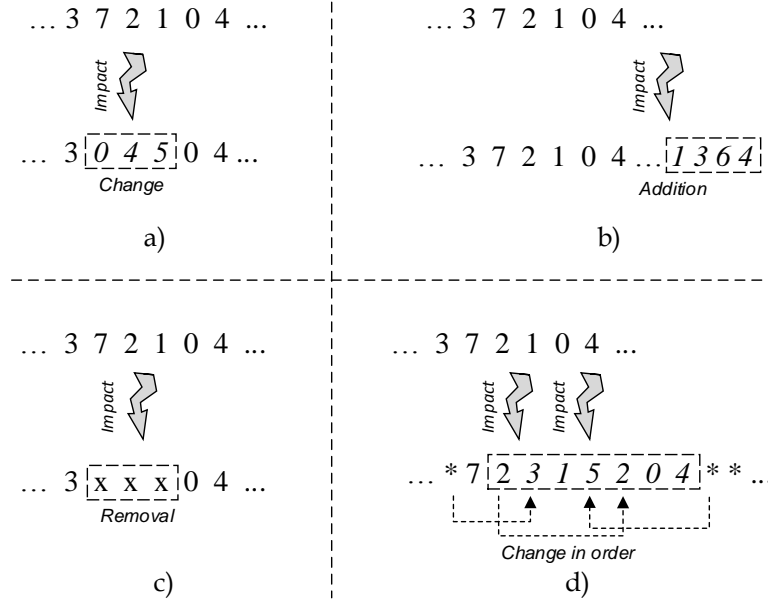


Fig. 2. The main types of PRS modification: a) change in the elements of the PRS, b) addition of new PRS elements, c) removal of the CAP elements, d) change in the order of the PRS elements

4 Analysis of Ways to Control the Generation of q -valued PRS

Currently, the necessary level of security for the functioning of the nodes for the q -valued PRS formation is achieved both through the use of redundant equipment (structural backup) and temporary redundancy due to various calculations repetition.

In the field of digital circuit design solutions based on the use of block redundant coding methods are known. To apply these methods to q -valued PRS generators it is necessary to solve the problem of parallelizing the calculation process of the q -valued PRS.

The solution of the problem is based on the use of classical parallel recursion calculation algorithms [12], for which the characteristic Eq. (3) corresponding to the generating polynomial (2) can be represented as a system of characteristic

equations:

(4)

where $k_{i-1}^{(j)} \in \text{GF}(q)$; $j = 0, 1, \dots, m-2, m-1$.

The system (4) forms an information matrix:

$$\mathbf{G}_{\text{Inf}} = \left\| \begin{array}{ccccc} k_0^{(m-1)} & k_1^{(m-1)} & \dots & k_{m-2}^{(m-1)} & k_{m-1}^{(m-1)} \\ k_0^{(m-2)} & k_1^{(m-2)} & \dots & k_{m-2}^{(m-2)} & k_{m-1}^{(m-2)} \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ k_0^{(1)} & k_1^{(1)} & \dots & k_{m-2}^{(1)} & k_{m-1}^{(1)} \\ k_0^{(0)} & k_1^{(0)} & \dots & k_{m-2}^{(0)} & k_{m-1}^{(0)} \end{array} \right\|.$$

Similar result can be obtained in another convenient way [1]:

$$\mathbf{G}_{\text{Inf}} = \left\| \begin{array}{ccccc} k_{m-1} & k_{m-2} & \dots & k_1 & k_0 \\ 1 & 0 & \dots & 0 & 0 \\ 0 & 1 & \ddots & 0 & 0 \\ 0 & 0 & \dots & 0 & 0 \\ 0 & 0 & \dots & 1 & 0 \end{array} \right\|^m,$$

where the elements raised to the power m are of a matrix which is created according to the known rules of linear algebra for the calculation of the next q -valued element of the PRS a_{p+m} :

$$\left\| \begin{array}{c} a_{p+m} \\ a_{p+m-1} \\ \vdots \\ a_{p+2} \\ a_{p+1} \end{array} \right\| = \left\| \begin{array}{c} a_{p+m-1} \\ a_{p+m-2} \\ \vdots \\ a_{p+1} \\ a_p \end{array} \right\| \cdot \left\| \begin{array}{ccc} k_{m-1} & \dots & k_0 \\ 1 & \dots & 0 \\ 0 & \dots & 0 \\ 0 & \dots & 0 \\ 0 & \dots & 0 \end{array} \right\|_q,$$

where $|\cdot|_q$ — is the smallest nonnegative deduction of the number “.” on module q .

The technique for raising a matrix to the power can be performed with help of symbolic calculations in any computer algebra system with the subsequent simplification (in accordance with the axioms of the algebra and logic) of the elements of the resulting matrix of the form $Yk_j^b = k_j$ according to the rules:

$$\mathbf{A}_t = \left| \mathbf{G}_{\text{Inf}} \cdot \mathbf{A}_{t-1} \right|_q,$$
$$\begin{aligned}\mathbf{A}_t &= \begin{bmatrix} a_{t,p+m-1} & a_{t,p+m-2} & \dots & a_{t,1} & a_{t,0} \end{bmatrix}^\top, \\ \mathbf{A}_{t-1} &= \begin{bmatrix} a_{t-1,p+m-1} & a_{t-1,p+m-2} & \dots & a_{t-1,1} & a_{t-1,0} \end{bmatrix}^\top.\end{aligned}$$
$$\left\{ \begin{array}{l} a_{t,p+m-1} = \bigoplus_{i=1}^m k_{i-1}^{(m-1)} a_{t-1,p+i-1}, \\ \dots\dots\dots \\ a_{t,0} = \bigoplus_{i=1}^m k_{i-1}^{(0)} a_{t-1,p+i-1}, \\ a_{t,p+r-1}^* = \bigoplus_{i=1}^r c_{i-1}^{(r-1)} a_{t-1,p+i-1}, \\ \dots\dots\dots \\ a_{t,0}^* = \bigoplus_{i=1}^r c_{i-1}^{(0)} a_{t-1,p+i-1}, \end{array} \right.$$

The forming matrix takes the form:

$$\mathbf{G}_{\text{Gen}} = \left\| \begin{array}{ccccc} k_0^{(m-1)} & k_1^{(m-1)} & \dots & k_{m-2}^{(m-1)} & k_{m-1}^{(m-1)} \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ k_0^{(0)} & k_1^{(0)} & \dots & k_{m-2}^{(0)} & k_{m-1}^{(0)} \\ c_0^{(r-1)} & c_1^{(r-1)} & \dots & c_{r-2}^{(r-1)} & c_{r-1}^{(r-1)} \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ c_0^{(0)} & c_1^{(0)} & \dots & c_{r-2}^{(0)} & c_{r-1}^{(0)} \end{array} \right\|.$$

$$\mathbf{A}_t^* = [a_{t,p+m-1} \dots a_{t,0} \ a_{t,p+r-1}^* \dots a_{t,0}^*]^\top$$
$$\mathbf{A}_t^* = |\mathbf{G}_{\text{Gen}} \cdot \mathbf{A}_{t-1}|_q.$$

The anti-jamming decoding procedure is performed using known rules [13].

The use of linear redundant codes and “hot” backup methods is not the only option for realizing functional diagnostics and increasing the fault tolerance of digital devices. Important advantages for these purposes are found in arithmetic redundant codes, in particular, the so-called AN-codes and codes of modular arithmetic (MA). However, arithmetic redundant codes are not applicable to logical data types. In logical calculations, their structure collapses, which leads to the impossibility of monitoring errors in logical calculations.

The use of arithmetic redundant codes to control logical data types must be ensured by the introduction of additional procedures related to the “arithmetic” of the logical count.

5 The Procedure for Parallelizing the Generation of q -valued PRS by Means of Arithmetic Polynomials

Parallelizing the “calculation” processes of complex systems or minimizing the number of operations involving the use of all resources makes it possible to achieve any utmost characteristic or quality index, which in turn is necessary in most practically important cases. In turn, the new direction formed at the end of the last century – parallel-logical calculations through arithmetic (numerical) polynomials [14], also allowed to provide “useful” structural properties. It became possible to use arithmetic redundant codes to control logical data types and increase the fault tolerance of implementing devices by representing arithmetic expressions [14] as logical operations, in particular, by linear numerical polynomials (LNP) and their modular forms [15].

In [5] an algorithm for parallelizing the generation of binary PRS is presented based on the representation of systems of generating recurring logical formulas by means of LNP offered by V. D. Malyugin, which allowed using the redundant modular code device to control the errors of the functioning of the PRS generation nodes and, ensure the required safety of their functioning in the MIS.

To ensure the possibility of applying code control methods to generators of q -valued PRS, it is necessary to solve the problem of parallelizing the process of calculating them, while in [6] in general terms, approach for the synthesis of parallel generators of q -valued PRS on arithmetic polynomials is presented, the essence of which is the following.

Let $a_0, a_1, a_2, \dots, a_{m-1}, \dots$ — be the elements of the q -valued PRS satisfying the recurrence Eq. (3). Knowing that random element a_p ($p \geq m$) of the sequence $a_0, a_1, a_2, \dots, a_{m-1}, \dots$ is determined by the preceding m elements, let us present the elements $a_{p+m}, a_{p+m+1}, \dots, a_{p+2m-1}$ of the section of the q -

with a system of arithmetic polynomials of the form (7), we obtain:

$$\begin{cases} L_1(a_p, a_{p+1}, \dots, a_{p+m-1}) = \sum_{i=0}^{q^{m-1}-1} l_{1,i} a_p^{i_0} a_{p+1}^{i_1} \cdots a_{p+m-1}^{i_{m-1}}, \\ L_2(a_p, a_{p+1}, \dots, a_{p+m-1}) = \sum_{i=0}^{q^{m-1}-1} l_{2,i} a_p^{i_0} a_{p+1}^{i_1} \cdots a_{p+m-1}^{i_{m-1}}, \\ \vdots \\ L_m(a_p, a_{p+1}, \dots, a_{p+m-1}) = \sum_{i=0}^{q^{m-1}-1} l_{m,i} a_p^{i_0} a_{p+1}^{i_1} \cdots a_{p+m-1}^{i_{m-1}}. \end{cases} \quad (8)$$

Let us multiply the polynomials of the system (8) by weights q^{e-1} ($e = 1, 2, \dots, m$):

$$\left\{ \begin{aligned} L_1^*(a_p, a_{p+1}, \dots, a_{p+m-1}) &= q^0 L_1(a_p, a_{p+1}, \dots, a_{p+m-1}) \\ &= \sum_{i=0}^{q^{m-1}-1} l_{1,i}^* a_p^{i_0} a_{p+1}^{i_1} \dots a_{p+m-1}^{i_{m-1}}, \\ L_2^*(a_p, a_{p+1}, \dots, a_{p+m-1}) &= q^1 L_2(a_p, a_{p+1}, \dots, a_{p+m-1}) \\ &= \sum_{i=0}^{q^{m-1}-1} l_{2,i}^* a_p^{i_0} a_{p+1}^{i_1} \dots a_{p+m-1}^{i_{m-1}}, \\ &\dots\dots\dots \\ L_m^*(a_p, a_{p+1}, \dots, a_{p+m-1}) &= q^{m-1} L_m(a_p, a_{p+1}, \dots, a_{p+m-1}) \\ &= \sum_{i=0}^{q^{m-1}-1} l_{m,i}^* a_p^{i_0} a_{p+1}^{i_1} \dots a_{p+m-1}^{i_{m-1}}, \end{aligned} \right.$$

where $l_{e,i}^* = q^{e-1}l_{e,i}$ ($e = 1, 2, \dots, m$; $i = 0, \dots, q^m - 1$).

Then we get:

$$L(a_p, a_{p+1}, \dots, a_{p+m-1}) = \sum_{i=0}^{q^{m-1}-1} \sum_{e=1}^d J_{e,i}^* a_p^{i_0} a_{p+1}^{i_1} \dots a_{p+m-1}^{i_{m-1}} \quad (9)$$

or using the provisions of [18]:

$$D(a_p, a_{p+1}, \dots, a_{p+m-1}) = \left| \bigoplus_{i=0}^{q^{m-1}-1} v_i a_p^{i_0} a_{p+1}^{i_1} \dots a_{p+m-1}^{i_{m-1}} \right|_{q^m}, \quad (10)$$

where

$$v_i = \bigoplus_{e=1}^m l_{e,i}^* \quad (i = 0, 1, \dots, q^{m-1} - 1).$$

Let us calculate the values of the desired MFAL. For this, the result of the calculation (10) is presented in the q -scale of notation and we apply the camouflage operator $\Xi^w\{D(a_p, a_{p+1}, \dots, a_{p+m-1})\}$:

Substituting in (12) the values of the MA balances for the corresponding bases for each coefficient (9) and the values of the variables $a_p, a_{p+1}, \dots, a_{p+m-1}$, we obtain the values of the polynomials of the system (12), where $U^{(1)}, U^{(2)}, \dots, U^{(\eta)}, \dots, U^{(\psi)}$ — are nonnegative integrals. In accordance with the Chinese balances theorem, we solve the system of equations:

$$\begin{cases} U^* = |U^{(1)}|_{s_1}, \\ U^* = |U^{(2)}|_{s_2}, \\ \dots\dots\dots \\ U^* = |U^{(\eta)}|_{s_\eta}, \\ \dots\dots\dots \\ U^* = |U^{(\psi)}|_{s_\psi}. \end{cases} \quad (13)$$

Since $s_1, s_2, \dots, s_\eta, \dots, s_\psi$ are simple pairwise, the only solution (13) gives the expression:

$$U^* = \left| \sum_{d=1}^{\psi} S_{d,\psi} \mu_{d,\psi} U^{(d)} \right|_{S_\psi}, \quad (14)$$

where $S_{d,\psi} = \frac{S_\psi}{s_d}$, $\mu_{d,\psi} = |S_{d,\psi}^{-1}|_{s_d}$, $S_\psi = \prod_{d=1}^{\psi} s_d$.

The occurrence of the calculation result (14) in the range (test expression)

$$0 \leq U^* < S_\eta,$$

means no detectable calculation errors.

Otherwise, the procedure for restoring the reliable functioning of the q -valued PRS generator can be implemented according to known rules [19].

7 Conclusion

A secure parallel generator of q -valued PRS on arithmetic polynomials is presented. The implementation of generators of q -valued PRS using arithmetic polynomials and redundant MA codes makes it possible to obtain a new class of solutions aimed to safely implement logical cryptographic functions. At the same time, both functional monitoring of equipment (in real time, which is essential for MIS) and its fault tolerance is ensured due to the possible reconfiguration of the calculator structure in the process of its degradation. The classical q -LFSR, studied in this work, forms the basis of more complex q -valued PRS generators.

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