# Sized Types for low-level Quantum Metaprogramming

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Abstract. One of the most fundamental aspects of quantum circuit design is the concept of *families* of circuits parametrized by an instance size. As in classical programming, metaprogramming allows the programmer to write entire families of circuits simultaneously, an ability which is of particular importance in the context of quantum computing as algorithms frequently use arithmetic over non-standard word lengths. In this work, we introduce metaQASM, a typed extension of the openQASM language supporting the metaprogramming of circuit families. Our language and type system, built around a lightweight implementation of *sized types*, supports subtyping over register sizes and is moreover typesafe. In particular, we prove that our system is strongly normalizing, and as such any well-typed metaQASM program can be statically unrolled into a finite circuit.

**Keywords:** Quantum programming, Circuit description languages, Metaprogramming.

### 1 Introduction

Quantum computers have the potential to solve a number of important problems, including integer factorization [29], quantum simulation [23], approximating the Jones polynomial [1] and unstructured searching [12] asymptotically faster than the best known classical algorithms. These algorithms are typically described abstractly and make heavy use of classical arithmetic such as modular exponentiation. To make such algorithms concrete, efficient, reversible implementations of large swaths of a classical arithmetic and computation is needed – moreover, due to the limited space constraints and special-purpose nature of quantum circuits, these operations are typically needed in a multitude of bit sizes.

In part due to the increasing viability of quantum computing and the scaling of NISQ [28] devices, there has been a recent explosion in quantum programming tools. Such tools range from software development kits (e.g., Qiskit [5], ProjectQ [32], Strawberry Fields [19], Pyquil [30]) to Embedded domain-specific languages (e.g., Quipper [11], Qwire [27],  $Q|SI\rangle$  [22]) and standalone languages and compilers (e.g., QCL [26], QML [2], ScaffCC [16], Q# [33]). Going beyond strict programming tools, software for the synthesis, optimization, and simulation of quantum circuits and

programs (e.g., Revkit [31], TOpt [15], Feynman [3], PyZX [20], Quantum++ [9], QX [17]) are becoming more and more abundant.

The proliferation of both hardware and software tools for quantum computing has in turn spurred a need for standardization and portability [14,24]. One such standard which has recently grown in popularity is the Quantum Assembly Language and its many various dialects (e.g., openQASM [6], QASM-HL [16], cQASM [18]). As a lightweight, modular language for specifying simple quantum circuits, programs with a well-defined syntax, QASM support – in particular, for the openQASM dialect – has been built-in to an increasingly large number of software tools, particularly standalone programs like circuit optimizers, as a way to support interoperability.

One feature that is noticeably lacking in these dialects is the ability to define *families* of quantum circuits parametrized over different register sizes, and by extension to generate concrete instances. This creates a barrier for the use of QASM in writing portable libraries of quantum circuit families, particularly for classical operations such as arithmetic. As a result, software designers typically end up re-implementing code – typically implemented in the host language for EDSLs, and hence not easily re-usable – for generating instances of simple operations such as adders and multipliers. Alternatively, programmers resort to using other compilers such as Quipper, Q# or ReVerC [4] to generate individual instances, which complicates the compilation or simulation process. While recent progress towards the development of portable libraries of circuit families with high-level non-embedded languages, standardization remains an ongoing process, and moreover a low-level approach is preferable in many situations, including as compilation targets and middle-ends.

In this paper we make progress towards the design of a low-level language for quantum programming that supports the metaprogramming of sized circuit families. In particular, we develop a typed extension of the untyped open quantum assembly language (openQASM) with metaprogramming over lightweight *sized types* à la dependent ML [34]. Our language, metaQASM, is further shown to be type-safe and stronglynormalizing, while the non-meta fragment is both more expressive than openQASM and admits a simpler syntax, owing to the type system. For the purposes of this paper, we focus on the type system design and metatheory of such a language, leaving implementation to future work.

### 1.1 Quantum metaprogramming

Most QRAM-based quantum programming languages are metaprogramming languages – called *circuit description languages* – in that they typically operate by building quantum circuits to be sent in a single batch to a quantum processor. Such quantum circuits can typically be composed, reversed, and depend on the result of classical computations.

In this paper, we are interested in a particular type of quantum circuit metaprogramming, wherein circuit families are parametrized over *shapes* [11,27], such as the number of input qubits. Existing languages offer varying support for such metaprogramming, either implicitly (e.g., uniform or *transversal* families of circuits in openQASM, iteration and qubit arrays

in Q#), or more explicitly (e.g., the generic QData type-class in Quipper, which can be instantiated via explicit type applications). Our approach differs from previous attempts by explicitly parametrizing registers and circuit families with *size* parameters. We adopt a typed approach for a number of reasons:

- it allows the light-weight verification of libraries of circuit generators,

- it provides a means of self-documentation, and

- it allows explicit generation of sized-specialized instances.

The ability to generate instances of circuit families in various sizes *without executing them* is particularly important for the purposes of resource estimation, and for benchmarking tools that operate on fixed-size but arbitrary input circuits, such as circuit optimizers [14].

As an illustration, given an in-place family of adders written in the style of (imperative) Quipper with the type

inplace\_add :: [Qubit] -> [Qubit] -> Circ (),

one may wish to generate a static, optimized instance of inplace\_add operating on 2-qubit registers, using an external circuit optimizer. Doing so requires the specialization to (and serialization of) a function

inplace\_add2 :: (Qubit, Qubit) -> (Qubit, Qubit) -> Circ ().

One possible method of generating such a function is to write the body of inplace\_add2 using a call to the generic inplace\_add applied to the 4 input qubits. However, this quickly gets unwieldy, both in the boilerplate code defining a particular instance, and in the large number of parameters.

A more common solution is to use *dummy parameters*, whereby the generic function is "applied" to lists of qubits, which are then taken by the serialization method as meaning arbitrary inputs. For instance, the following Quipper<sup>1</sup> code [10] prints out a PDF representation of inplace\_add2 using dummy parameters qubit :: Qubit

```
print_generic PDF inplace_add [qubit, qubit] [qubit, qubit].
```

The use of dummy parameters is partly a question of style, though it can cause problems when combining optimizations with *initialized* dummy parameters. In either case, the use explicitly sized circuit families carries further benefits to both readability and correctness [27].

### 1.2 Organization

The remainder of this paper is organized as follows. Section 2 gives a brief overview of quantum computing. Section 3 reviews the openQASM language and defines a formal semantics for it. Sections 4 and 5 extend openQASM with types and metaprogramming capabilities, and finally Section 6 concludes the paper.

<sup>&</sup>lt;sup>1</sup> The function inplace\_add2 could instead be directly generated by writing the adder as inplace\_add :: QData qa => qa -> qa -> Circ (), then specializing qa to the finite type (Qubit, Qubit) using *type applications*. However, the non-generic serialization functions in Quipper appear to work only for small finite tuple types.

### 2 Quantum computing

We give a brief overview of the basics of quantum computing. For a more in-depth introduction of quantum computation we direct the reader to [25], while an overview of quantum programming can be found in [8].

In the circuit model, the state of an *n*-qubit quantum system is described as a unit vector in a dimension  $2^n$  complex vector space. The  $2^n$  elementary basis vectors form the *computational* basis, and are denoted by  $|\mathbf{x}\rangle$ for bit strings  $\mathbf{x} \in \{0, 1\}^n$  – these are called the *classical* states. A general quantum state may then be written as a *superposition* of classical states

$$|\psi\rangle = \sum_{\mathbf{x}\in\mathbb{F}_2^n} \alpha_{\mathbf{x}} |\mathbf{x}\rangle,$$

for complex  $\alpha_{\mathbf{x}}$  and having unit norm. The states of two n and m qubit quantum systems  $|\psi\rangle$  and  $|\psi\rangle$  may be combined into an n+m qubit state by taking their tensor product  $|\psi\rangle \otimes |\psi\rangle$ . If to the contrary the state of two qubits cannot be written as a tensor product the two qubits are said to be *entangled*.

Quantum circuits, in analogy to classical circuits, carry qubits from left to right along wires through gates which transform the state. In the unitary circuit model gates are required to implement unitary operators on the state space – that is, quantum gates are modelled by complexvalued matrices U satisfying  $UU^{\dagger} = U^{\dagger}U = I$ , where  $U^{\dagger}$  is the complex conjugate of U. As a result, unitary quantum computations must be reversible, and in particular the quantum circuits performing classical computations are precisely the set of reversible circuits.

The standard universal quantum gate set, known as Clifford+T, consists of the two-qubit controlled-NOT gate (CNOT), and the single-qubit Hadamard (H) and T gates. As quantum circuits implement linear operators, we may define the above three gates by their effect on classical states:

$$CNOT|x\rangle|y\rangle = |x\rangle|x \oplus y\rangle, \qquad T|x\rangle = e^{\frac{2\pi i}{8}x}|x\rangle,$$
$$H|x\rangle = \frac{1}{\sqrt{2}}\sum_{x'\in\{0,1\}} (-1)^{x\cdot x'}|x'\rangle.$$

Figure 1 gives a pictorial representation of a quantum circuit over CNOT, H, and T gates. CNOT gates are written as a solid dot on their first argument and an exclusive-OR symbol  $(\oplus)$  on their second argument. More general quantum operations include qubit initialization and measurement, which effectively convert between classical and quantum data. As neither operation is unitary and hence not (directly) reversible, we regard them as functions of the classical computer rather than gates in a quantum circuit.



Fig. 1. An example of a quantum circuit implementing the Toffoli gate.

### 3 openQASM

The open quantum assembly language (openQASM [6]) is a low-level, untyped imperative quantum programming language, developed as a dialect of the informal QASM language. One of the key additions of the openQASM language is that of *modularity*, in the form of a simple module and import system. As this work is largely concerned with the question of *making this modularity more powerful* – specifically, to support the modular definition of entire circuit families – we first give a brief overview of the openQASM language.

The official specification of openQASM can be found in [6]. Programs in openQASM are structured as sequences of declarations and commands. Programmers can declare statically-sized classical or quantum registers, define unitary circuits (called *gates* in openQASM), apply gates or circuits, measure or initialize qubits and condition commands on the value of classical bits. Gate arguments are restricted to individual qubits, where the application of gates to one or more register of the same size is syntactic sugar for the application of a single gate in parallel across the registers. The listing below gives an example of an openQASM program performing quantum teleportation:

```
OPENQASM 2.0;
qreg q[3];
creg c0[1];
creg c1[1];
h q[1];
cx q[1],q[2];
cx q[0],q[1];
h q[0];
measure q[0] -> c0[0];
measure q[1] -> c1[0];
if(c0==1) z q[2];
if(c1==1) x q[2];
```

We give a slightly different syntax from the above, and from the concrete syntax [6], as it will be more convenient and readable for our purposes. As is common in imperative languages, we leave some of the concrete syntactic classes of openQASM [6] separate in our formalization – since all operations in openQASM nominally have unit type, this allows terms with unitary and non-unitary *effects* to be distinguished, without relying on an effect system or monadic types. In particular, terms of the class U of unitary statements represent computations with purely unitary effects, while commands C may have non-unitary effects, such as measurement. Statements of the form

 $E(E_1,\ldots,E_n)$ 

represent the application of a unitary gate or named circuit E to the (quantum) arguments  $E_1$  through  $E_n$ . While the openQASM specification includes built-in **cx** (controlled-NOT) and parametrized single qubit

```
Identifier x

Index I ::= i \in \mathbb{N}

Expression E ::= x \mid x[I]

Unitary Stmt U ::= \operatorname{cx}(E_1, E_2) \mid \mathbf{h}(E) \mid \mathbf{t}(E) \mid \mathbf{tdg}(E) \mid E(E_1, \dots, E_n) \mid U_1; U_2

Command C ::= \operatorname{creg} x[I] \mid \operatorname{qreg} x[I] \mid \operatorname{gate} x(x_1, \dots, x_n) \{ U \}

\mid \operatorname{measure} E_1 \rightarrow E_2 \mid \operatorname{reset} E \mid U

\mid \operatorname{if}(E==I) \{ U \} \mid C_1; C_2
```

```
Location l \in \mathbb{N}
Value V ::= (l_0, \dots, l_{I-1}) \mid \lambda x_1, \dots, x_n. U
```

Fig. 2. openQASM (abstract) syntax

gates U, we drop the parametrized U gate in favour of built-in Hadamard and  $T/T^{\dagger}$  gates h and t/tdg, respectively.

The commands **creg**, **qreg** and **gate** declare classical registers, quantum registers, and unitary circuits, respectively. The **if** statement differs from the formal openQASM definition by testing the value of a *single* classical bit, rather than a classical register – this was done to simplify the semantics of the language. Locations l and values V do not appear directly in openQASM programs, but are used to define the semantics. In particular, values of the form  $(l_0, \ldots, l_{I-1})$  denote registers and  $\lambda x_1, \ldots, x_n.U$  denote unitary circuits. We leave out a number of features of openQASM which are orthogonal to the extensions we describe here, namely classical arithmetic and the **barrier** and **opaque** terms. We also write parentheses around arguments and parameters.

As no formal semantics of openQASM is given in [6], we define an operational semantics in Figure 3. Our semantics is defined with respect to a *configuration*  $\langle S, \sigma, \eta, |\psi\rangle\rangle$ , which stores a term S taken from some syntactic class (e.g., C, U, E), an environment  $\sigma$  which maps variables to values, a classical heap  $\eta$  storing the value of the classical bits, and a quantum state  $|\psi\rangle$ . Gates applied to qubit l of a quantum state are written by added a subscript to the intended gate, e.g.,

$$H_l|\psi\rangle = (I^{\otimes l-1} \otimes H \otimes I^{\otimes n-l})|\psi\rangle$$

 $\sigma[x \leftarrow v]$  denotes the environment mapping x to v or  $\sigma(x)$  otherwise, and  $S\{X/x\}$  denotes the substitution of X for x in S. We assume for convenience that no valid program will run out of classical memory or quantum bits. We say  $\langle S, \sigma, \eta, |\psi\rangle \rangle \Downarrow v$  if S reduces to v, where the form of v depends on the syntactic class of S – for instance, expressions evaluate to locations, arrays or circuits while commands produce a new environment, heap and quantum state. Note that we use a call-by-name evaluation strategy, as openQASM has only globally scoped variables. Rather than give a full probabilistic reduction system to account for measurement probabilities, it suffices for our purposes to make the semantics non deterministic. In particular, rules are given for both of the possible

non-deterministic. In particular, rules are given for both of the possible measurement outcomes in measure  $E_1 \rightarrow E_2$ , setting the classical bit to the result  $c \in \{0, 1\}$  and non-destructively applying the projector  $P^c = |c\rangle \langle c|$  (appropriately normalized) to the measured qubit.

Expressions:

$$\frac{x \in \mathsf{dom}(\sigma)}{\langle x, \sigma, \eta, |\psi\rangle \rangle \Downarrow \sigma(x)} \qquad \frac{\langle x, \sigma, \eta, |\psi\rangle \rangle \Downarrow (l_0, \dots, l_{I'}) \quad I \leq I'}{\langle x[I], \sigma, \eta, |\psi\rangle \rangle \Downarrow l_I}$$

Unitary statements:

Commands:

$$\begin{array}{c} \begin{array}{c} \begin{array}{c} l_{0},\ldots,l_{I-1} \text{ are fresh heap indices} \\ \hline \langle \operatorname{creg} x[I],\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma[x \leftarrow (l_{0},\ldots,l_{I-1})],\eta,|\psi\rangle\rangle \\ \hline l_{0},\ldots,l_{I-1} \text{ are fresh qubit indices} \\ \hline \langle \operatorname{qreg} x[I],\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma[x \leftarrow (l_{0},\ldots,l_{I-1})],\eta,|\psi\rangle\rangle \\ \hline \langle \operatorname{gate} x(x_{1},\ldots,x_{n}) \left\{ \begin{array}{c} U \right\},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma[x \leftarrow \lambda x_{1},\ldots,x_{n}.U],\eta,|\psi\rangle\rangle \\ \hline \langle \operatorname{cessure} E_{1},\sigma,\eta,|\psi\rangle\rangle \Downarrow l_{1} \quad \langle E_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow l_{2} \\ \hline \langle \operatorname{measure} E_{1} \rightarrow E_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta[l_{2} \leftarrow 0],P_{l_{1}}^{0}|\psi\rangle\rangle \\ \hline \langle \operatorname{cessure} E_{1} \rightarrow E_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta[l_{2} \leftarrow 1],P_{l_{1}}^{1}|\psi\rangle\rangle \\ \hline \langle \operatorname{cessure} E_{1} \rightarrow E_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta,|l_{2} \leftarrow 1],P_{l_{1}}^{1}|\psi\rangle\rangle \\ \hline \langle \operatorname{cessure} E_{1} \rightarrow E_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta,|l_{2} \leftarrow 1],P_{l_{1}}^{1}|\psi\rangle\rangle \\ \hline \langle \operatorname{cessure} E_{1} \rightarrow E_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta,|\psi\rangle\rangle \Downarrow l \quad \eta(l) \neq I \\ \hline \langle \operatorname{tesset} E,\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta,P_{l}^{0}|\psi\rangle\rangle \\ \hline \langle \operatorname{tesset} E,\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta,P_{l}^{0}|\psi\rangle\rangle \\ \hline \langle \operatorname{tf}(E==I) \left\{ \begin{array}{c} U \right\},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma,\eta,|\psi\rangle\rangle \\ \hline \langle \operatorname{tf}(E==I) \left\{ \begin{array}{c} U \right\},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \sigma'',\eta'',|\psi''\rangle\rangle \\ \hline \langle \operatorname{c1}; \ C_{2},\sigma,\eta,|\psi\rangle\rangle \Downarrow \langle \operatorname{c1}; \ C_{2},\sigma$$

Fig. 3. openQASM semantics

 $\begin{array}{l} \text{Base types } \beta ::= \texttt{Bit} \mid \texttt{Qbit} \\ \text{Types } \tau ::= \beta \mid \beta[I] \mid \texttt{Circuit}(\tau_1, \dots, \tau_n) \\ \text{Command } C ::= \dots \mid \texttt{creg } x[I] \texttt{ in } \{ \ C \ \} \mid \texttt{qreg } x[I] \texttt{ in } \{ \ C \ \} \\ \mid \texttt{gate } x(x_1 : \tau_1, \dots, x_n : \tau_n) \ \{ \ U \ \} \texttt{ in } \{ \ C \ \} \end{array}$ 

Fig. 4. typedQASM specification

# 4 Adding types to QASM

Run-time errors may occur in syntactically valid openQASM programs in a number of ways – particularly when either an array access is out of bounds and the program halts, or a classical (resp. quantum) location is used in a context when a quantum (resp. classical) location is expected. In the official openQASM specification, the latter error is eliminated by the requirement that only (global) variables can be declared as quantum registers may be used as arguments to gates, for instance. In either case however, it is desirable to check that an openQASM program *will not* go wrong, as circuit simulations are frequently run on large, expensive supercomputers (e.g., [13]).

In this section we developed a typed variant of openQASM, called typedQASM, which provably rules out such runtime errors. Moreover, the type system uses *sized types* to eliminate out-of-bound accesses, which we later develop into the core of our metaprogramming type system. The use of a type system in this case actually allows *more* valid programs to be written than the standard openQASM specification, as the type system allows us to remove some syntactic distinctions and instead make them in the type system. In particular, our type system allows registers and circuits to be passed as functions to other circuits, whereas the formal specification restricts circuit arguments to only individual qubits. Figure 4 gives the syntax of typedQASM. We only show the syntactic elements which are different from openQASM or otherwise new. To simplify our analysis, declarations are given explicit block scope, though we leave textual examples in the regular openQASM style of declaration. As the semantics of typedQASM is effectively identical, modulo the block scoping, to openQASM we don't explicitly give the semantics.

#### 4.1 The type system

Figure 5 gives the rules of our type system. As is standard, the judgement  $\Gamma \vdash S : \tau$  states that in the context  $\Gamma$  consisting of pairs of identifiers and types, S can be assigned type  $\tau$ . We overload  $\vdash$  to allow environment judgements of the form  $\vdash \sigma : \Gamma$  stating that the  $\sigma$  maps identifiers x to values of the type  $\tau$  if  $x : \tau \in \Gamma$ .

The type system of typedQASM is mostly as expected, with the exception of static-length registers and register bounds checks in the typing rules for dereferences. To give the programmer flexibility to apply gates and circuits to just parts of a larger register – for instance, when performing an *n*-bit addition into a length 2n register as in binary multiplication

Environment:

$$\begin{array}{c} & \vdash \sigma : \varGamma \\ \hline \vdash \cdot : \cdot & \vdash \sigma[x \leftarrow (l_0, \dots, l_{I-1})] : \varGamma, x : \beta[I] \\ \hline \vdash \sigma : \varGamma & \varGamma, x_1 : \tau_1, \dots, x_n : \tau_n \vdash U : \texttt{Unit} \\ \hline \vdash \sigma[x \leftarrow \lambda x_1 : \tau_1, \dots, x_n : \tau_n.U] : \varGamma, x : \texttt{Circuit}(\tau_1, \dots, \tau_n) \end{array}$$

Expressions:

$$\begin{array}{c|c} \underline{x:\tau\in\Gamma} \\ \hline \Gamma\vdash x:\tau \end{array} \quad \begin{array}{c} \Gamma\vdash x:\beta[I'] & I\leq I'-1 \\ \hline \Gamma\vdash x[I]:\beta \end{array} \quad \begin{array}{c} \Gamma\vdash E:\beta[I'] & I\leq I' \\ \hline \Gamma\vdash E:\beta[I] \end{array}$$

Unitary statements:

$$\begin{array}{c|c} \hline \Gamma \vdash E_1: \texttt{Qbit} & \Gamma \vdash E_2: \texttt{Qbit} \\ \hline \Gamma \vdash \texttt{cx}(E_1, E_2): \texttt{Unit} \\ \hline \Gamma \vdash E: \texttt{Circuit}(\tau_1, \dots, \tau_n) \\ \hline \Gamma \vdash E_1: \tau_1 & \cdots & \Gamma \vdash E_n: \tau_n \\ \hline \Gamma \vdash E(E_1, \dots, E_n): \texttt{Unit} \\ \end{array} \begin{array}{c} \hline \Gamma \vdash U_1: \texttt{Unit} & \Gamma \vdash U_2: \texttt{Unit} \\ \hline \Gamma \vdash U_1; & U_2: \texttt{Unit} \\ \hline \end{array}$$

Commands:

$arGamma,x: \mathtt{Bit}[I] dash C: \mathtt{Unit}$	arGamma,x: Qbit[I] dash C: Unit
$\Gamma \vdash \texttt{creg} \ x[I] \ \texttt{in} \ \{ \ C \ \} : \texttt{Unit}$	$\Gamma \vdash \texttt{qreg} \ x[I] \ \texttt{in} \ \{ \ C \ \} : \texttt{Unit}$
$\Gamma, x_1: \tau_1, \ldots, x_n: \tau_n \vdash U: \texttt{Unit}  \Gamma, x: \texttt{Circuit}(\tau_1, \ldots, \tau_n) \vdash C: \texttt{Unit}$	
$\varGamma \vdash  extsf{gate} \hspace{0.1 cm} x(x_{1}: au_{1}, \dots, x_{n}: au_{n}) \hspace{0.1 cm} \Set{U} \hspace{0.1 cm}  extsf{in} \hspace{0.1 cm} \Set{C} \hspace{0.1 cm} : extsf{Unit}$	
$\Gamma \vdash E_1: \texttt{Qbit}$ $\Gamma \vdash E_2: \texttt{E}$	Bit $\Gamma \vdash E : \texttt{Qbit}$
$\Gamma \vdash \texttt{measure} \ E_1  extsf{->} \ E_2 : \texttt{Unit} \qquad \Gamma \vdash \texttt{reset} \ E : \texttt{Unit}$	
$\Gamma \vdash E : \texttt{Bit} \qquad \Gamma \vdash U : \texttt{Unit}$	$\Gamma \vdash C_1: \texttt{Unit} \qquad \Gamma \vdash C_2: \texttt{Unit}$
$\Gamma \vdash if(E == I) \{ U \} : Unit$	$\varGamma dash C_1$ ; $\overline{C}_2:$ Unit

Fig. 5. typedQASM typing rules

– the type system also implicitly supports subtyping of static length registers. Specifically, any length I array can be used in a context requiring *at most I* cells. While this adds a great deal of flexibility on the side of the programmer, as a downside typedQASM typing derivations are not unique.

As an example of a well-typed QASM program, we show an implementation of the Toffoli circuit from Figure 1 below:

```
gate toffoli(x:Qbit, y:Qbit, z:Qbit) {
    h(z);
    t(x); t(y); t(z);
    cx(x,y); cx(x,z);
    tdg(y); tdg(z);
    cx(y,z); cx(z,x);
    t(x); tdg(z);
    cx(z,x); cx(x,y); cx(y,z);
    h(z)
}
```

#### 4.2 Type safety

We now briefly sketch a proof of type safety for typedQASM. In particular, we show that typedQASM is strongly normalizing, as expected. As is standard, we establish strong normalization by giving type preservation and progress lemmas. While type preservation is effectively implicit in the semantics of typedQASM due to the different syntactic classes, expressions may return different types of values and so we give a form of type preservation for such terms.

**Lemma 1 (Preservation (expressions)).** If  $\Gamma \vdash E : \tau, \vdash \sigma : \Gamma$  and  $\langle E, \sigma, \eta, |\psi\rangle \rangle \Downarrow v$ , then either

- $-\tau = \beta$  and v = l for some base type  $\beta$  & location l,  $-\tau = \beta[I]$  and  $v = (l_0, \dots, l_{I'})$  where  $I' \ge I$ , or
- $-\tau = \text{Circuit}(\tau_1, \ldots, \tau_n) \text{ and } v = \lambda x_1 : \tau_1, \ldots, x_n : \tau_n.U.$

*Proof.* If  $\tau = \beta$  then we must have E = x[I], hence by the definition of  $\Downarrow$ , v = l. Likewise if  $\tau = \beta[I]$  then we must have E = x where  $x : \beta[I'] \in \Gamma$  for some  $I' \geq I$ , and since  $\vdash \sigma : \Gamma$  then  $v = \sigma(x) = (l_0, \ldots, l_{I'})$ . The case for  $\tau = \text{Circuit}(\tau_1, \ldots, \tau_n)$  is similar.

The following lemmas give progress properties – the fact that for a well-typed program, evaluation can always continue – for the different syntactic classes of typedQASM. Together with type preservation, the result is that any well-typed typedQASM program evaluates to a value, i.e. that typedQASM is strongly normalizing.

**Lemma 2 (Progress (expressions)).** If  $\Gamma \vdash E : \tau$  and  $\vdash \sigma : \Gamma$ , then for any  $\eta, |\psi\rangle, \langle E, \sigma, \eta, |\psi\rangle \downarrow \psi$ .

*Proof.* By case analysis on *E*. If E = x the proof is trivial, as  $x : \tau \in \Gamma$  by inversion and  $\vdash \sigma : \Gamma$  implies  $x \in \mathsf{dom}(x)$ . If on the other hand E = x[I], we must have  $x : \beta[I'] \in \Gamma$  for some I' > I. Then by preservation,  $\langle x, \sigma, \eta, |\psi \rangle \rangle \Downarrow (l_0, \ldots, l_{I''})$  for some  $I'' \ge I' - 1$ , hence  $\langle x, \sigma, \eta, |\psi \rangle \rangle \Downarrow l_I$ 

**Lemma 3 (Progress (unitary stmts)).** If  $\Gamma \vdash U$ : Unit and  $\vdash \sigma : \Gamma$ , then for any  $\eta, |\psi\rangle, \langle U, \sigma, \eta, |\psi\rangle \rangle \Downarrow |\psi'\rangle$ .

*Proof.* For the case  $U = E(E_1, \ldots, E_n)$ , by the typing derivation we have  $\Gamma \vdash E$ : Circuit $(\tau_1, \ldots, \tau_n)$  so by progress and preservation for expressions,  $\langle E, \sigma, \eta, |\psi\rangle \rangle \Downarrow \lambda x_1 : \tau_1, \ldots, x_n : \tau_n.U$ . By the substitution lemma below,  $\Gamma \vdash U\{E_1/x_1, \ldots, E_n/x_n\}$ : Unit and hence we can structural induction to show that  $\langle U, \sigma, \eta, |\psi\rangle \rangle \Downarrow |\psi'\rangle$ .

**Lemma 4 (Substitution).** If  $\Gamma, x_1 : \tau_1, \ldots, x_n : \tau_n \vdash U$ : Unit, and  $\Gamma \vdash E_i : \tau_i$  for each  $1 \le i \le n$  then  $\Gamma \vdash U\{E_1/x_1, \ldots, E_n/x_n\}$ : Unit

Lemma 5 (Progress (commands)). If  $\Gamma \vdash C$ : Unit and  $\vdash \sigma : \Gamma$ , then for any  $\eta, |\psi\rangle, \langle C, \sigma, \eta, |\psi\rangle \rangle \Downarrow \langle \sigma', \eta', |\psi'\rangle \rangle$ .

*Proof.* Proof by induction on the structure of C. We show one case:

$$C = \texttt{gate } x(x_1 : \tau_1, \dots, x_n : \tau_n) \{ U \} \texttt{ in } \{ C \}$$

We know that

$$\langle \texttt{gate } x(x_1:\tau_1,\ldots,x_n:\tau_n) \ \{ \ U \ \}, \sigma,\eta, |\psi\rangle\rangle \Downarrow \langle \sigma[x \leftarrow \lambda x_1,\ldots,x_n.U],\eta, |\psi\rangle\rangle.$$

By the typing derivation,  $\Gamma, x_1 : \tau_1, \ldots, x_n : \tau_n \vdash U$ : Unit and  $\Gamma, x$ : Circuit $(\tau_1, \ldots, \tau_n) \vdash C$ : Unit. It then follows that

$$\vdash \sigma[x \leftarrow \lambda x_1 : \tau_1, \dots, x_n : \tau_n.U] : \Gamma, x : \texttt{Circuit}(\tau_1, \dots, \tau_n),$$

and hence we can apply the inductive hypothesis to complete the case. The remaining cases are similar.

**Theorem 1** (Strong normalization). If  $\vdash C$ : Unit, then

 $\langle C, \emptyset, \lambda l.0, |00 \cdots \rangle \rangle \Downarrow \langle \sigma, \eta, |\psi \rangle \rangle.$ 

Proof. Direct consequence of Lemma 5.

### 5 MetaQASM

Now that we have a safe, array-bounds-checked, typed language, we can add metaprogramming features. In particular, we wish to  $support^2$ 

- circuit inversion/reversal, and
- circuits parametrized by sizes.

While the latter could be accomplished in an ad-hoc way, allowing *type-level* integers allows for more safety in that array bounds can be statically checked, and increases the readability of programs. Moreover, it enforces a clear separation between circuits and families of circuits, which naturally support different operations – for instance, a family of circuits can't easily be visualized diagrammatically, while a particular instance can [27].

Figure 6 gives the new syntax for metaQASM. Indices I are extended with index variables y and integer arithmetic, and a new syntactic form defining a family of quantum circuits parametrized over index variables is given. The index  $\infty$  only exists in the process of type checking and is not valid syntax in source code. Intuitively, the declaration

family
$$(y_1, \ldots, y_m) x(x_1 : \tau_1, \ldots, x_n : \tau_n) \{ U \}$$
 in  $\{ C \}$ 

introduces index variables  $y_1, \ldots, y_m$  into the evaluation and type checking contexts for  $\tau_i$  and U.

<sup>&</sup>lt;sup>2</sup> Controlled circuits are another desirable metaprogramming feature found in many quantum circuit description languages. While metaQASM gates are in fact closed over qubit controls, they require *ancillae* to construct [21]. This complicates the inclusion of a control instruction in metaQASM, and further abstracts away from concrete, resource-driven nature of QASM.

```
\begin{array}{l} \text{Types } \tau ::= \dots \mid \texttt{Family}(y_1, \dots, y_m)(\tau_1, \dots, \tau_n) \\ \text{Index } I ::= \dots \mid y \mid \infty \mid I_1 + I_2 \mid I_1 - I_2 \mid I_1 \cdot I_2 \\ \text{Range } \iota ::= [I_1, I_2] \\ \text{Expression } E ::= \dots \mid \texttt{instance}(I_1, \dots, I_m) \mid E \\ \text{Unitary Stmt } U ::= \dots \mid \texttt{reverse } U \mid \texttt{for } y = I_1 . . I_2 \mid \texttt{do } \{ U \} \\ \text{Command } C ::= \dots \mid \texttt{family}(y_1, \dots, y_m) \mid x(x_1 : \tau_1, \dots, x_n : \tau_n) \mid \{ U \} \mid \texttt{in } \{ C \} \\ \text{Value } V ::= \dots \mid IIy_1, \dots, y_m . V \end{array}
```

#### Fig. 6. metaQASM syntax

Figure 7 gives the semantics of the new syntax. Since index variables cannot be modified or captured, we use a substitution style of evaluation for circuit families. The **reverse** command introduces a new reduction relation  $\langle U, \sigma, |\psi \rangle \rangle \uparrow v$  for which reduction of U is inverted. We give a concrete semantics rather than an abstract rule such as

$$\frac{\langle U, \sigma, \eta, |\psi'\rangle \downarrow |\psi\rangle}{\langle \text{reverse } U, \sigma, \eta, |\psi\rangle \rangle \downarrow |\psi'\rangle}$$

so that metaQASM has a concrete execution model. Inversion of circuits is straightforward in metaQASM, as in any closed context a unitary statement can be statically unrolled to a finite sequence of gates.

As an illustration of metaprogramming in metaQASM, Figure 8 gives metaQASM code for a simple (non-garbage-cleaning) adder. Our syntax (and type system) also allows an instance of a family of circuits to accept other circuit families as arguments, a useful feature which allows circuit families to be parametric in the implementation of a sub-routine as shown below (using a minor syntax extension to allow array slicing).

By extending our syntax with parametrized gates as in regular open-QASM, we can also define a parametrized family of circuits computing the *quantum Fourier transform* as in [27].

```
include "cphase.qasm";
family(n) qft(x:Qbit[n]) {
   for i=0..n-1 do {
        h(x[i]);
        for j=i+1..n-1 do {
            cphase(j-1+1)(x[i], x[j])
        }
    }
}
```

 $\label{eq:Indices:} \underbrace{\langle I_1, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_1 \quad \langle I_2, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_2 \quad \star \in \{+, -, \cdot\}}_{\langle I_1 \star I_2, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_1 \star i_2}$ 

Expressions:

$$\frac{\langle E, \sigma, \eta, |\psi\rangle \Downarrow \Pi y_1, \dots, y_m.\lambda x_1 : \tau_1, \dots, x_n : \tau_n.U}{\langle \texttt{instance}(I_1, \dots, I_m) \ E, \sigma, \eta, |\psi\rangle \Downarrow (\lambda x_1 : \tau_1, \dots, x_n : \tau_n.U) \{I_1/y_1, \dots, I_m/y_m\}}$$

Unitary statements:

$$\begin{array}{c|c} \langle U, \sigma, \eta, |\psi\rangle\rangle \Uparrow |\psi'\rangle & \langle I_1, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_1 \quad \langle I_2, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_2 \quad i_1 > i_2 \\ \hline \langle \text{reverse } U, \sigma, \eta, |\psi\rangle\rangle \Downarrow |\psi'\rangle & \langle \text{for } y = I_1..I_2 \text{ do } \{ U \}, \sigma, \eta, |\psi\rangle\rangle \Downarrow |\psi\rangle \\ & \langle I_1, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_1 \quad \langle I_2, \sigma, \eta, |\psi\rangle\rangle \Downarrow i_2 \quad i_1 \leq i_2 \\ & \langle U\{i_1/y\}, \sigma, \eta, |\psi\rangle\rangle \Downarrow |\psi'\rangle \\ \hline \langle \text{for } y = i_1 + 1..i_2 \text{ do } \{ U \}, \sigma, \eta, |\psi'\rangle\rangle \Downarrow |\psi''\rangle \\ \hline \langle \text{for } y = I_1..I_2 \text{ do } \{ U \}, \sigma, \eta, |\psi\rangle\rangle \Downarrow |\psi''\rangle \end{array}$$

Reverse reduction:

$$\begin{array}{c|c} & \langle E,\sigma,\eta,|\psi\rangle\rangle \Downarrow l & \langle E,\sigma,\eta,|\psi\rangle\rangle \Downarrow l & \langle E,\sigma,\eta,|\psi\rangle\rangle \Downarrow l \\ \hline & \langle \mathbf{h}(E),\sigma,\eta,|\psi\rangle\rangle \Uparrow H_l|\psi\rangle & \langle \mathbf{L}(E),\sigma,\eta,|\psi\rangle\rangle \Uparrow T_l^{\dagger}|\psi\rangle & \langle \mathbf{L}g(E),\sigma,\eta,|\psi\rangle\rangle \Uparrow T_l|\psi\rangle \\ \hline & \langle \mathbf{h}(E),\sigma,\eta,|\psi\rangle\rangle \Downarrow h_l^{\phantom{\dagger}} \langle \mathbf{L}_2,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_2 & \langle E,\sigma,\eta,|\psi\rangle\rangle \Downarrow \lambda x_1,\ldots,x_n.U, \\ \hline & \langle E_1,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_1^{\phantom{\dagger}} \langle E_2,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_2 & \langle U\{E_1/x_1,\ldots,E_n/x_n\},\sigma,\eta,|\psi\rangle\rangle \Uparrow |\psi'\rangle \\ \hline & \langle \mathbf{cx}(E_1,E_2),\sigma,\eta,|\psi\rangle\rangle \Uparrow \nabla \nabla \nabla I_{l_1,l_2}|\psi\rangle & \langle U_1,\sigma,\eta,|\psi'\rangle\rangle \Uparrow |\psi'\rangle \\ \hline & \langle U_2,\sigma,\eta,|\psi\rangle\rangle \Uparrow |\psi'\rangle & \langle U_1,\sigma,\eta,|\psi'\rangle \land |\psi''\rangle \\ \hline & \langle U_1,U_2,\sigma,\eta,|\psi\rangle\rangle \Uparrow |\psi'\rangle & \langle U_1,\sigma,\eta,|\psi'\rangle \land |\psi''\rangle \\ \hline & \langle U,\sigma,\eta,|\psi\rangle\rangle \Downarrow |\psi'\rangle & \langle I_1,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_1^{\phantom{\dagger}} \langle I_2,\sigma,\eta,|\psi\rangle \Downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \langle I_1,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \langle I_1,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \langle I_1,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \langle I_1,\sigma,\eta,|\psi\rangle\rangle \downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \langle I_1,\sigma,\eta,|\psi\rangle\rangle \Downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \downarrow l_2^{\phantom{\dagger}} \langle I_1,\sigma,\eta,|\psi\rangle\rangle \downarrow l_2^{\phantom{\dagger}} \downarrow l_2^$$

Commands:

$$\frac{\langle C, \sigma[x \leftarrow \Pi y_1, \dots, y_m.\lambda x_1 : \tau_1, \dots, x_n : \tau_n.U], \eta, |\psi\rangle\rangle \Downarrow \langle \sigma', \eta', |\psi'\rangle\rangle}{\langle \mathsf{family}(y_1, \dots, y_m) \ x(x_1 : \tau_1, \dots, x_n : \tau_n) \ \{ \ U \ \} \ \mathsf{in} \ \{ \ C \ \}, \sigma, \eta, |\psi\rangle\rangle \Downarrow \langle \sigma, \eta', |\psi'\rangle\rangle}$$

 ${\bf Fig.~7.}\ {\rm metaQASM}\ {\rm semantics}$ 

```
Matthew Amy
```

```
include "toffoli.qasm";
gate maj(a:Qbit, b:Qbit, c:Qbit, res:Qbit) {
    toffoli(b, c, res);
    cx(b, c);
    toffoli(a, c, res);
    cx(b, c)
}
family(n) add(a:Qbit[n], b:Qbit[n], c:Qbit[n], anc:Qbit[n]) {
    cx(a[0], c[0]);
    cx(b[0], c[0]);
    toffoli(a[0], b[0], anc[0]);
    for i=1..n-1 do {
        cx(a[i], c[i]);
        cx(b[i], c[i]);
        cx(anc[i-1], c[i]);
        maj(a[i], b[i], anc[i-1], anc[i])
    }
}
```

Fig. 8. metaQASM implementation of a carry-ripple adder.

#### 5.1 Type system

The type system of metaQASM is inspired by Dependent ML [34]. Figure 9 gives the rules of our system. Type rules are defined over two contexts  $\Delta$ ;  $\Gamma$ , where  $\Delta$  contains interval constraints on index variables. As with typedQASM, array bounds are checked and subtyping on array lengths is allowed. Integer expressions are assigned intervals which may be arbitrary (well-formed) integer expressions. The judgement  $\Delta \models P$ which appears in the typing rules for integer expressions denotes that under the context  $\Delta$ , the (in)equality P holds. We leave a particular constraint solver up to implementation. It remains an open question whether undecidable constraints can be generated by our type system, though in practice it appears most common constraints can be efficiently solved with off-the-shelf constraint solvers [34].

The type system of Figure 9 also involves kind judgements of the form

 $\varDelta \vdash \tau :: \ast$ 

stating that  $\tau$  is a simple type in the index context  $\Delta$ . While the rules of our kind system are not given here, it is straightforward to derive. In particular,  $\tau$  has kind \* if  $\tau$  does not reference any free index variables, and does not contain any registers of negative length.

Remark 1. The fact that metaQASM has no means of specifying and checking relational properties on indices causes some programs to require counter-intuitive type schemes. For instance, the following *n*-bit adder is not well-typed due to the statement toffoli(x[n-2], ctrl, y[n-1]), though it does not cause run-time errors when  $n \geq 2$ .

Indices:

Expressions:

$$\begin{array}{c|c} \underline{\Delta; \Gamma \vdash x : \beta[I']} & \underline{\Delta} \models 0 \leq I < I' \\ \hline \underline{\Delta; \Gamma \vdash x[I] : \beta} \\ \underline{\Delta; \Gamma \vdash E : \mathtt{Family}(y_1, \dots, y_m)(\tau_1, \dots, \tau_n)} \\ \underline{\Delta \vdash I_1 : [0, \infty]} & \cdots & \underline{\Delta \vdash I_m : [0, \infty]} \end{array}$$

 $\Delta; \Gamma \vdash \texttt{instance}(I_1, \dots, I_m) \quad E: \texttt{Circuit}(\tau_1\{I_1/y_1, \dots, I_m/y_m\}, \dots, \tau_n\{I_1/y_1, \dots, I_m/y_m\})$ 

Unitary statements:

$$\begin{array}{c} \varDelta \vdash I : [I_1, I_2] \qquad \varDelta \vdash I' : [I_1', I_2'] \\ \hline \varDelta; \varGamma \vdash \texttt{reverse} \ U : \texttt{Unit} \qquad \hline \varDelta; \varGamma \vdash \texttt{for} \ y = I..I' \ \texttt{do} \ \{ \ U \ \} : \texttt{Unit} \end{array}$$

Commands:

$$\begin{array}{c} \Delta, y_1: [0,\infty], \ldots, y_m: [0,\infty] \vdash \tau_1 :: \ast & \cdots & \Delta, y_1: [0,\infty], \ldots, y_m: [0,\infty] \vdash \tau_n :: \ast \\ \Delta, y_1: [0,\infty], \ldots, y_m: [0,\infty]; \varGamma, x_1: \tau_1, \ldots, x_n: \tau_n \vdash U: \texttt{Unit}, \\ \underline{\Delta; \varGamma, x: \texttt{Family}(y_1, \ldots, y_m)(\tau_1, \ldots, \tau_n) \vdash C: \texttt{Unit}} \\ \hline \Delta; \varGamma \vdash \texttt{family}(y_1, \ldots, y_m) \ x(x_1: \tau_1, \ldots, x_n: \tau_n) \ \left\{ \begin{array}{c} U \end{array} \right\} \text{ in } \left\{ \begin{array}{c} C \end{array} \right\}: \texttt{Unit} \end{array}$$

 ${\bf Fig.~9.}\ {\rm metaQASM}\ {\rm typing\ rules}$ 

```
include "toffoli.qasm";
family(n) ctrlAdd(ctrl:Qbit, x:Qbit[n],
                  y:Qbit[n], c:Qbit) {
    toffoli(x[0], ctrl, y[0]);
    cx(x[0], c);
    toffoli(c, y[0], x[0]);
    for i=1...n-2 do {
        toffoli(x[i], ctrl, y[i]);
        cx(x[i-1], x[i]);
        toffoli(x[i-1], y[i], x[i])
    }
    toffoli(x[n-1], ctrl, y[n-1]);
    toffoli(x[n-2], ctrl, y[n-1]);
    for i=2..n-1 do {
        toffoli(x[n-i-1], y[n-i], x[n-i]);
        cx(x[n-i-1], x[n-i]);
        toffoli(x[n-i-1], ctrl, y[n-i])
    }
    toffoli(c, y[0], x[0]);
    cx(x[0], c);
    toffoli(c, ctrl, y[0])
}
```

The above adder can modified [7] to a well-typed program by using m = n-2 as the parameter, effectively specifying the number of entries greater than 2 that the input registers contain. The program snippet below gives the declaration required to make the controlled Adder implementation (with appropriate re-indexing) well-typed.

In most practical cases appropriate parameters can be given so as to allow a well-typed implementation of a circuit family. However, the family parameters can be counter-intuitive, and more egregiously it can be unclear as to how to generate an intended instance. We leave it as an avenue for future work to add specification and checking of bounds and relational properties to metaQASM.

#### 5.2 Type safety

As in the case of typedQASM, metaQASM is strongly normalizing, due to the lack of recursion and unbounded loops. Progress relies on the fact that during the course of evaluation, no free index variables are encountered – hence any term encountered by an interpreter is well-typed in the empty index context, and in particular indices can be evaluated to finite integers, as shown below.

**Lemma 6.** If  $\cdot \vdash I : [I_1, I_2]$ , then  $\langle I, \sigma, \eta, |\psi\rangle \rangle \Downarrow i$ .

*Proof.* Trivial since the judgement  $\cdot \vdash I : [I_1, I_2]$  requires that I does not contain any variables. Note also that there is no derivation of a judgement of the form  $\Delta \vdash \infty : [I_1, I_2]$  hence I cannot contain any infinite integers.

The remaining lemmas are extensions of results for typedQASM. Only the new or different cases are considered.

**Lemma 7 (Preservation (expressions)).** *If*  $\cdot$ ;  $\Gamma \vdash E : \tau$ ,  $\vdash \sigma : \Gamma$  *and*  $\langle E, \sigma, \eta, |\psi\rangle \rangle \Downarrow v$ , *then either* 

1.  $\tau = \beta$  and v = l, 2.  $\tau = \beta[I]$  and  $v = (l_0, \dots, l_{I'})$  where  $I' \ge I$ , or 3.  $\tau = \text{Circuit}(\tau_1, \dots, \tau_n)$  and  $v = \lambda x_1 : \tau_1, \dots, x_n : \tau_n.U$ 4.  $\tau = \text{Family}(y_1, \dots, y_m)(\tau_1, \dots, \tau_n)$  and

 $v = \Pi y_1, \dots, y_m \cdot \lambda x_1 : \tau_1, \dots, x_n : \tau_n \cdot U.$ 

*Proof.* The new Family case is effectively identical to the Circuit case. For the case where  $\tau = \beta$ , it suffices to note that by Lemma 6, the expressions I and I' in the typing derivation reduce to integers i, i' and the proof concludes as in the typedQASM case.

Finally we have to revise the  $\tau = \text{Circuit}(\tau_1, \ldots, \tau_n)$  case as we now have two possible derivations. The new case  $E = \text{instance}(I_1, \ldots, I_m)$  E is also trivial as the only reduction produces a value of the form  $\lambda x_1 :$  $\tau_1, \ldots, x_n : \tau_n.U$ . Note that the type  $\tau$  in the derivation has  $I_i$  substituted for index variables  $y_i$ , as in the conclusion of the reduction rule.

**Lemma 8 (Progress (expressions)).** If  $\cdot$ ;  $\Gamma \vdash E : \tau$  and  $\vdash \sigma : \Gamma$ , then for any  $\eta, |\psi\rangle, \langle E, \sigma, \eta, |\psi\rangle \rangle \Downarrow v$ .

*Proof.* Again, the new case  $E = \text{instance}(I_1, \ldots, I_m) E$  needs consideration. By inversion we see that E: Family $(y_1, \ldots, y_m)(\tau_1, \ldots, \tau_n)$ . By structural induction and the preservation lemma,  $\langle E', \sigma, \eta, |\psi\rangle \rangle \Downarrow \Pi y_1, \ldots, y_m \cdot \lambda x_1 : \tau_1, \ldots, x_n : \tau_n \cdot U$  and so  $\langle E, \sigma, \eta, |\psi\rangle \Downarrow \Downarrow v$ .

**Lemma 9 (Progress (unitary stmts)).** If  $\cdot$ ;  $\Gamma \vdash U$ : Unit and  $\vdash \sigma$ :  $\Gamma$ , then for any  $\eta$ ,  $|\psi\rangle$ ,  $\langle U, \sigma, \eta, |\psi\rangle \rangle \Downarrow |\psi'\rangle$ .

*Proof.* The case U =**reverse** U requires a separate progress lemma for reverse reduction, which follows similar to progress for unitary statements.

For the remaining case  $U = \text{for } y = I_1 .. I_2$  do  $\{U\}$ , it suffices to observe that by inversion,  $\cdot \vdash I_i : [I_i, I'_i]$  and so both bounds reduce to integers. As each recursive call increases the lower bound  $I_1$ , and  $I_2$  is necessarily finite, there can be no infinite chains of reductions. The only condition that needs checking is that  $\langle U\{i_1/y\}, \sigma, \eta, |\psi\rangle\rangle \Downarrow |\psi'\rangle$ , for which we need the following substitution lemma.

**Lemma 10.** If  $\Delta, y : [I_1, I'_2]; \Gamma \vdash U : Unit, and <math>\Delta \vdash i_1 : [I_1, I'_2]$  then  $\Delta; \Gamma \vdash U\{i_1/y\}: Unit$ 

To complete the proof, another lemma is needed stating that the result of evaluating an integer expression is within the bounds of the expression's type. We leave this as an easy exercise. Lemma 11 (Progress (commands)). If  $:; \Gamma \vdash C :$  Unit and  $:; \cdot \vdash \sigma :$  $\Gamma$ , then for any  $\eta, |\psi\rangle, \langle C, \sigma, \eta, |\psi\rangle \rangle \Downarrow \langle \sigma', \eta', |\psi'\rangle \rangle.$ 

*Proof.* We have one new command to check,

 $C = \texttt{family}(y_1, \dots, y_m) \ x(x_1 : \tau_1, \dots, x_n : \tau_n) \ \{ \ U \ \} \ \texttt{in} \ \{ \ C \ \}.$ 

The proof in this case is effectively identical to regular gate declaration.

**Theorem 2** (Strong normalization). If  $\cdot; \cdot \vdash C$ : Unit, then

 $\langle C, \emptyset, \lambda l.0, |00 \cdots \rangle \rangle \Downarrow \langle \sigma, \eta, |\psi \rangle \rangle.$ 

Proof. Follows directly from Lemma 11

# 6 Conclusion

We have described a typed extension to openQASM that supports static array bounds checking, higher-order circuits, and lightweight metaprogramming in the form of size-indexed families of circuits. The resulting language is powerful enough to use for writing libraries of general quantum circuit families, such as for reversible arithmetic, while low-level enough to be used wherever openQASM is used.

As this is preliminary work, much remains to be done to make metaQASM a practical language for quantum library development. In particular, a concrete implementation needs to be developed, as do more examples of practical circuit families. A major question which remains is whether a decision procedure for the simple, non-linear integer constraints generated by our type system exists.

Another interesting question for future work is whether *parametrized re*source counts for algorithms can be computed directly from metaQASM programs. In particular, a desirable feature would be to compute closedform formulas for the number of qubits, gates, etc., in an arbitrary instance of a circuit family, so that different implementations of the same circuit family can be analytically compared for any instance size. Doing so would help not only with resource estimation, but also compilation by allowing compilers to automatically select the best implementation for a particular cost model.

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