Batch-efficient EigenDecomposition for Small and Medium Matrices

Yue $\operatorname{Song}^{[0000-0003-1573-5643]}$, Nicu Sebe, and Wei Wang

DISI, University of Trento, Trento 38123, Italy yue.song@unitn.it
https://github.com/KingJamesSong/BatchED

Abstract. EigenDecomposition (ED) is at the heart of many computer vision algorithms and applications. One crucial bottleneck limiting its usage is the expensive computation cost, particularly for a mini-batch of matrices in the deep neural networks. In this paper, we propose a QR-based ED method dedicated to the application scenarios of computer vision. Our proposed method performs the ED entirely by batched matrix/vector multiplication, which processes all the matrices simultaneously and thus fully utilizes the power of GPUs. Our technique is based on the explicit QR iterations by Givens rotation with double Wilkinson shifts. With several acceleration techniques, the time complexity of QR iterations is reduced from $O(n^5)$ to $O(n^3)$. The numerical test shows that for small and medium batched matrices (e.g., dim<32) our method can be much faster than the Pytorch SVD function. Experimental results on visual recognition and image generation demonstrate that our methods also achieve competitive performances.

Keywords: Differentiable SVD, Global Covariance Pooling, Universal Style Transfer, Vision Transformer

1 Introduction

The EigenDecomposition (ED) or the Singular Value Decomposition (SVD) explicitly factorize a matrix into the eigenvalue and eigenvector matrix, which serves as a fundamental tool in computer vision and deep learning. Recently, many algorithms integrated the SVD as a meta-layer into their models to perform some desired spectral transformations [34, 33, 31, 5, 23, 9, 45, 24, 8, 13, 47, 46, 38]. The applications vary in global covariance pooling [31, 44, 38], decorrelated Batch Normalization (BN) [23, 45, 24, 39], Perspective-n-Points (PnP) problems [5, 8, 13], and Whitening and Coloring Transform (WCT) [32, 9, 47].

The problem setup of the ED in computer vision is quite different from other fields. In other communities such as scientific computing, batched matrices rarely arise and the ED is usually used to process a single matrix. However, in deep learning and computer vision, the model takes a mini-batch of matrices as the input, which raises the requirement for an ED solver that works for batched matrices efficiently. Moreover, the differentiable ED works as a building block

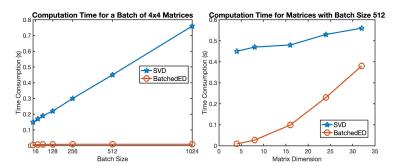


Fig. 1. The speed comparison of our Batched ED against the TORCH.SVD. (*Left*) Time consumption for a mini-batch of 4×4 matrices with different batch sizes. (*Right*) Time consumption for matrices with batch size 512 but in different matrix dimensions.

and needs to process batched matrices millions of times during the training and inference. This poses a great challenge to the efficiency of the ED solver and could even stop people from adding the ED meta-layer in their models due to the huge time consumption (see Fig. 1).

In the current deep learning frameworks such as Pytorch [37] or Tensorflow [1], the ED solvers mainly adopt the SVD implementation from the linear algebra libraries (e.g., LAPACK [3] and Intel MKL [43]). These solvers can efficiently process a single matrix but do not support batched matrices on GPUs well. Most of the implementations are based on the Divide-and-Conquer (DC) algorithm [11,21]. This algorithm partitions a matrix into multiple small submatrices and performs the ED simultaneously for each sub-matrix. Aided by the power of parallel and distributed computing, its speed is only mildly influenced by the matrix dimension and can be very fast for a single matrix. The core of the DC algorithm is the characteristic polynomials $\det(\lambda \mathbf{I} - \mathbf{A}) = 0$, which can be solved by various methods, such as secular equations [21] and spectral division [35]. However, solving the polynomial requires simultaneously localizing all the eigenvalue intervals for each individual matrix. Despite the high efficiency for a single matrix, these DC algorithms do not scale to batched matrices.

Except for the DC algorithm, some ED solvers would use the QR iteration. The QR iteration has many implementation methods and one particular batch-efficient choice is by Givens rotation. The Givens rotation can be implemented via matrix-matrix multiplications, which naturally extends to batched matrices. During the QR iterations, the Givens rotation is applied successively to annihilate the off-diagonal entries until the matrix becomes diagonal. The major drawback limiting the usage of QR iterations is the $O(n^5)$ time cost, which makes this method only applicable to tiny matrices (e.g., dim < 9). To alleviate this issue, modern QR-based ED implementations apply the technique of deflation [2, 6, 7], i.e., partition the matrix into many sub-matrices. The deflation technique can greatly improve the speed of the QR iterations but only works for an individual matrix. For the QR iteration, the convergence speed is related with the adjacent eigenvalue ratio $\frac{\lambda_{i+1}}{\lambda_i}$. For multiple matrices within a mini-batch, the

off-diagonal entries of each matrix converge to zero with inconsistent speed and where each matrix can be partitioned is different. Consequently, the deflation technique does not apply to batched matrices either. To give a concrete example, consider 2 matrices of sizes 8×8 in a mini-batch. Suppose that the deflation would split one into two 3×3 and 5×5 matrices, while the other matrix might be partitioned into two 4×4 matrices. In this case, the partitioned matrices cannot be efficiently processed as a mini-batch due to the inconsistent matrix sizes.

To attain a batch-friendly and GPU-efficient ED method dedicated to computer vision field, we propose a QR-based ED algorithm that performs the ED via batched matrix/vector multiplication. Each step of the ED algorithm is carefully motivated for the best batch-efficient and computation-cheap consideration. We first perform a series of batched Householder reflectors to tri-diagonalize the matrix by the batched matrix-vector multiplication. Afterward, the explicit QR iteration by matrix rotation with double Wilkinson shifts [48] is conducted to diagonalize the matrix. The proposed shifts make the last two diagonal entries of the batched matrices have consistent convergence speed. Thereby the convergence is accelerated and the matrix dimension can be progressively shrunk during the QR iterations. Besides the dimension reduction, we also propose some economic computation methods based on the complexity analysis. The time complexity of QR is thus reduced from $O(n^5)$ to $O(n^3)$. The numerical tests demonstrate that, for matrices whose dimensions are smaller than 24, our Pytorch implementation is consistently much faster than the default SVD routine for any batch size. For matrices with larger dimensions (e.g., dim=32 or 36), our method could also have an advantage when the batch size is accordingly large (see also Fig. 1). We validate the effectiveness of our method in several applications of differentiable SVD, including decorrelated BN, covariance pooling for vision transformers, and neural style transfer. Our Batched ED achieves competitive performances against the SVD.

The contributions of the paper are summarized threefold:

- We propose an ED algorithm for a mini-batch of small and medium matrices which is dedicated to many application scenarios of computer vision. Each step of ED is carefully motivated and designed for the best batch efficiency.
- We propose dedicated acceleration techniques for our Batched ED algorithm. The progressive dimension shrinkage is proposed to reduce the matrix size during the iterations, while some economic computation methods grounded on the complexity analysis are also developed.
- Our batch-efficient ED algorithm is validated in several applications of differentiable SVD. The experiments on visual recognition and image generation demonstrate that our method achieves very competitive performances against the SVD encapsuled in the current deep learning platforms.

2 Related Work

In this section, we discuss the related work in computing the differentiable ED and its applications.

2.1 Computing the Differentiable ED

To perform the ED, modern deep learning frameworks (e.g., Pytorch and Tensorflow) call the LAPACK's SVD routine by default. The routine uses the Divide-and-Conquer algorithm [11,21] to conduct the ED. Assisted by the power of parallel and distributed computing, the divide-and-conquer-based ED can simultaneously process each sub-matrix and achieve high efficiency for a single matrix regardless of the matrix size. However, solving the core characteristic polynomials requires simultaneously finding all the eigenvalue intervals for each individual matrix, which causes this algorithm unable to scale to batched matrices well. There are also some routines that use QR iterations with deflation for performing the ED [6,7]. Equipped with the deflation technique to partition the matrices, the QR iteration can also have a fast calculation speed. When it comes to a mini-batch of matrices, the off-diagonal entries of each matrix converge to zero with different speeds, and where each matrix can be partitioned is inconsistent. Hence, the deflation technique cannot be applied to batched matrices.

For the back-propagation of the ED, it suffers from the numerical instability caused by the close and repeated eigenvalues. Recently, several methods have been proposed to solve the instability issue [45, 46, 38]. Wei et al. [45] propose to use Power Iteration (PI) to approximate the SVD gradients. Song et al. [38] propose to use Padé approximants to closely estimate the gradients. Despite the applicability of these methods, a more practical approach is to divide the features $\mathbf{X} \in \mathbb{R}^{C \times BHW}$ into groups $\mathbf{X} \in \mathbb{R}^{G \times \frac{C}{G} \times BHW}$ in the channel dimension and attain a mini-batch of small covariance matrices $\mathbf{X} \mathbf{X}^T \in \mathbb{R}^{G \times \frac{C}{G} \times \frac{C}{G}}$ [23, 36], which can keep more channel statistics and naturally avoid the gradient explosion issue caused by the rank-deficiency. This also raises the need of such an ED solver that works for batched matrices efficiently.

To attain the batch-efficient ED algorithm dedicated to computer vision field, we propose our QR-based algorithm for small and medium batched matrices. We motivate each step of our ED algorithm for the best batch efficiency. Our ED solver integrates double Wilkinson shifts [48] to guarantee that the last two diagonal entries have consistent convergence speed within the mini-batch, and consequently the matrix dimension can be progressively reduced. With several other acceleration technique grounded on the complexity analysis, our solver can be much faster than Pytorch SVD for a mini-batch of small matrices.

2.2 Applications of the Differentiable ED

The need for differentiable ED arises in numerous applications of computer vision. Some methods adopt the end-to-end ED to compute the matrix square root of the global covariance feature before the fully-connected layer [34, 31, 30, 44, 38, 49, 17, 40, 41]. Such approaches are termed as Global Covariance Pooling (GCP) methods, and they have achieved state-of-the-art performances on both generic and fine-grained visual recognition. Another line of research uses the ED to perform the decorrelated batch normalization (BN) [23, 26, 36, 24, 39, 25, 39]. The process resembles the ZCA whitening transform to compute the inverse square

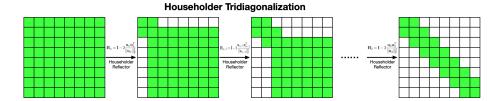


Fig. 2. Visual illustration of our batched Householder tri-diagonalization. After (n-2) designed reflections, the symmetric matrix **A** is reduced to a tri-diagonal matrix **T**.

root for eliminating the correlation between features. The differentiable ED can be also applied in the area of neural style transfer. As pointed out in [18, 19], the feature covariance naturally embeds the style information. Some methods use the differentiable ED to perform successive WCT on the feature covariance for the universal style transfer [32, 9, 10]. In the geometric vision, the ED is often applied to solve the PnP problem and estimate the camera pose [29, 5, 8, 13]. Besides the main usages above, there are some other minor applications [12, 42].

3 Methodology

In this section, we present our method that performs the Batched ED. Our algorithm is implemented via the sequential batched Householder reflectors to tri-diagonalize the matrix and the batched QR iteration to diagonalize the tri-diagonal matrix. Both processes are GPU-friendly and batch-efficient. Now we illustrate each process in detail. Notice that every step is applied on batched matrices for the best efficiency.

3.1 Batched Tri-diagonalization based on Householder Reflection

Given the Hermitian matrix A, the tri-diagonalization process is defined as:

$$\mathbf{A} = \mathbf{P}\mathbf{T}\mathbf{P}^T \tag{1}$$

where \mathbf{T} is a tri-diagonal matrix, and \mathbf{P} is an orthogonal matrix. To perform such an orthogonal similarity transform, we can decompose \mathbf{P} into n-2 Householder reflectors. This leads to the re-formulation:

$$\mathbf{T} = \mathbf{P}^T \mathbf{A} \mathbf{P} = (\mathbf{H}_n \dots \mathbf{H}_4 \mathbf{H}_3)^T \mathbf{A} (\mathbf{H}_n \dots \mathbf{H}_4 \mathbf{H}_3)$$
(2)

Each reflector is both orthogonal ($\mathbf{H}\mathbf{H}^T = \mathbf{I}$) and unitary ($\mathbf{H} = \mathbf{H}^T$). The reflector is constructed using an vector:

$$\mathbf{H} = \mathbf{I} - 2 \frac{\mathbf{u}\mathbf{u}^T}{||\mathbf{u}||_{\mathrm{F}}^2} \tag{3}$$

The matrix **H** reflects the vector **u** along the direction that is perpendicular to the hyper-plane orthogonal to **u**. This property can be used to tri-diagonalize a symmetric matrix by reflecting each row and column sequentially. A Householder reflection is computed by:

$$\mathbf{H}\mathbf{A}\mathbf{H} = (\mathbf{I} - 2\frac{\mathbf{u}\mathbf{u}^{T}}{||\mathbf{u}||_{F}^{2}})\mathbf{A}(\mathbf{I} - 2\frac{\mathbf{u}\mathbf{u}^{T}}{||\mathbf{u}||_{F}^{2}})$$

$$= \mathbf{A} - \mathbf{p}\mathbf{u}^{T} - \mathbf{u}\mathbf{q}^{T}$$
(4)

where the temporary variables \mathbf{q} , \mathbf{p} , and K are defined as:

$$\mathbf{q} = \mathbf{p} - K\mathbf{u}, \mathbf{p} = \frac{2\mathbf{A}\mathbf{u}}{\|\mathbf{u}\|_{\mathrm{F}}^{2}}, K = \frac{\mathbf{u}^{T}\mathbf{p}}{\|\mathbf{u}\|_{\mathrm{F}}^{2}}$$
(5)

As can be seen, Eq. (4) actually defines a symmetric rank-2 update on **A**. By some deductions on Eq. (4), each Householder reflector can be designed to introduce zero entries to a row and a column (see Fig. 2). We omit the derivation of the vector for conciseness and give the result here:

$$\mathbf{u}_{i} = [0, \dots, a_{i,i}, a_{i,i+1}, \dots, a_{i,n-1}, \sigma],$$

$$\sigma = \pm \sqrt{a_{i,i}^{2} + a_{i,i+1}^{2} + \dots + a_{i,n-1}^{2}}$$
(6)

where $a_{i,j}$ denotes the entry of **A** at *i*-th row and *j*-th column, and the sign of σ is usually chosen as $\operatorname{sign}(a_{i,n})$ to reduce the round-off error. By such a construction, only n-2 reflections are needed to transform the symmetric matrix into the tridiagonal form. Each householder reflection needs 2 matrix-matrix multiplication, which takes $O(2n^3)$ complexity. However, as indicated in Eq. (4) and Eq. (5), the calculation can be reduced to one matrix-matrix multiplication and two matrix-vector multiplication, which needs the complexity of $O(n^3+2n^2)$.

When the eigenvector is required, we can calculate ${\bf P}$ by accumulating the Householder reflectors:

$$\mathbf{P} = \mathbf{H}_n \dots \mathbf{H}_4 \mathbf{H}_3 \tag{7}$$

The computation needs (n-2) matrix multiplication where the complexity of each multiplication is $O(n^3)$. We note this step can be further accelerated by:

Theorem 1 (WY representation [4]) For any accumulation of m Householder matrices $\mathbf{H}_1 \dots \mathbf{H}_m$, there exists $\mathbf{W}, \mathbf{Y} \in \mathbb{R}^{(n-2) \times m}$ such that we have the relation $\mathbf{I} - 2\mathbf{W}\mathbf{Y}^T = \mathbf{H}_1 \dots \mathbf{H}_m$. Computing \mathbf{W} and \mathbf{Y} takes O((n-2)m) time and (m-1) sequential Householder multiplications.

Relying on this theorem, we can divide the accumulation $\mathbf{H}_n ... \mathbf{H}_4 \mathbf{H}_3$ into (n-2)/m sub-sequences and compute them in parallel. Each sub-sequence takes $O((m-1)n^3+(n-2)m)$ time to compute the WY representation and $O((n-2)^2m)$ time to compute $\mathbf{I}-2\mathbf{W}\mathbf{Y}^T$. Combining all the sub-sequence needs extra time of $O((n-2)^3/m)$. This can further reduce the complexity of computing \mathbf{P} from $O((n-3)n^3)$ to $O((m-1)n^3+(n-2)m+(n-2)^2m+(n-2)^3/m)$. The computation saving would be huge when n is large.



Fig. 3. Visual illustration of the batched Givens diagonalization. For each QR iteration, the Givens rotation is applied from the left top corner to the bottom right corner to reduce the magnitude of the off-diagonal elements. The iteration continues till the off-diagonal entries become zero or below a certain tolerance.

3.2 Batched Diagonalization based on QR Iteration

After obtaining the tri-diagonal matrix **T**, we use the Givens rotation to perform the QR iterations, which can be implemented efficiently via batched matrix multiplication. Based on the ordinary QR iteration, we further apply several techniques to speed up the convergence and save the computational budget.

Basic QR Iteration by Givens Rotation. Given the tri-diagonal matrix T, the QR iteration takes the following iterative update:

$$\mathbf{T}_k = \mathbf{Q}_k \mathbf{R}_k, \ \mathbf{T}_{k+1} = \mathbf{R}_k \mathbf{Q}_k \tag{8}$$

where \mathbf{Q}_k denotes the orthogonal matrix, and \mathbf{R}_k is the upper-triangular matrix. Replacing \mathbf{R}_k with $\mathbf{Q}_k^T \mathbf{T}_k$ leads to the re-formulation of Eq. (8):

$$\mathbf{T}_{k+1} = \mathbf{Q}_k^T \mathbf{T}_k \mathbf{Q}_k \tag{9}$$

As can be seen, a single QR iteration is equivalent to performing an orthogonal similarity transform. By performing the iterations, the sub-diagonal and super-diagonal entries are gradually reduced till the matrix becomes diagonal.

For each QR iteration, we construct the orthogonal transform using successive Givens rotations moving from the left top corner to the right bottom corner. The 2×2 Givens Rotation and its $n\times n$ extension are defined by:

$$\mathbf{R}_{2\times2} = \begin{bmatrix} \cos\theta - \sin\theta \\ \sin\theta & \cos\theta \end{bmatrix}, \mathbf{R}_{n\times n} = \begin{bmatrix} \mathbf{I} & \mathbf{0} & \mathbf{0} \\ \mathbf{0} & \mathbf{R}_{2\times2} & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{I} \end{bmatrix}$$
(10)

where θ is the rotation angle, and the rotation matrix is orthogonal but not symmetric (*i.e.*, $\mathbf{R}^T\mathbf{R}=\mathbf{I}$ and $\mathbf{R}^T\neq\mathbf{R}$). As shown in Fig. 3, by design of the rotation angle, the successive Givens Rotation applied on \mathbf{T} can keep the tri-diagonal form but reduce the magnitude of off-diagonal elements. For the derivation of Givens rotation, please refer to the supplementary material for detail. The sequential Givens rotations moving along the diagonal form one single QR iteration:

$$\mathbf{T}_{k+1} = (\mathbf{R}_{n-2}^T \dots \mathbf{R}_0^T) \mathbf{T}_k (\mathbf{R}_0 \dots \mathbf{R}_{n-2})$$
(11)

where \mathbf{R}_i denotes the *i*-th rotation counting from the left top corner. For the orthogonal matrix \mathbf{Q}_i in the *i*-th QR iteration, we can easily find out

$$\mathbf{Q}_k = \mathbf{R}_0 \dots \mathbf{R}_{n-2} \tag{12}$$

Taking the Householder tri-diagonalization and Givens diagonalization together, our batch-efficient ED algorithm can be formally defined by:

$$\mathbf{A} = (\mathbf{PQ}_0 \dots \mathbf{Q}_k) \mathbf{\Lambda} (\mathbf{PQ}_0 \dots \mathbf{Q}_k)^T$$
(13)

where k is the iteration times of the QR iteration, Λ is the eigenvalue matrix, and $\mathbf{PQ}_0 \dots \mathbf{Q}_k$ is the eigenvector matrix. For the convergence, we have:

Theorem 2 (Convergence of QR iteration) Let \mathbf{T} be the positive definite tri-diagonal matrix with the eigendecomposition $\mathbf{Q}\mathbf{\Lambda}\mathbf{Q}^T$ and assume \mathbf{Q}^T can be LU decomposed. Then the QR iteration of \mathbf{T} will converge to $\mathbf{\Lambda}$.

We defer the proof to the Supplementary Material. The key results of this theorem is that the convergence speed depends on the adjacent eigenvalue ratio $\frac{\lambda_i}{\lambda_j}$ for i>j. The QR iterations usually take 2n iterations to make the resultant matrix diagonal [16]. Consider the fact that each iteration takes (n-1) Givens rotation. The computation overhead would be huge. For deriving the eigenvalues, we need $4n(n-1)n^3$ time, while it takes the complexity of $2n(n-2)n^3+(2n-1)n^3$ to compute the eigenvector. The time complexity of the QR iteration is quintic to the matrix dimension n, which would make this method only applicable to the tiny matrices (<9). Existing deflation techniques [6,7] to accelerate the computation cannot be applied to our batched matrices. To resolve this issue, we propose the following techniques:

Double Wilkinson Shift. As indicated in Theorem 2, the convergence speed of QR iteration depends on the ratio $\frac{\lambda_i}{\lambda_j}$, where i > j. A natural approach to accelerate the convergence is to shift the matrix by $\mathbf{T} - \mu \mathbf{I}$ such that the convergence speed becomes $\frac{\lambda_i - \mu}{\lambda_j - \mu}$. A preferable shift coefficient should be $u = \lambda_j$, as this can help the matrices to converge quickly: $\frac{\lambda_i - \mu}{\lambda_j - \mu} = \infty$. This is particularly useful for matrices in a mini-batch as the speed can be made consistent by shifting.

Since each Givens rotation will affect the area rotated by the previous one, only the last 2×2 Givens rotation will not be influenced, *i.e.*, the two eigenvalues of the last block can be locally estimated. Thus, we propose to extract the shift coefficients from the 2×2 block on the right bottom corner:

$$\mu_{n-2}, \mu_{n-1} = Wilkinson(\mathbf{T}_k[n-2:n]) \tag{14}$$

where $\mathbf{T}_k[n-2:n]$ denotes the last 2×2 block of \mathbf{T}_k , and μ_{n-2} and μ_{n-1} are the two eigenvalues computed from this block. These shifting coefficients are referred to as the Wilkinson shift [48], and we give the derivation in the supplementary

material. After attaining the shifts, we can reformulate the QR iterations with double shifts:

$$\mathbf{T}_{k+1/2} = \mathbf{Q}_k^T (\mathbf{T}_k - \mu_{n-1} \mathbf{I}) \mathbf{Q}_k + \mu_{n-1} \mathbf{I}$$

$$\mathbf{T}_{k+1} = \mathbf{Q}_k^T (\mathbf{T}_{k+1/2} - \mu_{n-2} \mathbf{I}) \mathbf{Q}_k + \mu_{n-2} \mathbf{I}$$
(15)

With the shifts, the integrated iteration consists of two sequential QR iterations shifted by the eigenvalues μ_{n-2} and μ_{n-1} , respectively.

Progressive Dimension Shrinkage. One direct benefit brought by the Wilkinson shift is that, for all the matrices in a mini-batch, the last two diagonal entries can quickly converge to the corresponding eigenvalues and the off-diagonal elements can converge to zero:

$$\begin{array}{l} t_{n-2,n-2} \!\!\to\!\! \lambda_{n-2}, \ t_{n-2,n-3} \!\!=\!\! t_{n-3,n-2} \!\!\to\!\! 0 \\ t_{n-1,n-1} \!\!\to\!\! \lambda_{n-1}, \ t_{n-2,n-1} \!\!=\!\! t_{n-1,n-2} \!\!\to\!\! 0 \end{array} (16)$$

We can use this property to speed up the computation by gradually reducing the matrix dimension, *i.e.*, shrinking the matrix by $\mathbf{T} \in \mathbb{R}^{n \times n} \to \mathbf{T} \in \mathbb{R}^{(n-1) \times (n-1)}$ after one iteration. As shown in Fig. 4, when the last subdiagonal entry is below a given small threshold (*e.g.*, 1*e*-5), we could shrink the matrix

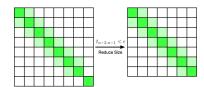


Fig. 4. Illustration of the progressive dimension reduction in the QR iterations. After one iteration, if the last sub-diagonal entry is below a small threshold ϵ , we can remove the last row and column.

by removing the last row and column. In doing so, the matrix size is progressively reduced during the QR iterations. With the dimension reduction, one QR iteration would take (n-1-r) Givens rotations, where r is the reduction times.

Economic Eigenvalue Calculation. For a Givens rotation, it only affects the adjacent 4×4 block. We can save the computation budget by applying the matrix multiplication on the 4×4 rotation region in the neighborhood. This reduces the time of a rotation from $O(2n^3)$ to $O(2\times4^3)=O(128)$, which makes each rotation consume a constant time cost. Taking the above dimension reduction into account, the QR iterations need O(256n(n-1-r)) time to derive the eigenvalues.

Economic Eigenvector Calculation. Equipped with the progressive dimension reduction, the orthogonal transform \mathbf{Q}_k in a QR iteration is defined by:

$$\mathbf{Q}_k = \mathbf{R}_0 \mathbf{R}_1 \dots \mathbf{R}_{n-2-r} \tag{17}$$

where we need (n-2-r) rotations for each iteration. The computation can be potentially simplified by the theorem:

Theorem 3 (Implicit Q Theorem [20]) Let **B** be an upper Hessenberg and only have positive elements on its first sub-diagonal. Assume there exists a unitary transform $\mathbf{Q}^H \mathbf{A} \mathbf{Q} = \mathbf{B}$. Then \mathbf{Q} and \mathbf{B} are uniquely determined by \mathbf{A} and the first column of \mathbf{Q} .

We give the proof and some discussion in the supplementary document. This theorem implies that, without the need of explicit QR iteration, the orthogonal transform ${\bf Q}$ and the transformed matrix ${\bf B}$ can be both implicit calculated. However, it assumes that the sub-diagonal elements of ${\bf B}$ are positive. In our case, the Givens rotation can easily zero out the last two sub-diagonal entries. Consequently, directly using the theorem would cause large round-off errors and data overflow.

Although the theorem cannot be directly applied, it allows us to simplify the eigenvector calculation. As indicated by the theorem, the i-th rotation would only affect the orthogonal matrix \mathbf{Q} on the area after the i-th row and column. We can reduce the computation by involving only part of the matrix and simplify the calculation in Eq. (17) as:

$$\mathbf{Q}_k = \mathbf{R}_0[1:]\mathbf{R}_1[2:]\dots\mathbf{R}_{n-2-r}[n-2-r:]$$
(18)

where [i:] denotes part of the matrix that excludes the first i rows and columns. By doing so, the time complexity of calculating \mathbf{Q}_k can be reduced to:

$$(n-2-r)^{2}n+(n-3-r)^{2}n+\ldots+1^{2}n$$

$$=\sum_{i=1}^{n-2-r}i^{2}n=\frac{(n-2-r)(n-1-r)(2n-3-2r)}{6}n$$
(19)

Compared with the original time cost $O((n-2-r)n^3)$, the saving would be considerable for large n and r.

3.3 Computation Complexity Summary

Table 1. Comparison of time complexity of the basic QR-based ED solver and our ED solver dedicated for batched matrices. Here n denotes the matrix size and r represents the average reduction times during the QR iterations.

Time	Basic QR-based ED Solver						
	Eigenvalue	Eigenvector					
Tri-diag.	$2n^3$	$(n-3)n^3$					
QR	$4n(n-1)n^3$	$2n(n-2)n^3 + (2n-1)n^3$					
Sum	$(4n^2-4n+2)n^3$	$(2n^2-n-4)n^3$					
Time	Our Batched ED Solver						
111110	Eigenvalue	Eigenvector					
Tri-diag.	$n^3 + 2n^2$	$(m-1)n^3 + (n-2)m + (n-2)^2m + \frac{(n-2)^3}{m}$					
QR	256(n-1-r)n	$\frac{(n-2-r)(n-1-r)(2n-3-2r)}{6}2n^2+(2n-1)n^3$					
Sum	$n^3 + 258n^2 - 256n(1+r)$	$\frac{2}{m^5}$ $\frac{(2m+1)m^4+(2m^2+6m+7+m)m^3}{(2m+1)m^4+(2m^2+6m+7+m)m^3}$					

Table 1 summarizes the time complexity of the basic QR-based ED solver and our proposed ED solver dedicated for batched matrices. Taking the highest-order

term for simpler analysis, our ED solver reduces the time from $O(4n^5)$ to $O(n^3)$ for computing the eigenvalues, and saves the time from $O(2n^5)$ to $O(\frac{2}{3}n^5)$ for eigenvectors. Moreover, depending on the reduction times r, the complexity can be further reduced with the term -256(1+r)n for eigenvalues and $-(2r+1)n^4$ for eigenvectors.

3.4 Convergence and Error Bounds

For the tri-diagonalization process, the convergence is guaranteed with n-2 Householder reflectors. The error is only related to the machine precision and data precision, which can be sufficiently neglected. For the QR iterations, the convergence mainly depends on the adjacent eigenvalue ratio $\frac{\lambda_{i+1}}{\lambda_i}$ and the shift μ . In certain cases when the two eigenvalue are close $(\frac{\lambda_{i+1}}{\lambda_i} \approx 1)$, the convergence speed is slow and the residual term $(\frac{\lambda_{i+1}-\mu}{\lambda_i-\mu})^{2n}$ becomes the error. Another error source comes from the tolerance ϵ for the dimension reduction. Let $\bar{\Lambda}$ represent the exact eigenvalues and Λ denote the eigenvalues calculated by our ED solver. Then the error is bounded by:

$$||\bar{\mathbf{\Lambda}} - \mathbf{\Lambda}||_{\mathcal{F}} \le \max_{i} \left(\left(\frac{\lambda_{i+1} - \mu}{\lambda_{i} - \mu} \right)^{2n} |l_{i+1,i}| \right) + \epsilon \tag{20}$$

where $l_{i+1,i}$ is the entry of **L** computed by $\mathbf{Q}^T = \mathbf{L}\mathbf{U}$, and the shift μ changes every QR iteration. Since **Q** is orthogonal, the magnitude of $l_{i+1,i}$ is often quite small. Considering the small magnitude of $l_{i+1,i}$ and the additional shifting technique, the accuracy of our method will not get affected.

4 Experiments

In this section, we first perform a numerical test to compare our method with SVD for matrices in different dimensions and batch sizes. Subsequently, we evaluate the effectiveness of the proposed methods in three computer vision applications: decorrelated BN, second-order vision transformer, and neural style transfer. The implementations details are referred to supplementary material.

4.1 Numerical Test

Fig. 5 depicts the computational time of our Batched ED against the SVD for different matrix dimensions and batch sizes. The time cost of the SVD grows almost linearly with the batch size, while the time consumption of our Batched ED only has slight or mild changes against varying batch sizes. For matrices whose dimensions are smaller than 24, our Batched ED is consistently faster than the SVD for any batch size. When the matrix dimension is 32, our method is faster than the SVD from batch size 256 on. The speed of our Batched ED is more advantageous for smaller matrix dimensions and larger batch sizes.

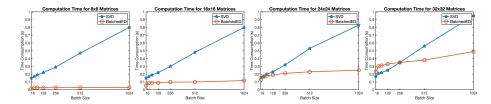


Fig. 5. The speed comparison of our Batched ED against TORCH.SVD for different batch sizes and matrix dimensions. Our implementation is more batch-friendly and the time cost does not vary much against different batch sizes. For matrices in small and moderate sizes, our method can be significantly faster than the Pytorch SVD.

Table 2. Validation error of decorrelated BN on ResNet-18 [22]. The results are reported based on 5 runs, and we measure the time of the forward ED in a single step.

Solver	Group	Size	Time (s)	CIFAR1	.0	CIFAR100		
Solvei	Group			mean±std	min	$mean \pm std$	min	
SVD	16	$16 \times 4 \times 4$	0.172	4.52 ± 0.09	4.33	21.24 ± 0.17	20.99	
Batched ED			0.006	$4.37 {\pm} 0.11$	4.29	21.25 ± 0.20	20.90	
SVD	8	8×8×8	0.170	4.55 ± 0.13	4.34	21.32 ± 0.31	20.88	
Batched ED	°		0.016	$4.36 {\pm} 0.11$	4.25	$20.97 {\pm} 0.27$	20.62	
SVD	1 4	$4 \times 16 \times 16$	0.165	4.52 ± 0.14	4.33	21.30 ± 0.33	20.86	
Batched ED			0.075	$ 4.45{\pm}0.11 $	4.32	${\bf 21.19 {\pm} 0.21}$	20.98	

4.2 Decorrelated BN

Following [39], we first conduct an experiment on the task of ZCA whitening. In the whitening process, the inverse square root of the covaraince is multiplied with the feature as $(\mathbf{X}\mathbf{X}^T)^{-\frac{1}{2}}\mathbf{X}$ to eliminate the correlation between each dimension. We insert the ZCA whitening meta-layer into the ResNet-18 [22] architecture and evaluate the validation error on CIFAR10 and CIFAR100 [28]. Table 2 compares the performance of our Batched ED against the SVD. Depending on the number of groups, our method can be 2X faster, 10X faster, and even 28X faster than the SVD. Furthermore, our method outperforms the SVD across all the metrics on CIFAR10. With CIFAR100, the performance is also on par.

4.3 Second-order Vision Transformer

We turn to the experiment on the task of global covariance pooling for the Second-order Vision Transformer (So-ViT) [49]. To leverage the rich semantics embedded in the visual tokens, the covariance square root of the visual tokens $(\mathbf{X}\mathbf{X}^T)^{\frac{1}{2}}$ are used to assist the classification task. Since the global covariance matrices are typically very ill-conditioned [38], this task poses a huge challenge to the stability of the ED algorithm. We choose the So-ViT architecture with different depths and validate the performance on ImageNet [14]. As observed from Table 3, our Batched ED has the competitive performance against the standard SVD. Moreover, our method is about 44% and 27% faster than the SVD for covariance in different sizes.

Table 3. Validation accuracy on ImageNet [14] for the second-order vision transformer with different depths. Here 32 and 36 denote the spatial dimension of visual tokens. We report the time consumption of the forward ED in a single step.

Solver	Size	Time (s)	Architecture			
Solvei			So-ViT-7	So-ViT-10		
SVD	768×32×32	0.767	76.01 / 93.10	77.97 / 94.10		
Batched ED			76.04 / 93.05			
SVD Batched ED	769 126 126	0.835	76.10 / 93.14	78.09 / 94.13		
Batched ED	100×30×30	0.612	76.07 / 93.10	78.11 / 94.19		



Fig. 6. Exemplary visual comparison. The red circle/rectangular indicates the region with subtle details. In this example, our method generates sharper images with more coherent style information and less artifacts. Zoom in for a better view.

4.4 Universal Style Transfer

Now we apply our Batched ED in the WCT for neural style transfer. Given the content feature \mathbf{X}_c and the style feature \mathbf{X}_s , the WCT performs successive whitening $((\mathbf{X}_c\mathbf{X}_c)^{-\frac{1}{2}}\mathbf{X}_c)$ and coloring $((\mathbf{X}_s\mathbf{X}_s)^{\frac{1}{2}}\mathbf{X}_c)$ to transfer the target style. We follow [32, 47] to use the LPIPS distance and the user preference as the evaluation metrics. Table 4 presents the quantitative comparison with different groups. Our Batched ED achieves very competitive performance and predominates the speed. To give a concrete example, when the group number is 64, our method is about 35X faster than the default SVD. Fig. 6 displays the exemplary visual comparison. In this specific example, our Batched ED generates images with better visual appeal.

Similar to the finding in [9], we also observe that the number of groups has an impact on the extent of transferred style. As shown in Fig. 7, when more groups are used, the style in the transferred image becomes more distinguishable and the details are better preserved. Since the number of groups determines the number of divided channels and the covariance size, more groups correspond to smaller covariance and this might help to better capture the local structure. Despite

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Table 4. The LPIPS distance between the transferred image and the content image and the user preference (%) on the Artworks [27] dataset. We report the time consumption of the forward ED that is conducted 10 times to exchange the style and content feature at different network depths. The batch size is set to 4.

	la	l au	lm. ()	Ir prpg [eq] (i)	ID 6 (1)
Solver	Group	Size	Time (s)	LPIPS [50] (↑)	Preference (↑)
SVD	64	256×4×4	3.146	0.5776	48.25
Batched ED	04	250×4×4	0.089	0.5798	47.75
SVD	32	128×8×8	2.306	0.5722	47.75
Batched ED	32	120×0×0	0.257	0.5700	48.75
SVD	16	64×16×16	1.973	0.5614	46.25
Batched ED			0.876	0.5694	47.75

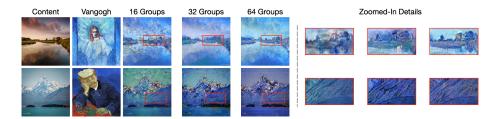


Fig. 7. Visual illustration of the impact of groups. When more groups are used, the strength of the target style is increased and the details are better preserved.

this superficial conjecture, giving a more comprehensive and rigorous analysis is worth further research.

To sum up, our ED solver has demonstrated the superior batch efficiency for small matrices in various real-world experiments and numerical tests. The limitation on large matrices indicates the key difference: our method is more batch-efficient, while TORCH.EIG/SVD is more dimension-efficient.

5 Conclusion

In this paper, we propose a batch-efficient QR-based ED algorithm dedicated for batched matrices which are common in the context of computer vision and deep learning. Aided by the proposed acceleration techniques, our solver is much faster than Pytorch SVD function for a mini-batch of small and medium matrices. Our method can directly benefit a wide range of computer vision applications and we showcase this merit in several applications of differentiable ED. Extensive experiments on visual recognition and image generation demonstrate that our method can also achieve very competitive performances.

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This document presents the detailed illustration of some used techniques (Sec. A), the introduction of our experimental settings and implementation details (Sec. B), and the detailed analysis and discussion of our method (Sec. C).

A Theoretical Deviation

A.1 2×2 Givens Rotation

Our goal is to eliminate the sub-diagonal/super-diagonal entry of a 2×2 matrix. Consider a given vector $\mathbf{x}^T = [x_1, x_2]$. We can set the entries of Givens rotation as:

$$\cos \theta = \frac{x_1}{\sqrt{x_1^2 + x_2^2}}, \sin \theta = -\frac{x_2}{\sqrt{x_1^2 + x_2^2}}$$
 (21)

Then the rotation can eliminate the entry by:

$$\mathbf{R}^T \mathbf{x} = \begin{bmatrix} \cos \theta - \sin \theta \\ \sin \theta & \cos \theta \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} = \begin{bmatrix} \sqrt{x_1^2 + x_2^2} \\ 0 \end{bmatrix}$$
 (22)

For the simplicity concern, we use c and s to represent $\cos \theta$ and $\sin \theta$, respectively. Let us extend the vector \mathbf{x} to a symmetric matrix \mathbf{X} . The Givens rotation is applied by an orthogonal similarity transform:

$$\mathbf{R}^{T}\mathbf{X}\mathbf{R} = \begin{bmatrix} c - s \\ s & c \end{bmatrix} \begin{bmatrix} x_{1} & x_{2} \\ x_{2} & x_{3} \end{bmatrix} \begin{bmatrix} c & s \\ -s & c \end{bmatrix}$$

$$= \begin{bmatrix} x_{1}c - x_{2}s & x_{2}c - x_{3}s \\ x_{1}s + x_{2}c & x_{2}s + x_{3}c \end{bmatrix} \begin{bmatrix} c & s \\ -s & c \end{bmatrix}$$

$$= \begin{bmatrix} x_{1}c^{2} - 2x_{2}cs + x_{3}s^{2} & x_{2}(c^{2} - s^{2}) + (x_{1} - x_{3})cs \\ x_{2}(c^{2} - s^{2}) + (x_{1} - x_{3})cs & x_{1}s^{2} + 2x_{2}cs + x_{3}c^{2} \end{bmatrix}$$
(23)

As can be seen, the symmetric and orthogonal form still manifests after rotation. From Eq. (21) and Eq. (22), we have $cx_2+sx_1=0$. Injecting this relation into Eq. (23) leads to the re-formulation:

$$\begin{bmatrix} x_1c^2 - 2x_2cs + x_3s^2 & -x_2s^2 - x_3cs \\ -x_2s^2 - x_3cs & x_1s^2 + 2x_2cs + x_3c^2 \end{bmatrix}$$
(24)

The magnitude of sub-diagonal entries gets smaller. A series of such Givens rotations moving along the diagonal form the orthogonal matrix \mathbf{Q}_k for a QR iteration:

$$\mathbf{Q}_k = \mathbf{R}_{0:2} \mathbf{R}_{1:3} \dots \mathbf{R}_{N-2:N} \tag{25}$$

where the sub-script of ${\bf R}$ denotes the region where the the rotation is applied. Notice that the successive Givens rotations still keep the tri-diagonal form of the matrix but gradually reduce the strength of super-diagonal entries. This accounts for why the QR iterations can transform a tri-diagonal matrix into a diagonal one.

A.2 Convergence of QR iteration

We give the proof for the theorem about convergence speed of QR iterations.

Theorem 2 (Convergence of QR iteration) Let \mathbf{T} be the positive definite tri-diagonal matrix with the eigendecomposition $\mathbf{Q}\mathbf{\Lambda}\mathbf{Q}^T$ and assume \mathbf{Q}^T can be LU decomposed. Then the QR iteration of \mathbf{T} will converge to $\mathbf{\Lambda}$.

Proof. Since we have $\mathbf{T} = \mathbf{Q} \mathbf{\Lambda} \mathbf{Q}^T$, then:

$$\mathbf{T}^k = \mathbf{Q} \mathbf{\Lambda}^k \mathbf{Q}^T = (\mathbf{Q}_0 \dots \mathbf{Q}_k) (\mathbf{R}_k \dots \mathbf{R}_0)$$
 (26)

By assuming $\mathbf{Q}^T = \mathbf{L}\mathbf{U}$, this equation can be written as:

$$\mathbf{Q}\mathbf{\Lambda}^{k}\mathbf{L}\mathbf{U} = (\mathbf{Q}_{0}\dots\mathbf{Q}_{k})(\mathbf{R}_{k}\dots\mathbf{R}_{0})$$
$$\mathbf{Q}\mathbf{\Lambda}^{k}\mathbf{L}\mathbf{\Lambda}^{-k} = (\mathbf{Q}_{0}\dots\mathbf{Q}_{k})(\mathbf{R}_{k}\dots\mathbf{R}_{0})\mathbf{U}^{-1}\mathbf{\Lambda}^{-k}$$
 (27)

For $\Lambda^k \mathbf{L} \Lambda^{-k}$, its entry is defined by:

$$(\mathbf{\Lambda}^{k}\mathbf{L}\mathbf{\Lambda}^{-k})_{i,j} = \begin{cases} l_{i,j}(\frac{\lambda_{i}}{\lambda_{j}})^{k} & i > j\\ 1 & i = j\\ 0 & otherwise \end{cases}$$
 (28)

When $k\to\infty$, we have $(\frac{\lambda_i}{\lambda_j})^k\to 0$ and $\mathbf{\Lambda}^k\mathbf{L}\mathbf{\Lambda}^{-k}\to \mathbf{I}$. Due to the uniqueness of the QR factorization, we also have $\mathbf{Q}_0\ldots\mathbf{Q}_k\to\mathbf{Q}$ and $\mathbf{R}_k\ldots\mathbf{R}_0\mathbf{U}^{-1}\mathbf{\Lambda}^{-k}\to \mathbf{I}$. Then the QR iterations can be formulated as:

$$(\mathbf{Q}_k^T \dots \mathbf{Q}_0^T) \mathbf{T}(\mathbf{Q}_0 \dots \mathbf{Q}_k) \rightarrow \mathbf{Q}^T \mathbf{T} \mathbf{Q} = \mathbf{\Lambda}$$
 (29)

As seen above, the QR iteration converges to the eigenvalue.

This theorem implies that the convergence speed of QR iterations is actually dependent on the adjacent eigenvalue ratio λ_i/λ_j for i>j.

A.3 Wilkinson Shift

The Wilkinson shift denotes extracting the two eigenvalues from the right bottom 2×2 block of a matrix and uses them as the shift coefficients. This can be also accomplished by Givens rotation. Consider the general form of the orthogonal transform by Givens rotation in Eq. (23). Setting the off-diagonal entries to zero leads to the linear equations:

$$\begin{cases} (x_1 - x_3)cs + x_2(c^2 - s^2) = 0\\ c^2 + s^2 = 1 \end{cases}$$
 (30)

Let we define two variables by:

$$m = \frac{x_1 - x_3}{2x_2}, \ n = \frac{s}{c} = \tan \theta$$
 (31)

Then Eq. (30) is equivalent to:

$$n^2 - 2mn - 1 = 0 (32)$$

The above equation has two roots that are defined by

$$n = m \pm \sqrt{1 + m^2} \tag{33}$$

We select the smaller one to ensure that the rotation angle θ is within 45 degrees. Then the entries of the rotation are given by:

$$c = \frac{1}{\sqrt{1+m^2}}, \ s = cn \tag{34}$$

The two eigenvalues are derived and used as the shifts.

A.4 Implicit Q Theorem

In the paper, we present the following implicit Q theorem without proof.

Theorem 3 (Implicit Q Theorem) Let **B** be an upper Hessenberg and only have positive elements on its first sub-diagonal. Assume there exists a unitary transform $\mathbf{Q}^H \mathbf{A} \mathbf{Q} = \mathbf{B}$. Then \mathbf{Q} and \mathbf{B} are uniquely determined by \mathbf{A} and the first column of \mathbf{Q} .

Now we give a short proof and illustrate why the theorem cannot be directly applied in our case.

Proof. Since the QR iteration is a unitary transform, we can write $\mathbf{Q}^H \mathbf{A} \mathbf{Q} = \mathbf{B}$ as:

$$\mathbf{AQ} = \mathbf{QB} \tag{35}$$

If we represent **Q** by a vector of columns $\mathbf{Q}=[\mathbf{q}_0,\ldots,\mathbf{q}_{n-1}]$, Eq. (35) can be re-written as:

$$\mathbf{A}[\mathbf{q}_0, \dots, \mathbf{q}_{n-1}] = [\mathbf{q}_0, \dots, \mathbf{q}_{n-1}]\mathbf{B}$$
(36)

Recall that **B** is a tri-diagonal matrix and only has non-zero entries at $b_{i-1,i}$, $b_{i,i}$, and $b_{i+1,i}$. Relying on this property, we have:

$$\mathbf{A}\mathbf{q}_{i+1} = b_{i-1,i}\mathbf{q}_{i-1} + b_{i,i}\mathbf{q}_{i} + b_{i+1,i}\mathbf{q}_{i+1}$$

$$\mathbf{q}_{i+1} = \frac{\mathbf{A}\mathbf{q}_{i+1} - b_{i-1,i}\mathbf{q}_{i-1} - b_{i,i}\mathbf{q}_{i}}{b_{i+1,i}}$$
(37)

Since **q** is orthogonal, *i.e.*, $\mathbf{q}^T\mathbf{q}=\mathbf{I}$, we have:

$$b_{i+1,i} = ||\mathbf{A}\mathbf{q}_{i+1} - b_{i-1,i}\mathbf{q}_{i-1} - b_{i,i}\mathbf{q}_{i}||_{2}$$

$$\mathbf{q}_{i+1} = \frac{\mathbf{A}\mathbf{q}_{i+1} - b_{i-1,i}\mathbf{q}_{i-1} - b_{i,i}\mathbf{q}_{i}}{||\mathbf{A}\mathbf{q}_{i+1} - b_{i-1,i}\mathbf{q}_{i-1} - b_{i,i}\mathbf{q}_{i}||_{2}}$$
(38)

As indicated above, each column of ${\bf Q}$ and the sub-diagonal entries of ${\bf B}$ can be uniquely computed by the previous columns.

This theorem presents an algorithm that could greatly simplify the QR iterations without explicit Givens rotations. However, as can be seen from Eq. (37), the theorem relies on the assumption that the sub-diagonal entry $b_{i+1,i}$ is nonzero. In our case, any Givens rotation aims at zeroing out the sub-diagonal entry $b_{i+1,i}$. As a consequence, $b_{i+1,i}$ are very small and even can be zero after rotation, which violates the assumption. This is more serious for batched matrices, as more matrices could amplify the probability. Directly applying the theorem could introduce large round-off errors and may cause data overflow.

Nonetheless, this theorem implies that the i-th column of \mathbf{Q} only depends on the previous two columns of \mathbf{Q} and \mathbf{B} , but not on the columns after i-th column. This shows that the i-th Givens rotation will only affect part of \mathbf{Q} . Therefore, we propose our economic eigenvector calculation to involve part of the matrix for each rotation.

B Experimental Settings

B.1 Implementation Details

All the source codes are implemented in Pytorch 1.7.0 with the self-contained CUDA wrapper. Older versions till 1.0.0 should be also compatible. We compare our method with the function TORCH.SVD, which calls the LAPACK's SVD gesdd routine that uses the divide-and-conquer strategy to solve the eigenvalue problem. Some other functions, such as TORCH.SYMEIG and TORCH.EIG, can be also adopted to perform the ED. However, we empirically found that they are not as numerically stable as TORCH.SVD and often cause the network to fail in converging. We thus only compare our method with the function TORCH.SVD. All the numerical tests are conducted on a workstation equipped with a GeForce GTX 1080Ti GPU and a 6-core Intel(R) Xeon(R) GPU @ 2.20GHz. The computational time is measured based on 10,000 randomly generated matrices. We note that if a low-level programming language (e.g., CUDA C++) is used to implement our algorithm, the speed might get further improved.

For our method, the threshold for the dimension reduction is set as 1e-5. Our process of batched Givens diagonalization lasts n iterations, where each iteration consists of two sequential QR iterations with Wilkinson shifts. We use the techniques implemented in [38] for the backward gradient computation.

B.2 Decorrelated BN

To perform the ZCA whitening, given the reshaped feature map $\mathbf{X} \in \mathbb{R}^{C \times BHW}$, the covaraince of the feature is first computed as:

$$\mathbf{A} = (\mathbf{X} - \mu)(\mathbf{X} - \mu)^T + \epsilon \mathbf{I}$$
(39)

where $\mathbf{A} \in \mathbb{R}^{C \times C}$, μ is the mean of \mathbf{X} , and ϵ is a small positive constant to ensure the positive-definitiveness of \mathbf{A} . Afterwards, the inverse square root of the covaraince is applied to whiten the feature:

$$\mathbf{X}_{whitened} = \mathbf{A}^{-\frac{1}{2}}\mathbf{X} \tag{40}$$

Compared with the BN, the whitened feature map $\mathbf{X}_{whitened}$ further eliminates the data correlation between the features. The statistics μ and $\mathbf{A}^{-\frac{1}{2}}$ during the training phase are stored and used in the inference stage.

In the practical implementation, one often split the feature X into multiple groups in the channel dimension and attain a mini-batch of matrices. This division allows each group to have own training statistics, which could improve the stability and generalization performance. This is particularly helpful in the regime of small batch sizes [23]. Let G denote the group numbers. Then the split can be formally defined by:

$$\mathbf{X} \in \mathbf{R}^{C \times BHW} \to \mathbf{X} \in \mathbf{R}^{\frac{C}{G} \times G \times BHW}$$
 (41)

The covariance is changed accordingly as:

$$\mathbf{A} \in \mathbf{R}^{C \times C} \to \mathbf{A} \in \mathbf{R}^{\frac{C}{G} \times G \times G} \tag{42}$$

As indicated above, the covariance becomes a mini-batch of matrices. The calculation raises the need of our batch-friendly ED algorithm.

As depicted in Fig. 8, we insert the decorrelated BN layer after the first convolution layer of the ResNet-18 architecture. For the calculation of backward gradients, we use the Taylor polynomial for gradient approximation [38, 46]. The degree of the Taylor polynomial is set to 9. For the other settings, we follow the experiments in [46].

B.3 Second-order Vision Transformer

Fig. 9 depicts the architectural overview of the second-order vision transformer. In the ordinary vision transformer [15], only the class token is used to output the class predictions, which results in the need of pre-training on ultra-large-scale

First Layer of ResNet-18

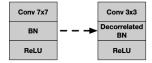


Fig. 8. The detailed architecture changes of ResNet-18 after replacing the BN layer with the ZCA whitening meta-layer. Following [46], we reduce both the kernel size and the strides of the first convolution layer. The rest blocks of the model are not modified.

datasets. In the second-order vision transformer, the covariance square root of the visual tokens are utilized to assist the classification task. The process can be formulated as:

$$y = FC(c) + FC((\mathbf{X}\mathbf{X}^T)^{\frac{1}{2}})$$
(43)

where c is the class token, \mathbf{X} denotes the visual token, and y is the final class predictions. Equipped with the covariance pooling layer, the So-ViT model is free of pre-training and can achieve comparable performances with CNNs even if trained from scratch.

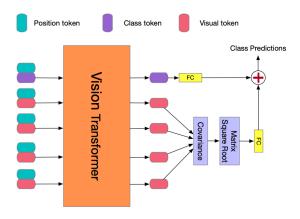


Fig. 9. Scheme of the second-order vision transformer [49]. The covariance square root of the visual token is utilized to assist the classification task, which removes the need of pre-training on ultra-large-scale datasets.

For training transformer architectures, people often use some advanced mixed-precision training techniques such as NVIDIA Apex. Due to the use of low-precision weights and data, these techniques can not only accelerate the training process, but also can reduce the risk of gradient explosion. However, as pointed out in [38], the low precision often poses a challenge to the SVD. As the SVD often needs double precision to attain the effective numerical representation of the eigenvalues, using a low precision (*i.e.*, float or half) can cause the network

to fail in converging. To avoid this issue, we first train the model using Newton-Schulz iteration that computes the approximate matrix square root for the first 50 epochs. Then we switch to the SVD or our Batched ED and continue the training. This hybrid approach can avoid the non-convergence at the beginning of training.

We set the spatial dimension of the visual tokens to 32×32 and 36×36 in our paper. The batch size is set as 768. For the other experimental settings, we follow [49].

B.4 Universal Style Transfer

Table 5. The LPIPS distance and the user preference (%) on each sub-set of the Artworks [27] dataset. We report the time consumption of the forward ED process that is conducted 10 times to exchange the style and content feature at different network depths.

Solver	Group	Size	Time (s)	LPIPS [50] (↑) Vangogh Monet Cezanne Ukiyoe			Preference (†)				
				Vangogh	Monet	Cezanne	Ukiyoe	Vangogh	Monet	Cezanne	Ukiyoe
SVD	64	256×4×4	3.146	0.5448	0.5317	0.6035	0.6306	44	47	49	53
Batched ED	04	250×4×4	0.089	0.5346	0.5027	0.6229	0.6589	52	49	45	45
SVD	99	32 128×8×8	2.306	0.5298	0.5127	0.5751	0.6713	44	47	50	50
Batched ED	32		0.257	0.5096	0.5258	0.6208	0.6239	55	45	48	47
SVD	1 16	16 64×16×16	1.973	0.4987	0.5257	0.5882	0.6630	41	48	45	51
Batched ED			0.876	0.5085	0.5151	0.6041	0.6498	57	43	49	42

Following [46], we adopt the WCT process in the network architecture proposed in [9] for the universal style transfer. Fig. 10 displays the overview of the model. The WCT performs successive whitening and coloring transform on the content and style feature. Consider the content feature $\mathbf{X}_c \in \mathbb{R}^{B \times C \times H \times W}$ and the style feature $\mathbf{X}_s \in \mathbb{R}^{B \times C \times H \times W}$. We first divide the features into groups and reshape them as:

$$\mathbf{X}_{c} \in \mathbf{R}^{B \times C \times H \times W} \to \mathbf{X}_{c} \in \mathbf{R}^{\frac{BC}{G} \times G \times HW}$$

$$\mathbf{X}_{c} \in \mathbf{R}^{B \times C \times H \times W} \to \mathbf{X}_{c} \in \mathbf{R}^{\frac{BC}{G} \times G \times HW}$$
(44)

where G denotes the group number. Subsequently, we remove the style information from the content feature as:

$$\mathbf{X}_{c}^{whitened} = \left((\mathbf{X}_{c} - \mu(\mathbf{X}_{c}))(\mathbf{X}_{c} - \mu(\mathbf{X}_{c}))^{T} \right)^{-\frac{1}{2}} \mathbf{X}_{c}$$
 (45)

Then we extract the desired style information from the style feature \mathbf{X}_s and transfer it to the whitened content feature:

$$\mathbf{X}_{c}^{colored} = \left((\mathbf{X}_{s} - \mu(\mathbf{X}_{s}))(\mathbf{X}_{s} - \mu(\mathbf{X}_{s}))^{T} \right)^{\frac{1}{2}} \mathbf{X}_{c}^{whitened}$$
 (46)

The resultant feature $\mathbf{X}_c^{colored}$ is compensated with the mean of style feature and combined with the original content feature:

$$\mathbf{X} = \alpha (\mathbf{X}_c^{colored} + \mu(\mathbf{X}_s)) + (1 - \alpha)\mathbf{X}_c \tag{47}$$

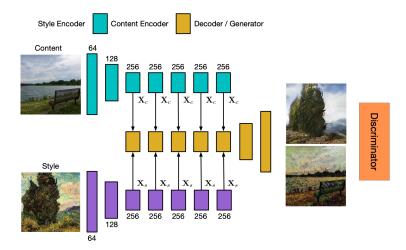


Fig. 10. The architecture overview of our model for neural style transfer. Two encoders take input of the style and content image respectively, and generate the multi-scale content/style features. A decoder is applied to absorb the feature and perform the WCT process at 5 different scales, which outputs a pair of images that exchange the styles. Finally, a discriminator is further adopted to tell apart the authenticity of the images.

where α is a weight bounded in [0,1] to control the strength of style transfer. Finally, we feed the resultant feature **X** into the decoder to generate the realistic image where the style is transferred.

For the loss functions, we follow [9] and use the cycle-consistent reconstruction loss in both the latent and the pixel space. The image is resized to the resolution of 216×216 before passing to the network, and the model is trained for 100,000 iterations. The batch size is set to 4.

Table 5 displays the detailed quantitative evaluation. As suggested in [32, 47], we use the metrics LPIPS score between each pair of transferred image and the content image as well as the user preference. For the user study, we randomly select 100 images from each dataset and ask 20 volunteers to vote for the image that characterizes more the style information. In certain cases where the volunteer thinks neither of the two generated images correctly carries the style, he/she can abstain and does not vote for any one.

C Detailed Analysis and Comparison

C.1 Speed Comparison against EIG

Though TORCH.EIG is not numerically stable, its speed is usually faster than TORCH.SVD. To ensure a fair comparison, we also compare our ED solver against TORCH.EIG in Table 6. Our method is more efficient in the regime of large batch sizes. When the batch size is small, our method also has comparable speed. The

Matrix Dim Batch Size	4	8	16	24
1	4/5	8/8	53/25	98/ 59
4	6/7	13/ 9	75/44	113/ 68
16	7 /10	18/ 10	88/ 69	146/92
64	8/40	24/49	90 /130	170 /210
256	9/160	26 /163	98 /219	191 /343
1024	a /610	28/625	117/7/0	270 /800

Table 6. Speed comparison against TORCH.EIG. The results (ms) are reported in the format of our BatchedED / TORCH.EIG.

time cost of our BatchedED grows cubically versus matrix dimensions, whereas the cost of TORCH.EIG drastically increases when the batch size increases. This demonstrates that our method is more batch-efficient, while TORCH.EIG/SVD is more dimension-efficient.

C.2 Error Evaluation

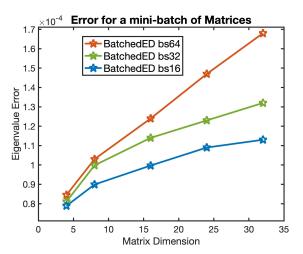


Fig. 11. The error of eigenvalue estimation $||\Lambda - \hat{\Lambda}||_F$ for a mini-batch of matrices where $\hat{\Lambda}$ denotes the ground truth eigenvalue computed by SVD, and Λ represents the eigenvalue of our Batched ED.

Fig. 11 displays the computation error of the eigenvalue for a mini-batch of matrices in different dimensions. When the batch size or matrix dimension increases, the error increases accordingly. However, the overall error is at an

acceptance level for (<2e-4). The computer vision experiments in the paper also demonstrate that such a small error will not affect the performance.

C.3 Memory Usage Comparison

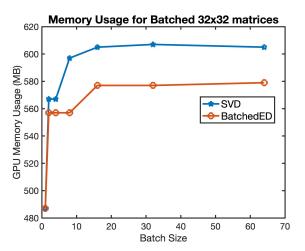


Fig. 12. The comparison of GPU memory for a mini-batch of 32×32 matrices.

Fig. 12 displays the GPU memory usage (MB) of performing the ED for a mini-batch of 32×32 matrices. For a single matrix, both methods consume almost the same memory. When the input scales to batched matrices, our Batched ED uses slightly less memory than the default LAPACK's SVD routine.

C.4 Average Reduction Times

As discussed in the paper, the speed of our Batched ED is greatly improved by the reduction times r for the matrix shrinkage. More specifically, the time complexity is reduced by O(-256n(1+r)) for deriving eigenvalues and by $O(-(2r+1)n^4)$ for eigenvectors. Since our double Wilkinson shifts guarantee that the last two diagonal entries converge to zero quickly, i.e., $\frac{\lambda_i - \mu}{\lambda_j - \mu} = \infty$, the matrix dimension is very likely to shrink by one every QR iteration or every other QR iteration. According to our observations, the average reduction times r is mainly in the range $\left[\frac{n}{3}, \frac{3n}{4}\right]$.

C.5 Why Batched ED Outperforms SVD

In some experiments of the paper, our Batched ED can even achieve slightly better performances than the SVD. We think it is related to the implementation technique and the data precision. Due to the computation of secular equations, the divide-and-conquer strategy used in the TORCH.SVD naturally requires a higher precision than the QR iterations of our method. Solving secular equations needs to perform the rational osculatory interpolation and it is more likely to trigger round-off errors when using a low data precision. In the regime of single-precision or half-precision, our QR-based Batched ED algorithm might have a slight advantage.

C.6 Limitation of Our Method

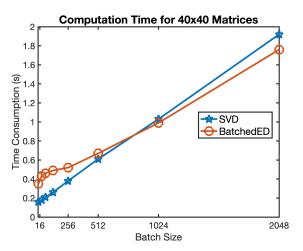


Fig. 13. Time consumption for a mini-batch of 40×40 matrices.

As discussed in the paper, our Batched ED fully utilizes the power of GPUs and can be very fast against varying batch sizes for small and medium matrices. However, one accompanying limitation is the cubic time cost $O(n^3)$ to the matrix dimension, which constrains our method to be applicable only to small-sized and moderate-sized matrices. A interesting question is to investigate where the critical point of our method against SVD lies, *i.e.*, from what matrix dimension on, our method is no longer competitive against the SVD. Fig. 13 presents the time comparison for a mini-batch of matrices in the size 40×40 . Our method only has the marginal advantage over the SVD when the batch size is larger than 1024. This concludes that when the matrix dimension is larger than 40 and the batch size is small, TORCH.SVD can be a drop-in replacement for our method.

A similar question is when the batch size is fixed, in what range of matrix dimensions, our method holds a speed advantage over SVD. From the numerical test in the paper, we already know that when the matrix dimension is smaller than 24, our Batched ED is consistently faster than the SVD. So the critical point should be larger than matrix dimension 24. Fig. 14 compares the time

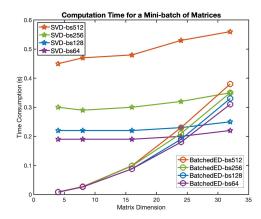


Fig. 14. Time consumption of our Batched ED against SVD for a mini-batch of matrices in different batch sizes and matrix dimensions. Lines with the same batch sizes are in the same color.

consumption for batched matrices in batch sizes 512, 256, 128, and 64. The intersections of each pair of lines locate the critical points. For batch sizes 64 and 128, the intersection points are at matrix dimensions 26 and 28, respectively. When the batch size is 256, our method has a faster speed for matrix dimensions less than 33. As for matrices in batch size 512, the intersection might be around 40. Larger batch sizes would make our Batched ED more advantageous.