Lecture Notes in Computer Science

7016

Commenced Publication in 1973
Founding and Former Series Editors:
Gerhard Goos, Juris Hartmanis, and Jan van Leeuwen

Editorial Board

David Hutchison

Lancaster University, UK

Takeo Kanade

Carnegie Mellon University, Pittsburgh, PA, USA

Josef Kittler

University of Surrey, Guildford, UK

Jon M. Kleinberg

Cornell University, Ithaca, NY, USA

Alfred Kobsa

University of California, Irvine, CA, USA

Friedemann Mattern

ETH Zurich. Switzerland

John C. Mitchell

Stanford University, CA, USA

Moni Naor

Weizmann Institute of Science, Rehovot, Israel

Oscar Nierstrasz

University of Bern, Switzerland

C. Pandu Rangan

Indian Institute of Technology, Madras, India

Bernhard Steffen

TU Dortmund University, Germany

Madhu Sudan

Microsoft Research, Cambridge, MA, USA

Demetri Terzopoulos

University of California, Los Angeles, CA, USA

Doug Tygar

University of California, Berkeley, CA, USA

Gerhard Weikum

Max Planck Institute for Informatics, Saarbruecken, Germany

Yang Xiang Alfredo Cuzzocrea Michael Hobbs Wanlei Zhou (Eds.)

Algorithms and Architectures for Parallel Processing

11th International Conference, ICA3PP 2011 Melbourne, Australia, October 24-26, 2011 Proceedings, Part I



Volume Editors

Yang Xiang Wanlei Zhou Deakin University, School of Information Technology Melbourne Burwood Campus, 221 Burwood Highway

E-mail: {yang, wanlei}@deakin.edu.au

Burwood, VIC 3125, Australia

Alfredo Cuzzocrea ICAR-CNR and University of Calabria Via P. Bucci 41 C, 87036 Rende (CS), Italy E-mail: cuzzocrea@si.deis.unical.it

Michael Hobbs
Deakin University, School of Information Technology
Geelong Waurn Ponds Campus, Pigdons Road
Geelong, VIC 3217, Australia
E-mail: mick@deakin.edu.au

ISSN 0302-9743 e-ISSN 1611-3349 ISBN 978-3-642-24649-4 e-ISBN 978-3-642-24650-0 DOI 10.1007/978-3-642-24650-0 Springer Heidelberg Dordrecht London New York

Library of Congress Control Number: 2011937820

CR Subject Classification (1998): F.2, H.4, D.2, I.2, G.2, H.3

LNCS Sublibrary: SL 1 – Theoretical Computer Science and General Issues

© Springer-Verlag Berlin Heidelberg 2011

This work is subject to copyright. All rights are reserved, whether the whole or part of the material is concerned, specifically the rights of translation, reprinting, re-use of illustrations, recitation, broadcasting, reproduction on microfilms or in any other way, and storage in data banks. Duplication of this publication or parts thereof is permitted only under the provisions of the German Copyright Law of September 9, 1965, in its current version, and permission for use must always be obtained from Springer. Violations are liable to prosecution under the German Copyright Law.

The use of general descriptive names, registered names, trademarks, etc. in this publication does not imply, even in the absence of a specific statement, that such names are exempt from the relevant protective laws and regulations and therefore free for general use.

Typesetting: Camera-ready by author, data conversion by Scientific Publishing Services, Chennai, India

Printed on acid-free paper

Springer is part of Springer Science+Business Media (www.springer.com)

Message from the ICA3PP 2011 Program Chairs

A warm welcome to the 11th International Conference on Algorithms and Ar chitectures for Parallel Processing (ICA3PP 2011) and to Melbourne, Australia.

ICA3PP 2011 is the 11th in this series of conferences that started in 1995 and is devoted to algorithms and architectures for parallel processing. ICA3PP is now recognized as the main regular event focusing on the many dimensions of parallel algorithms and architectures, encompassing fundamental theoretical approaches, practical experimental results, and commercial components and systems. As applications of computing systems have permeated every aspects of daily life, the power of computing systems has become increasingly critical. On top of these motivations, ICA3PP 2011 provides a widely-known forum for researchers and practitioners from countries around the world to exchange ideas for improving the computation power of computing systems.

In response to the ICA3PP 2011 call for papers, we received 85 submissions from 33 different countries. These papers were evaluated on the basis of their originality, significance, correctness, relevance, and technical quality. Each paper was reviewed by at least three members of the Program Committee. Based on these evaluations, 24 regular papers and 17 short papers were selected for presentation at the conference, representing an acceptance rate of 28.2% for regular papers and 20% for short papers.

We would like to thank the Program Committee members and additional reviewers from all around the world for their efforts in reviewing the large number of papers. We are grateful to all the associated Conference/Workshop Chairs for their dedication and professionalism. We would like to extend our sincere thanks to the ICA3PP Steering Committee Chairs, Prof. Wanlei Zhou and Prof. Yi Pan, and to the General Chairs, Prof. Andrzej Goscinski and Prof. Peter Brezany. They provided us with invaluable guidance throughout the process of paper selection and program organization. We thank Georgi Cahill, the Conference Secretary, for her professional organization. We also thank Yu Wang and Sheng Wen for their help on completing the final proceedings.

Last but not least, we would also like to take this opportunity to thank all the authors for their submissions to ICA3PP 2011 and the associated symposium/workshops. Many of you have travelled some distance to participate in the conference.

Welcome to Melbourne and enjoy!

October 2011

Yang Xiang Alfredo Cuzzocrea Michael Hobbs

Message from the ICA3PP 2011 General Chairs

Welcome to the beautiful and 'World's Most Livable City' – Melbourne. We are privileged and delighted to welcome you to the 11th International Conference on Algorithms and Architectures for Parallel Processing (ICA3PP 2011).

Following the traditions of the previous successful ICA3PP conferences held in Hangzhou, Brisbane, Singapore, Melbourne, Hong Kong, Beijing, Cyprus, Taipei and Busan, this year ICA3PP 2011 is held in Melbourne, Australia. The objective of ICA3PP 2011 is to bring together researchers and practitioners from academia, industry and government to advance the theories and technologies in parallel and distributed computing. ICA3PP 2011 focuses on two broad areas of parallel and distributed computing, i.e., architectures, algorithms and networks, and systems and applications. The conference of ICA3PP 2011 is organized by Deakin University, Australia.

In addition to the ICA3PP 2011 main conference, one symposium and three workshops are being held together with ICA3PP 2011. They are:

- 1. 2011 International Symposium on Advances of Distributed Computing and Networking (ADCN 2011)
- 2. The 4th IEEE International Workshop on Internet and Distributed Computing Systems (IDCS 2011)
- 3. The 1st IEEE International Workshop on Parallel Architectures for Bioinformatics Systems (HardBio 2011)
- 4. The 3rd International Workshop on Multicore and Multithreaded Architectures and Algorithms (M2A2 2011)

We sincerely thank the many people who have helped in organizing ICA3PP 2011 and the associated symposium/workshops. We would like to thank the Program Chairs, Yang Xiang, Alfredo Cuzzocrea and Michael Hobbs, for their leadership in providing the excellent technical program.

We wish you a very enjoyable and rewarding experience at ICA3PP 2011 in Melbourne!

October 2011

Andrzej Goscinski Peter Brezany

ICA3PP 2011 Committees

General Chairs

Andrzej Goscinski Deakin University, Australia Peter Brezany University of Vienna, Austria

Program Chairs

Yang Xiang Deakin University, Australia

Alfredo Cuzzocrea ICAR-CNR and University of Calabria, Italy

Michael Hobbs Deakin University, Australia

Steering Committee Chairs

Wanlei Zhou Deakin University, Australia Yi Pan Georgia State University, USA

Workshop Chairs

Wen Tao Zhu Chinese Academy of Sciences, China Muhammad Khurram Khan King Saud University, Saudi Arabia

Publicity Chairs

Ali Shahrabi Glasgow Caledonian University, UK

Haixin Duan Tsinghua University, China

Publication Chairs

Meikang Qiu University of Kentucky, USA

Program Committee

Bechini Alessio University of Pisa, Italy

Giuseppe Amato ISTI-CNR, Italy

Cosimo Anglano Università del Piemonte Orientale, Italy Novella Bartolini Univ. of Rome La Sapienza, Italy

Ladjel Bellatreche ENSMA, France

Ateet Bhalla NRI Institute of Information Science and

Technology, India

Angelo Brayner University of Fortaleza, Brazil Massimo Cafaro University of Salento, Italy Jiannong Cao
Andre Carvalho
Tania Cerquitelli
Ruay-Shiung Chang
Yue-Shan Chang
Tzung-Shi Chen
Zizhong Chen
Carmela Comito
Raphaël Couturier
Gennaro Della Vecchia

Der-Rong Din

Susan Donohue
Shantanu Dutt
Todd Eavis
Giuditta Franco
Karl Fuerlinger
Jerry Gao
Jinzhu Gao
Jose Daniel Garcia
Irene Garrigos
Alex Gerbessiotis
Harald Gjermundrod
Houcine Hassan

Pilar Herero Ching-Hsien Hsu Tsung-Chuan Huang Yo-Ping Huang

George Karypis

Muhammad Khurram Khan

Soo-Kyun Kim Changhoon Lee Deok-Gyu Lee Laurent Lefevre Daniele Lezzi Keqin Li

Keqin Li Keqiu Li Kai Lin Pangfeng Liu

Alberto Marchetti-Spaccamela

Tomas Margalef Amiya Nayak Hong Kong Polytechnic University, Hong Kong

Universidade de Sao Paulo, Brazil

Politecnico di Torino, Italy

National Dong Hwa University, Taiwan National Taipei University, Taiwan National University of Tainan, Taiwan

Colorado School of Mines, USA University of Calabria, Italy

University of Franche Comté, France

ICAR-CNR, Italy

National Changhua University of Education,

Taiwan

The College of New Jersey, USA University of Illinois at Chicago, USA

Concordia University, Canada University of Verona, Italy University of California, USA San Jose State University, USA University of the Pacific, USA

University Carlos III of Madrid, Spain

University of Alicante, Spain

New Jersey Institute of Technology, USA

University of Nicosia, Cyprus

Univ. Politécnica de Valencia, Spain Univ. Politécnica de Madrid, Spain Chung Hua University, Taiwan

National Sun Yat-sen University, Taiwan National Taipei University of Technology,

Taiwan

University of Minnesota, USA King Saud University, Saudi Arabia

PaiChai University, Korea Hanshin University, Korea

ETRI, Korea INRIA, France

Barcelona Supercomputing Center, Spain State University of New York at New Paltz,

USA

SAP Research, France

Dalian University of Technology, China Dalian University of Technology, China National Taiwan University, Taiwan

Sapienza U. of Rome, Italy

Universitat Autonoma de Barcelona, Spain

University of Ottawa, Canada

Leonardo B. Oliveira

Marion Oswald Deng Pan

Apostolos Papadopoulos

Dana Petcu Rubem Pereira Kleanthis Psarris

Pedro Pereira Rodrigues Casiano Rodriguez-Leon

Marcel C. Rosu

Giovanni Maria Sacco Erich Schikuta Martin Schulz

Seetharami Seelam

Edwin Sha Rahul Shah

Giandomenico Spezzano

Peter Strazdins Domenico Talia

Uwe Tangen Jichiang Tsai

Chen Wang

Cho-Li Wang Xiaofang Wang

Qishi Wu

Fatos Xhafa

Zheng Yan Chao-Tung Yang

Zhiwen Yu Eiko Yoneki

Sotirios G. Ziavras

Roger Zimmermann

Unicamp, Brazil

Hungarian Academy of Sciences, Hungary Florida International University, USA Aristotle Univ. of Thessaloniki, Greece West University of Timisoara, Romania Liverpool John Moores University, UK

The University of Texas at San Antonio, USA

University of Porto, Portugal Universidad de La Laguna, Spain

IBM, USA

Universitá di Torino, Italy University of Vienna, Austria

Lawrence Livermore National Laboratory, USA IBM T.J. Watson Research Center, USA

University of Texas at Dallas, USA Louisiana State University, USA

ICAR-CNR, Italy

The Australian National University, Australia

Universitá della Calabria, Italy Ruhr-Universität Bochum, Germany National Chung Hsing University, Taiwan

CSIRO ICT Centre, Australia

The University of Hong Kong, Hong Kong

Villanova University, USA University of Memphis, USA

Polytechnic University of Catalonia, Spain

Nokia Research Center, Finland Tunghai University, Taiwan

Northwestern Polytechnical University, China University of Cambridge Computer Laboratory,

UK

NJIT, USA

National University of Singapore, Singapore

ICA3PP 2011 Additional Reviewers

Atif, Muhammad

Cai. Jie

Canonico, Massimo Chan, Philip Ding, Chong

Dionysiou, Ioanna

Eldefrawy, Mohamed Estévez, José Ignacio Figueiredo, Thomaz

Gouvea, Conrado P.L.

Guazzone, Marco

Jin, Chao Khan, Bilal Macias, Mario

Miranda-Valladares, Gara

Mochetti, Karina Mou. Duxing Printista, Marcela

XII ICA3PP 2011 Committees

Rodríguez Martínez, Diego Ruj, Sushmita Segredo Gonzalez, Eduardo Manuel Segura, Carlos Song, Huaguang Tiskin, Alexander Tsai, Pei-Wei Vlad, Ioan Zhu, Kai Zola, Matteo

Table of Contents – Part I

ICA3PP 2011 Keynote	
Keynote: Assertion Based Parallel Debugging	1
ICA3PP 2011 Regular Papers	
Secure and Energy-Efficient Data Aggregation with Malicious Aggregator Identification in Wireless Sensor Networks	2
Dynamic Data Race Detection for Correlated Variables	14
Improving the Parallel Schnorr-Euchner LLL Algorithm	27
Distributed Mining of Constrained Frequent Sets from Uncertain Data	40
Set-to-Set Disjoint-Paths Routing in Recursive Dual-Net	54
Redflag: A Framework for Analysis of Kernel-Level Concurrency Justin Seyster, Prabakar Radhakrishnan, Samriti Katoch, Abhinav Duggal, Scott D. Stoller, and Erez Zadok	66
Exploiting Parallelism in the H.264 Deblocking Filter by Operation Reordering	80
Tsung-Hsi Weng, Yi-Ting Wang, and Chung-Ping Chung	00
Compiler Support for Concurrency Synchronization	93
Fault-Tolerant Routing Based on Approximate Directed Routable Probabilities for Hypercubes	106
Finding a Hamiltonian Cycle in a Hierarchical Dual-Net with Base Network of p -Ary q-Cube	117

Adaptive Resource Remapping through Live Migration of Virtual Machines	129
Muhammad Atif and Peter Strazdins	129
LUTS: A Lightweight User-Level Transaction Scheduler	144
Verification of Partitioning and Allocation Techniques on Teradata DBMS	158
Memory Performance and SPEC OpenMP Scalability on Quad-Socket x86_64 Systems	170
Anonymous Communication over Invisible Mix Rings	182
Game-Based Distributed Resource Allocation in Horizontal Dynamic Cloud Federation Platform	194
Stream Management within the CloudMiner	206
Security Architecture for Virtual Machines	218
Fast and Accurate Similarity Searching of Biopolymer Sequences with GPU and CUDA	230
Read Invisibility, Virtual World Consistency and Probabilistic Permissiveness are Compatible	244
Parallel Implementations of Gusfield's Cut Tree Algorithm Jaime Cohen, Luiz A. Rodrigues, Fabiano Silva, Renato Carmo, André L.P. Guedes, and Elias P. Duarte Jr.	258
Efficient Parallel Implementations of Controlled Optimization of Traffic Phases	270

Virtual Clusters

William Voorsluys, Saurabh Kumar Garg, and Rajkumar Buyya

395

XVI Table of Contents – Part I

A Principled Approach to Grid Middleware: Status Report on the	400
Minimum Intrusion Grid	409
Performance Analysis of Preemption-Aware Scheduling in Multi-cluster Grid Environments	419
Mohsen Amini Salehi, Bahman Javadi, and Rajkumar Buyya	
Performance Evaluation of Open Source Seismic Data Processing Packages	433
Izzatdin A. Aziz, Andrzej M. Goscinski, and Michael M. Hobbs	
Reputation-Based Resource Allocation in Market-Oriented Distributed Systems	443
Cooperation-Based Trust Model and Its Application in Network Security Management	453
Performance Evaluation of the Three-Dimensional Finite-Difference Time-Domain(FDTD) Method on Fermi Architecture GPUs	460
The Probability Model of Peer-to-Peer Botnet Propagation Yini Wang, Sheng Wen, Wei Zhou, Wanlei Zhou, and Yang Xiang	470
A Parallelism Extended Approach for the Enumeration of Orthogonal Arrays	483
Author Index	495

Table of Contents – Part II

ADCN 2011 Papers
Lightweight Transactional Arrays for Read-Dominated Workloads
Massively Parallel Identification of Intersection Points for GPGPU Ray Tracing
Cascading Multi-way Bounded Wait Timer Management for Moody and Autonomous Systems
World-Wide Distributed Multiple Replications in Parallel for Quantitative Sequential Simulation
Comparison of Three Parallel Point-Multiplication Algorithms on Conic Curves
Extending Synchronization Constructs in OpenMP to Exploit Pipeline Parallelism on Heterogeneous Multi-core
Generic Parallel Genetic Algorithm Framework for Protein Optimisation
A Survey on Privacy Problems and Solutions for VANET Based on Network Model
Scheduling Tasks and Communications on a Hierarchical System with Message Contention
Spiking Neural P System Simulations on a High Performance GPU Platform

SpotMPI: A Framework for Auction-Based HPC Computing Using Amazon Spot Instances	109
Investigating the Scalability of OpenFOAM for the Solution of Transport Equations and Large Eddy Simulations	121
Shibboleth and Community Authorization Services: Enabling Role-Based Grid Access	131
A Secure Internet Voting Scheme	141
A Hybrid Graphical Password Based System	153
Privacy Threat Analysis of Social Network Data	165
IDCS 2011 Papers	
Distributed Mechanism for Protecting Resources in a Newly Emerged Digital Ecosystem Technology	175
Reservation-Based Charging Service for Electric Vehicles	186
Intelligent Ubiquitous Sensor Network for Agricultural and Livestock Farms	196
Queue-Based Adaptive Duty Cycle Control for Wireless Sensor Networks	205
Experimental Evaluation of a Failure Detection Service Based on a Gossip Strategy	215
On the Performance of MPI-OpenMP on a 12 Nodes Multi-core Cluster	225

Table of Contents – Part II	XIX
A Protocol for Discovering Content Adaptation Services	235
Securing RFID Systems from SQLIA	245
Modeling QoS Parameters of VoIP Traffic with Multifractal and Markov Models	255
Hybrid Feature Selection for Phishing Email Detection	266
M2A2 2011 Papers	
On the Use of Multiplanes on a 2D Mesh Network-on-Chip	276
A Minimal Average Accessing Time Scheduler for Multicore Processors	287
Fast Software Implementation of AES-CCM on Multiprocessors Jung Ho Yoo	300
A TCM-Enabled Access Control Scheme	312
Binary Addition Chain on EREW PRAM	321
A Portable Infrastructure Supporting Global Scheduling of Embedded Real-Time Applications on Asymmetric MPSoCs	331
Emotional Contribution Process Implementations on Parallel Processors	343
A Cluster Computer Performance Predictor for Memory Scheduling Mónica Serrano, Julio Sahuquillo, Houcine Hassan, Salvador Petit, and José Duato	353

HardBio 2011 Papers

Reconfigurable Hardware Computing for Accelerating Protein Folding	
Simulations Using the Harmony Search Algorithm and the 3D-HP-Side	
Chain Model	363
César Manuel Vargas Benítez, Marlon Scalabrin,	
Heitor Silvério Lopes, and Carlos R. Erig Lima	
Clustering Nodes in Large-Scale Biological Networks Using External	
Memory Algorithms	375
Ahmed Shamsul Arefin, Mario Inostroza-Ponta, Luke Mathieson,	
Regina Berretta, and Pablo Moscato	
Reconfigurable Hardware to Radionuclide Identification Using	
Subtractive Clustering	387
Marcos Santana Farias, Nadia Nedjah, and	
Luiza de Macedo Mourelle	
A Parallel Architecture for DNA Matching	399
Edgar J. Garcia Neto Segundo, Nadia Nedjah, and	
Luiza de Macedo Mourelle	
Author Index	409