Lecture Notes in Computer Science

Commenced Publication in 1973 Founding and Former Series Editors: Gerhard Goos, Juris Hartmanis, and Jan van Leeuwen

Editorial Board

David Hutchison Lancaster University, UK Takeo Kanade Carnegie Mellon University, Pittsburgh, PA, USA Josef Kittler University of Surrey, Guildford, UK Jon M. Kleinberg Cornell University, Ithaca, NY, USA Alfred Kobsa University of California, Irvine, CA, USA Friedemann Mattern ETH Zurich, Switzerland John C. Mitchell Stanford University, CA, USA Moni Naor Weizmann Institute of Science, Rehovot, Israel Oscar Nierstrasz University of Bern, Switzerland C. Pandu Rangan Indian Institute of Technology, Madras, India Bernhard Steffen TU Dortmund University, Germany Madhu Sudan Microsoft Research, Cambridge, MA, USA Demetri Terzopoulos University of California, Los Angeles, CA, USA Doug Tygar University of California, Berkeley, CA, USA Gerhard Weikum Max Planck Institute for Informatics, Saarbruecken, Germany Ernie Cohen Andrey Rybalchenko (Eds.)

Verified Software: Theories, Tools, Experiments

5th International Conference, VSTTE 2013 Menlo Park, CA, USA, May 17-19, 2013 Revised Selected Papers



Volume Editors

Ernie Cohen 107 Hewett Road, Wyncote, PA 19095, USA E-mail: erniecohen1@gmail.com

Andrey Rybalchenko Microsoft Research Cambridge 21 Station Road, CB1 2FB Cambridge, UK E-mail: rybal@microsoft.com and Technical University Munich Boltzmannstr. 3, 85748 Munich, Germany E-mail: rybal@in.tum.de

ISSN 0302-9743 e-ISSN 1611-3349 ISBN 978-3-642-54107-0 e-ISBN 978-3-642-54108-7 DOI 10.1007/978-3-642-54108-7 Springer Heidelberg New York Dordrecht London

Library of Congress Control Number: 2013957812

CR Subject Classification (1998): D.2.4, D.2, F.3, D.3, D.1, C.2, F.4

LNCS Sublibrary: SL 2 - Programming and Software Engineering

© Springer-Verlag Berlin Heidelberg 2014

This work is subject to copyright. All rights are reserved by the Publisher, whether the whole or part of the material is concerned, specifically the rights of translation, reprinting, reuse of illustrations, recitation, broadcasting, reproduction on microfilms or in any other physical way, and transmission or information storage and retrieval, electronic adaptation, computer software, or by similar or dissimilar methodology now known or hereafter developed. Exempted from this legal reservation are brief excerpts in connection with reviews or scholarly analysis or material supplied specifically for the purpose of being entered and executed on a computer system, for exclusive use by the purchaser of the work. Duplication of this publication or parts thereof is permitted only under the provisions of the Copyright Law of the Publisher's location, in its current version, and permission for use must always be obtained from Springer. Permissions for use may be obtained through RightsLink at the Copyright Clearance Center. Violations are liable to prosecution under the respective Copyright Law.

The use of general descriptive names, registered names, trademarks, service marks, etc. in this publication does not imply, even in the absence of a specific statement, that such names are exempt from the relevant protective laws and regulations and therefore free for general use.

While the advice and information in this book are believed to be true and accurate at the date of publication, neither the authors nor the editors nor the publisher can accept any legal responsibility for any errors or omissions that may be made. The publisher makes no warranty, express or implied, with respect to the material contained herein.

Typesetting: Camera-ready by author, data conversion by Scientific Publishing Services, Chennai, India

Printed on acid-free paper

Springer is part of Springer Science+Business Media (www.springer.com)

Preface

This volume contains the papers presented at the 5th International Conference on Verified Software: Theories, Tool and Experiments (VSTTE), which was held in Menlo Park, USA, during May 17–19, 2013. Historically, the conference originated from the Verified Software Initiative (VSI), a cooperative, international initiative directed at the scientific challenges of large-scale software verification. The inaugral VSTTE conference was held at ETH Zurich in October 2005. Starting in 2008, the conference became a biennial event, VSTTE 2008 was held in Toronto, VSTTE 2010 was held in Edinburgh, and VSTTE 2012 was held in Philadelphia, which changed this year.

The goal of the VSTTE conference is to advance the state of the art through the interaction of theory development, tool evolution, and experimental validation.

VSTTE 2013 is especially interested in submissions describing large-scale verification efforts that involve collaboration, theory unification, tool integration, and formalized domain knowledge. We welcome papers describing novel experiments and case studies evaluating verification techniques and technologies. Topics of interest include education, requirements modeling, specification languages, specification/verification case-studies, formal calculi, software design methods, automatic code generation, refinement methodologies, compositional analysis, verification tools (e.g., static analysis, dynamic analysis, model checking, theorem proving, satisfiability), tool integration, benchmarks, challenge problems, and integrated verification environments.

There were 35 submissions. Each submission was reviewed by at least two, and on average 2.7, Program Committee members. The committee decided to accept 17 papers. The program also includes three invited talks, by Alex Aiken (Stanford University), Nikhil Swamy (Microsoft Research), and Andre Platzer (CMU), as well as an invited tutorial by Sandrine Blazy (University of Rennes 1).

We would like to thank the invited speakers, all submitting authors, the Steering Committee, the conference chair, the publicity chair, the external reviewers, and especially the Program Committee, who put a lot of hard work into reviewing and selecting the papers that appear in this volume.

We thank Andrei Voronkov for the access to EasyChair and Springer.

VSTTE 2013 was supported in part by NSF funding CISE award 1033105.

November 2013

Ernie Cohen Andrey Rybalchenko

Organization

Program Committee

Josh Berdine Ahmed Bouajiani Marsha Chechik Ernie Cohen Jean-Christophe Filliatre Silvio Ghilardi Aarti Gupta Arie Gurfinkel Jifeng He Andrew Ireland Ranjit Jhala Cliff Jones Rajeev Joshi Gerwin Klein Daniel Kroening Gary Leavens Xavier Leroy Zhiming Liu Pete Manolios Tiziana Margaria David Monniaux Peter Mueller David Naumann Aditva Nori Peter O'Hearn Matthew Parkinson Wolfgang Paul Andreas Podelski Andrey Rybalchenko Natarajan Shankar Zhong Shao Willem Visser Thomas Wies Jim Woodcock Kwangkeun Yi Pamela Zave Lenore Zuck

Microsoft Research University of Paris 7. France Toronto University, Canada Microsoft CNRS, LRI, Inria, France University of Milan, Italy NEC Labs CMU SEI East China Normal University, China Heriot-Watt University, UK UC San Diego, USA Newcastle University, UK NASA JPL. USA NICTA, Australia Oxford University, UK University of Central Florida, USA Inria. France UNI-IIST Northeastern University University of Potsdam, Germany VERIMAG, France ETHZ Stevens Institute of Technology, USA Microsoft Research UCL, UK Microsoft Research University of Saarland, Germany University of Freiburg, Germany TUM SRI International, Singapore Yale University, USA University of Stellenbosch, South Africa NYU University of York, UK Seoul National University, South Korea AT&T Labs University of Illinois at Chicago, USA

Additional Reviewers

Chamarthi, Harsh Raju Chen, Zhenbang Christ, Jürgen David, Cristina Faber, Johannes Joshi, Saurabh Majumdar, Rupak Nipkow, Tobias Papavasileiou, Vasilis Popeea, Corneliu Qamar, Nafees Tautschnig, Michael Invited Talks

Using Learning Techniques in Invariant Inference

Alex Aiken

Stanford University

Abstract. Arguably the hardest problem in automatic program verification is designing appropriate techniques for discovering loop invariants (or, more generally, recursive procedures). Certainly, if invariants are known, the rest of the verification problem becomes easier. This talk presents a family of invariant inference techniques based on using test cases to generate an underapproximation of program behavior and then using learning algorithms to generalize the underapproximation to an invariant. These techniques are simpler, much more efficient, and appear to be more robust than previous approaches to the problem. If time permits, some open problems will also be discussed.

F*: Certified Correctness for Higher-Order Stateful Programs

Nikhil Swamy

Microsoft Research

Abstract. Abstract: F^* is an ML-like programming language being developed at Microsoft Research. It has a type system based on dependent types and a typechecker that makes use of an SMT solver to discharge proof obligations. The type system is expressive enough to express functional correctness properties of typical, higher-order stateful programs. We have used F^* in a variety of settings, including in the verification of security protocol implementations; as a source language for secure web-browser extensions; as an intermediate verification language for JavaScript code; to verify the correctness of compilers; as a relational logic for probabilistic programs; and as a proof assistant in which to carry out programming language metatheory. We have also used F^* to program the core typechecker of F^* itself and have verified that it is correct. By bootstrapping this process using the Coq proof assistant, we obtain a theorem that guarantees the existence of a proof certificate for typechecked programs.

I will present a brief overview of the F* project, drawing on the examples just mentioned to illustrate the features of the F* language and certification system.

For more about F*, visit http://research.microsoft.com/fstar.

How to Explain Cyber-Physical Systems to Your Verifier

André Platzer

CMU

Abstract. Despite the theoretical undecidability of program verification, practical verification tools have made impressive advances. How can we take verification to the next level and use it to verify programs in cyber-physical systems (CPSs), which combine computer programs with the dynamics of physical processes. Cars, aircraft, and robots are prime examples where this matters, because they move physically in space in a way that is determined by discrete computerized control algorithms. Because of their direct impact on humans, verification for CPSs is even more important than it already is for programs.

This talk describes how formal verification can be lifted to one of the most prominent models of CPS called hybrid systems, i.e. systems with interacting discrete and continuous dynamics. It presents the theoretical and practical foundations of hybrid systems verification. The talk shows a systematic approach that is based on differential dynamic logic comes with a compositional proof technique for hybrid systems and differential equations. This approach is implemented in the verification tool KeYmaera and has been used successfully for verifying properties of aircraft, railway, car control, autonomous robotics, and surgical robotics applications.

A Tutorial on the CompCert Verified Compiler

Sandrine Blazy

University of Rennes 1

Abstract. Compilers are complicated pieces of software that sometimes contain bugs causing wrong executable code to be silently generated from correct source programs. In turn, this possibility of compiler-introduced bugs diminishes the assurance that can be obtained by applying formal methods to source code. This talk gives an overview of the CompCert project: an ongoing experiment in developing and formally proving correct a realistic, moderately-optimizing compiler from a large subset of C to popular assembly languages. The correctness proof, mechanized using the Coq proof assistant, establishes that the generated assembly code behaves exactly as prescribed by the semantic of the C source, eliminating all possibilities of compiler-introduced bugs and generating unprecedented confidence in this compiler. For more about CompCert, please visit http://compcert.inria.fr.

Table of Contents

| Classifying and Solving Horn Clauses for Verification Philipp Rümmer, Hossein Hojjat, and Viktor Kuncak | 1 |
|--|-----|
| Static Analysis of Programs with Imprecise Probabilistic Inputs Assale Adje, Olivier Bouissou, Jean Goubault-Larrecq, Eric Goubault, and Sylvie Putot | 22 |
| Effect Analysis for Programs with Callbacks Etienne Kneuss, Viktor Kuncak, and Philippe Suter | 48 |
| Compositional Network Mobility Pamela Zave and Jennifer Rexford | 68 |
| Parallel Bounded Verification of Alloy Models by TranScoping Nicolás Rosner, Carlos Gustavo López Pombo, Nazareno Aguirre, Ali Jaoua, Ali Mili, and Marcelo F. Frias | 88 |
| Extending the Theory of Arrays: memset, memcpy, and Beyond Stephan Falke, Florian Merz, and Carsten Sinz | 108 |
| An Improved Unrolling-Based Decision Procedure for Algebraic Data Types <i>Tuan-Hung Pham and Michael W. Whalen</i> | 129 |
| Program Checking with Less Hassle Julian Tschannen, Carlo A. Furia, Martin Nordio, and Bertrand Meyer | 149 |
| Verified Calculations K. Rustan M. Leino and Nadia Polikarpova | 170 |
| Preserving User Proofs across Specification Changes François Bobot, Jean-Christophe Filliâtre, Claude Marché, Guillaume Melquiond, and Andrei Paskevich | 191 |
| An Automatic Encoding from VeriFast Predicates into Implicit Dynamic Frames Daniel Jost and Alexander J. Summers | 202 |
| Automated Code Proofs on a Formal Model of the X86 Shilpi Goel and Warren A. Hunt Jr. | 222 |

| Verification of a Virtual Filesystem Switch Gidon Ernst, Gerhard Schellhorn, Dominik Haneberg, Jörg Pfähler, and Wolfgang Reif | 242 |
|--|-----|
| Verifying Chinese Train Control System under a Combined Scenario by Theorem Proving Liang Zou, Jidong Lv, Shuling Wang, Naijun Zhan, Tao Tang, Lei Yuan, and Yu Liu | 262 |
| Formal Verification of Loop Bound Estimation for WCET Analysis Sandrine Blazy, André Maroneze, and David Pichardie | 281 |
| Result Certification of Static Program Analysers with Automated Theorem Provers Frédéric Besson, Pierre-Emmanuel Cornilleau, and Thomas Jensen | 304 |
| A Formally Verified Generic Branching Algorithm for Global Optimization Anthony Narkawicz and César Muñoz | 326 |
| Author Index | 345 |