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VLSI Algorithms and Architectures

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FOREWORD

The papers in this volume were presented at the Aegean Workshop on Computing: VLSI Algorithms and Architectures (AWOC 88), organized by the Computer Technology Institute in Patras in cooperation with ACM, EATCS, IEEE and the General Secretariat of Research and Technology (Ministry of Industry, Energy & Technology of Greece). They were selected from 119 abstracts submitted in response to the program committee's call for papers and to additional invitations from John Reif.

AWOC 88 took place in Corfu, Greece, June 28-July 1, 1988. AWOC 88 is the third meeting in the International Workshop on Parallel Computing & VLSI series; the first meeting took place in Amalfi, Italy, 1984, and the second at Loutraki, Greece, 1986.

F. Preparata and J.H. Reif were the program chairmen of AWOC 88.

The program committee consisted of:

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U. Vishkin (ISRAEL)

The very enjoyable local arrangements were made by:

F. Makedon
P. Spirakis, and
T. Papatheodorou

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