CORRECTION

Correction to: Design and Implementation of Power-Efficient Cryptography Scheme Using a Novel Multiplication Technique

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Correction to: Wireless Personal Communications
https://doi.org/10.1007/s11277-023-10427-y

In this article the affiliation details for author D. Ajitha were incorrectly given as 'Department of Electronics and Communication Engineering, School of Computer Science and Engineering (SCOPE), VIT, Vellore, Tamil Nadu, India' but should have been 'Department of Software Systems, School of Computer Science and Engineering (SCOPE), Vellore Institute of Technology, Vellore, Tamil Nadu, India'.

The original article has been corrected.

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The online version of the original article can be found at https://doi.org/10.1007/s11277-023-10427-y

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