## **RETRACTION NOTE**



## Retraction Note: Model for estimating power dissipation along the interconnect length in single on-chip topology

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The Editor in Chief has retracted this article because an investigation by the publisher found evidence of attempts to subvert the peer review process and evidence to suggest that authorship in this article had been offered for sale prior to its

publication. Zaffar Ahmed Shaikh, Alexey Mikhaylov, Sergey Glushkov and Dmitry Gura disagree to this retraction. Maksim Zaikov has not responded to any correspondence from the Editor about this retraction.

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