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Preface

This book contains the papers presented at the 13th International Workshop on Field Programmable Logic and Applications (FPL) held on September 1–3, 2003. The conference was hosted by the Institute for Systems and Computer Engineering-Research and Development of Lisbon (INESC-ID) and the Department of Electrical and Computer Engineering of the IST-Technical University of Lisbon, Portugal.

The FPL series of conferences was founded in 1991 at Oxford University (UK), and has been held annually since: in Oxford (3 times), Vienna, Prague, Darmstadt, London, Tallinn, Glasgow, Villach, Belfast and Montpellier. It brings together academic researchers, industrial experts, users and newcomers in an informal, welcoming atmosphere that encourages productive exchange of ideas and knowledge between delegates.

Exciting advances in field programmable logic show no sign of slowing down. New grounds have been broken in architectures, design techniques, run-time reconfiguration, and applications of field programmable devices in several different areas. Many of these innovations are reported in this volume.

The size of FPL conferences has grown significantly over the years. FPL in 2002 saw 214 papers submitted, representing an increase of 83% when compared to the year before. The interest and support for FPL in the programmable logic community continued this year with 216 papers submitted. The technical program was assembled from 90 selected regular papers and 56 posters, resulting in this volume of proceedings. The program also included three invited plenary keynote presentations from LSI Logic, Xilinx and Cadence, and three industrial tutorials from Altera, Mentor Graphics and Dafca.

Due to the inclusive tradition of the conference, FPL continues to attract submissions from all over the world. The accepted contributions were submitted by researchers from 32 different countries:

USA	42	Belgium	6	Brazil	2	Estonia	1
Spain	33	Netherlands	6	Canada	2	Norway	1
UK	29	Mexico	5	Hungary	2	India	1
Germany	14	Greece	4	Iran	2	Slovakia	1
Japan	13	Poland	4	Korea	2	Slovenia	1
Portugal	12	Switzerland	4	Romania	2		
Italy	9	Australia	3	Singapore	2		
Czech Rep.	8	Ireland	3	Austria	1		
France	7	China	2	Egypt	1		

We would like to thank all the authors for submitting their first versions of the papers and the final versions of the accepted papers. We also gratefully acknowledge the reviewing work done by the Program Committee members and many additional reviewers who contributed their time and expertise towards the compilation of this volume. The members of our Program Committee and all other reviewers are listed on the following pages. We are particularly pleased that of the 1029 reviews sought, 95% were completed.

We would like to thank QuickSilver Technology for their sponsorship of the Michal Servit Award, Celoxica for sponsoring the official FPL website www.fpl.org, Xilinx and Synplicity for their early support of the conference, and Coreworks for help in registration processing. We are indebted to Richard van de Stadt, the author of CyberChair. This excellent free software made our task of managing the submission and reviewing process much easier. We are grateful for the help and advice received from Wayne Luk and Horácio Neto. In addition, we acknowledge the help of the following research students from Imperial College London in checking the integrity of the manuscripts: Christos Bouganis, Wim Melis, Gareth Morris, Andy Royal, Pete Sedcole, Nalin Sidahao, and Theerayod Wiangtong.

We are grateful to Springer-Verlag, particularly Alfred Hofmann and Anna Kramer, for their work in publishing this book.

June 2003

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