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Correction to: A Reduced Hardware ISI and Mismatch Shaping DEM Decoder

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Correction to: Circuits Syst Signal Process
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The original version of the article unfortunately contained an error in figure. The presentation of Fig. 2 is incorrect. Figure 1 was erroneously duplicated and published as Fig. 2 in the article. The corrected Fig. 2 is given below:

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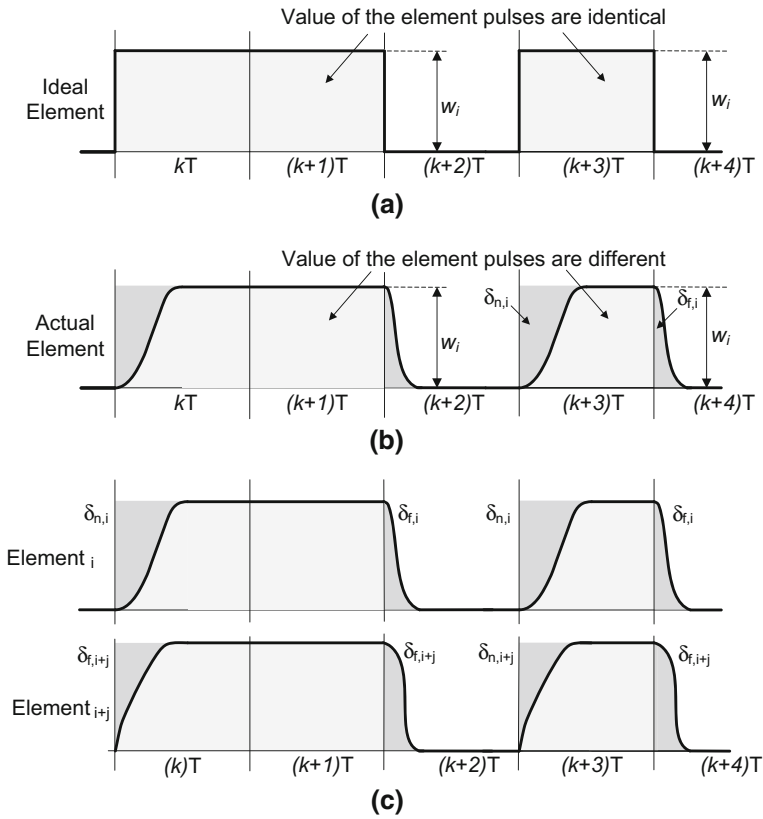[1] University of Limerick, Limerick, Ireland

Fig. 2 **a** Ideal pulse, **b** actual pulse $\delta_{n,i} \neq \delta_{f,i}$, **c** variation in rise and fall errors between elements $\delta_{n,i} \neq \delta_{n,i+j}$ $\delta_{f,i} \neq \delta_{f,i+j}$