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# Practical Collision Attacks against Round-Reduced SHA-3 ${ }^{\star}$ 

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#### Abstract

The Keccak hash function is the winner of the SHA-3 competition (2008-2012) and became the SHA-3 standard of NIST in 2015. In this paper, we focus on practical collision attacks against round-reduced SHA-3 and some Keccak variants. Following the framework developed by Dinur et al. at FSE 2012 where 4-round collisions were found by combining 3 -round differential trails and 1-round connectors, we extend the connectors to up to three rounds and hence achieve collision attacks for up to 6 rounds. The extension is possible thanks to the large degree of freedom of the wide internal state. By linearizing S-boxes of the first round, the problem of finding solutions of 2-round connectors is converted to that of solving a system of linear equations. When linearization is applied to the first two rounds, 3 -round connectors become possible. However, due to the quick reduction in the degree of freedom caused by linearization, the connector succeeds only when the 3 -round differential trails satisfy some additional conditions. We develop dedicated strategies for searching differential trails and find that such special differential trails indeed exist. To summarize, we obtain the first real collisions on six instances, including three round-reduced instances of SHA-3, namely 5 -round SHAKE128, SHA3-224, and SHA3-256, and three instances of KECCak contest, namely $\operatorname{Keccak}[1440,160,5,160]$, $\operatorname{Keccak}[640,160,5,160]$


[^0]and KECCAK[1440, $160,6,160$ ], improving the number of practically attacked rounds by two. It is remarked that the work here is still far from threatening the security of the full 24-round SHA-3 family.

Keywords: Cryptanalysis, hash function, SHA-3, KECCAK, collision, linearization, differential, GPU.

## 1 Introduction

The KEccak hash function [3] was a submission to the SHA-3 competition [22] in 2008. After four years of intensive evaluation, it was selected as the winner of the competition in 2012. It was then formally standardized in 2015 by the National Institute of Standards and Technology (NIST) of the U.S. as Secure Hash Algorithm-3 (SHA-3) [27]. The SHA-3 family has four instances with fixed digest lengths, namely, SHA3-224, SHA3-256, SHA3-384 and SHA3-512, which correspond to $\operatorname{Keccak}[c] \triangleq \operatorname{KEccak}[r=1600-c, c]$ where $c \in\{448,512,768,1024\}$. The SHA-3 family also has two eXtendable-Output Functions (XOFs) SHAKE128 and SHAKE256. To promote cryptanalysis of the Keccak hash function, the Keccak designers proposed variants with lower security levels in the Keccak Crunchy Crypto Collision and Pre-image Contest (the Keccak contest for short) [2], for which the digest lengths are 80 and 160 bits for pre-image and collision resistance, respectively. For clarity, these variants are denoted by $\operatorname{KEccak}\left[r, c, n_{r}, d\right]$ with parameters $\left\{r, c, n_{r}, d\right\}$ to be specified later.

Since the Keccak hash function was made public in 2008, there have been intensive cryptanalysis from the public research community $[1,9,10,11,12$, $13,14,16,17,21]$. In this paper, we mainly investigate collision attacks on the Keccak hash function, in particular, those with practical attack complexities. In collision attacks, the aim is to find two distinct messages which lead to the same hash digest. Up to date, the best practical collision attacks against KEC$\operatorname{CAK}[448] / \mathrm{KECCAK}^{2} 512$ ] is of 4 (out of 24) rounds found by Dinur et al. [10] in 2012 and later furnished in the journal version [12]. These 4-round collisions were generated by combining a 1 -round connector and a 3 -round differential trail. The same authors presented practical collision attacks on 3-round KECCAK[768]/KECCAK[1024], and theoretical collision attacks (with complexities beyond the reach of practical resources) on 5/4-round KECCAK[512]/KECCAK[768] in [11] using internal differentials. For the KEccak contest, the best solutions reached up to 4 rounds in $[12,18]$. To the best of our knowledge, there exist neither practical collision attacks against 5-round KECCAK[448]/KECcak[512]/ KECCAK[768]/KECCAK[1024], nor for any 5-round instances of the KECCAK contest.

Our contributions. Following the framework of Dinur et al. [10], we develop a new algebraic and differential hybrid method to launch collision attacks on KECCAK family and present seven real collisions on six KECCAK variants, including 5 -round SHAKE128, 5 -round SHA3-224, 5 -round SHA3-256, and 5 -round as well as 6 -round instances of the KEccak collision contest.

These results are obtained by combining a differential trail and a connector which links the initial state of KECCAK to the input difference of the trail. Previously, connectors of KECCAK covered one round [10], while in this paper we propose new connectors of up to three rounds. The improvements on connectors are possible mainly due to S-box linearizations of KECCAK, i.e., the linearization of the nonlinear layer of KECCAK. Specifically, we linearize the first round for constructing 2-round connectors for KECCAK. When the degree of freedom of a 2-round connector is sufficiently large, it can be extended to three rounds by further linearizing (part of) the second round.

In this paper, two types of S-box linearizations are proposed, i.e., full linearization and non-full linearization, which are achieved based on several crucial observations. First, we observe that the Keccak S-box can be re-expressed with linear transformations when the input is restricted to certain affine subspaces. Daemen et al. [4, 8] and Dinur et al. [10] have already noted that when the input and output differences of the KECCAK S-box are fixed, the solution set forms an affine subspace of dimension up to 3 . In this work, we show that the maximum subspaces allowing linearization of the S-box is of dimension 2 and any 2 -dimensional affine subspace allows S-box linearization. For affine subspaces of dimension 3, six 2-dimensional affine subspaces out of it could allow the linearization. However, we do not have to fully linearize all S-boxes of the first round if only partial output bits of them need to be linear for constructing 2-round connectors. We further observe that non-full S-box linearizations help to save some degrees of freedom. More specifically, when linearizing part (not all) of the output bits of a non-active S-box (defined by whether there is any difference in the input and output), at most 2 degrees of freedom are consumed by specifying 2 binary linear equations over the input bits. For an active S-box whose entry in the differential distribution table (DDT) is 8,4 out of 5 output bits are already linear when the input is chosen from the solution set. Note that to restrict the input to the solution set for such an S-box, 2 binary linear equations should be specified over the input bits. Therefore, for both non-active and active S -boxes, two or fewer degrees of freedom may be enough to linearize part of the output bits, while fully linearizing an S-box consumes at least 3 degrees of freedom.

With these properties in mind, we linearize S-boxes of the first round such that their output bits which affect the input difference of the trail that our connector tries to link to become linear. Therefore the first round function of the KEccak permutation is transformed into a (partial) linear one. Combining with an inversion method of the S-box layer of the second round, we convert the problem of finding 2-round connectors into that of solving a system of linear equations. Solving the system produces sufficiently many solutions so that at least one pair of them will follow the differential trails over the last rounds. To extend the connector to 3 rounds, we first construct a 2 -round connector where the first round is fully linearized by enforcing full S-box linearizations. Then upon the 2 -round connector, 3-round connectors are obtained through constructing 2 -round connectors over the second and third round, where nonfull S-box linearizations are applied to the second round.

A side effect of S-box linearization is a quick reduction in the degree of freedom which in turn determines the existence of 2 -round or 3 -round connectors. To address this problem, we aim to find differential trails that impose the least possible conditions on the connectors. We design dedicated strategies to find suitable differential trails of 3 and 4 rounds. Our GPU implementations confirmed the correctness of this idea, and successfully found desirable differential trails for our collisions attacks.

Results obtained in this paper are listed in Table 1, compared with the best previous practical collision attacks and related theoretical attacks. In brief, we obtain actual collisions on three 5 -round instances of SHA-3, i.e., SHAKE128, and SHA3-224, SHA3-256, and three instances of KECCAK contest. The number of practically attacked rounds of KECCAK instances now is increased to 6 . For the instance of contest $\operatorname{KEccak}[1440,160,6,160]$, an inner collision [3] on the last 160 -bit of the output state is mounted to construct collisions from messages of any block length.

Table 1: Summary of our attacks and comparison with related works

| Target $[r, c, d]$ | $n_{r}$ | Complexity | Reference |
| :---: | :---: | :---: | :---: |
| KECCAK[1024] | 3 | Practical | [11] |
| Keccak[768] | 3 | Practical |  |
| Keccak[768] | 4 | $2^{147}$ |  |
| Keccak[512] | 5 | $2^{115}$ |  |
| Keccak[512] | 4 | Practical | [10, 12] |
| Keccak[448] | 4 | Practical |  |
| Keccak[1440, 160, 160] | 4 | Practical |  |
| SHAKE128 | 5 | Practical ${ }^{\dagger}$ | Sect. 6 |
| SHA3-224 | 5 | Practical |  |
| SHA3-256 | 5 | Practical |  |
| Keccak[1440, 160, 160] | 5 | Practical |  |
| Keccak [ 640, 160, 160] | 5 | Practical |  |
| Kеccak [1440, 160, 160] | 6 | Practical |  |
| Keccak $\left[1440,160,160^{-1}\right]^{\ddagger}$ | 6 | Practical |  |

${ }^{\dagger}$ For exact complexities, refer to Table 6.
$\ddagger 160^{-1}$ corresponds to the last 160 -bit collision rather than the general digest collision.

Organization. The rest of the paper is organized as follows. In Section 2, a brief introduction to the SHA-3 hash function is given. Section 3 first presents the notations used in this paper and subsequently provides a synopsis of the collision attacks against SHA-3 and KECCAK instances. The properties of S-box linearization and non-full linearization are illustrated in Section 4, followed by detailed
explanations over how the 2 -round and 3 -round connectors are constructed. Section 5 outlines the search for differential trails as well as the highly efficient GPU implementations of Keccak. In Section 6, the experimental results of collision attacks are given. We conclude the paper in Section 7. Details of the differential trails and actual collisions are postponed to the Appendix.

## 2 Description of SHA-3

### 2.1 The Sponge Function

The sponge construction is a framework for constructing hash functions from permutations, as depicted in Fig. 1. The construction consists of three components: an underlying $b$-bit permutation $f$, a parameter $r$ called rate and a padding rule. The value $c=b-r$ is called capacity. A hash function following this construction takes in a message $M$ as input and outputs a digest of $d$ bits. Given a message $M$, it is first padded and split into $r$-bit blocks. The $b$-bit state is initialized to be all zeros. The sponge construction then proceeds in two phases. In the absorbing phase, each message block is XORed into the first $r$ bits of the state, followed by application of the permutation $f$. This process is repeated until all message blocks are processed. Then, the sponge construction switches to the squeezing phase. In this phase, each iteration returns the first $r$ bits of the state as (part of) the output and then applies the permutation $f$ to the current state. This repeats until all $d$ bits digest are obtained.


Figure 1: Sponge Construction [3]

### 2.2 The Keccak Hash Function

The Keccak hash function follows the sponge construction. The underlying permutation of KECCAK is chosen from a set of seven KECCAK- $f$ permutations, denoted by KECcaK- $f[b]$, where $b \in\{25,50,100,200,400,800,1600\}$ is the width
of the permutation in bits. The default Keccak employs Keccak- $f$ [1600]. The 1600 -bit state can be viewed as a 3 -dimensional $5 \times 5 \times 64$ array of bits, denoted as $A[5][5][64]$. Let $0 \leq i, j<5$, and $0 \leq k<64, A[i][j][k]$ represents one bit of the state at position $(i, j, k)$. Defined by the designers of KEccak, $A[*][j][k]$ is called a row, $A[i][*][k]$ a column, and $A[i][j][*]$ a lane. Note the lane size is $\frac{b}{25}$ which varies according to $b$.

The KECCAK- $f$ permutation has $12+2 l$ rounds $\left(l=\log _{2} \frac{b}{25}\right)$, each of which consists of five mappings $R=\iota \circ \chi \circ \pi \circ \rho \circ \theta$.
$\theta: \quad A[i][j][k] \leftarrow A[i][j][k]+\sum_{j^{\prime}=0}^{4} A[i-1]\left[j^{\prime}\right][k]+\sum_{j^{\prime}=0}^{4} A[i+1]\left[j^{\prime}\right][k-1]$.
$\rho: \quad A[i][j] \leftarrow A[i][j] \lll T(i, j)$, where $T(i, j)$ s are constants.
$\pi: \quad A[j][2 i+3 j] \leftarrow A[i][j]$.
$\chi: \quad A[i][j][k] \leftarrow A[i][j][k]+(A[i+1][j][k]+1) \cdot A[i+2][j][k]$.
$\iota: \quad A[0][0] \leftarrow A[0][0]+R C_{i_{r}}$, where $R C_{i_{r}}$ is the $i_{r}$-th round constant.
Here, the additions and multiplications between the terms are in GF(2). As $\iota$ plays no essential role in our attacks, we will ignore it in the rest of the paper unless otherwise stated.

### 2.3 Instances of Keccak and SHA-3

There are four instances of Keccak, denoted by $\operatorname{Keccak}[c] \triangleq \operatorname{Keccak}[r=$ $1600-c, c]$, where the capacity $c \in\{448,512,768,1024\}$ and the digest length $d$ is half of $c$. To promote cryptanalysis against Keccak, the Keccak design team also proposed variants with lower security levels in the Keccak contest, where $b \in\{1600,800,400,200\},(d=80, c=160)$ for pre-image contest and ( $d=160, c=160$ ) for collision contest. In this paper, we follow the designers' notation $\operatorname{Keccak}\left[r, c, n_{r}, d\right]$ for the instances in the contest, where $r$ is the rate, $c=b-r$ is the capacity, $d$ is the digest size, and $n_{r}$ is the number of rounds the underlying permutation KECCAK- $f$ is reduced to.

The Keccak hash function uses the multi-rate padding rule which appends to the original message $M$ a single bit 1 followed by the minimum number of bits 0 and a single bit 1 such that the length of the resulted message is a multiple of the block length $r$. Namely, the padded message $\bar{M}$ is $M \| 10^{*} 1$.

The SHA-3 standard adopts the four KECCAK instances and names them SHAЗ-224, SHA3-256, SHA3-384 and SHA3-512, respectively. In these four instances of SHA -3 , the message is appended ' 01 ' first. After that, the multi-rate padding is applied. The SHA-3 standard also contains two extendable-output functions named SHAKE128 and SHAKE256, which are defined from two instances of KECCAK with the capacity $c$ being 256 and 512 respectively and the digest of any length. For SHAKE, a four-bit suffix ' 1111 ' is added to the original message $M$ before applying the multi-rate padding.

## 3 Notations and Overview

### 3.1 Notations

We summarize the major notations to be used in this paper here.

| c | Capacity of a sponge function |
| :---: | :---: |
| $r$ | Rate of a sponge function |
| $b$ | Width of a KECCAK permutation in bits, $b=r+c$ |
| $d$ | Length of the digest in bits |
| $d^{\dashv}$ | Length of collision on the last bits |
| $p$ | Number of fixed bits in the initial state due to padding |
| $n_{r}$ | Number of rounds |
| $\theta, \rho, \pi, \chi, \iota$ | The five mappings that comprise a round. A subscript $i$ denotes the mapping at the $i$-th round, e.g., $\chi_{i}$ denotes the $\chi$ layer at the $i$-th round for $i=0,1,2, \cdots$. |
| $L$ | Composition of $\theta, \rho, \pi$ and its inverse denoted by $L^{-1}$ |
| $R C_{i}$ | Round constant for the $i$-th round, $i=0,1,2$, |
| $\mathrm{R}^{i}(\cdot)$ | Keccak permutation reduced to the first $i$ rounds |
| $\mathrm{S}(\cdot)$ | 5-bit S-box operating on each row of Keccak state |
| $\delta_{\text {in }}, \delta_{\text {out }}$ | 5 -bit input and output differences of an S-box, |
| DDT | Differential distribution table, and $\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{o u t}\right)=\mid\{x: \mathrm{S}(x)+\mathrm{S}(x+$ $\left.\left.\delta_{\text {in }}\right)=\delta_{\text {out }}\right\} \mid$, where $\|\cdot\|$ denotes the size of a set. |
| $\alpha_{i}$ | Input difference of the $i$-th round function, $i=0,1,2, \cdots$ |
| $\beta_{i}$ | Input difference of $\chi$ in the $i$-th round, $i=0,1,2, \cdots$ |
| $w_{i}$ | Weight of the $i$-th round, $w_{i}, i=0,1,2, \cdots$ |
| DF | Degree of freedom of the solution space of connectors |
| $\bar{M}$ | Padded message of $M$. Note that $\bar{M}$ is of one block in our attacks. |
| $M_{1} \\| M_{2}$ | Concatenation of strings $M_{1}$ and $M_{2}$ |

### 3.2 Overview of the Attack

In this subsection, we give an overview of our collision attacks. Following the framework by Dinur et al. [10], as well as many other collision attacks utilizing differential trails, our collision attacks consist of two parts, i.e., a high probability differential trail and a connector linking the differential trail with the initial state, as depicted in Fig. 2. Let $\Delta S_{I}$ and $\Delta S_{O}$ denote the input and output differences of the differential trail, respectively. A connector covering $n_{r_{1}}$ rounds produces message pairs $\left(M, M^{\prime}\right)$ such that

$$
\mathrm{R}^{n_{r_{1}}}\left(\bar{M} \| 0^{c}\right)+\mathrm{R}^{n_{r_{1}}}\left(\overline{M^{\prime}} \| 0^{c}\right)=\Delta S_{I}
$$

always holds, i.e., the difference after $n_{r_{1}}$ rounds is always $\Delta S_{I}$. The differential trail is then fulfilled probabilistically with many such message pairs, and collisions can be found if the first $d$ bits of $\Delta S_{O}$ are zero.

Given an $n_{r_{2}}$-round differential of probability $2^{-w}$, our $\left(n_{r_{1}}+n_{r_{2}}\right)$-round collision attack proceeds in two stages:


Figure 2: Overview of $\left(n_{r_{1}}+n_{r_{2}}\right)$-round collision attacks

- Stage 1 - Connecting stage: Construct an $n_{r_{1}}$-round connector and get a subspace of messages bypassing the first $n_{r_{1}}$ rounds.
- Stage 2 - Brute-force searching stage: By brute force, find a colliding pair following the $n_{r_{2}}$-round differential trail from the subspace found in stage 1. The time complexity of this stage is $2^{w}$.

The brute-force searching stage is simple, though it may be time-consuming. Therefore, the core steps of the attack are finding good differentials and constructing connectors. In [10], Dinur et al. explored a method, which they call target difference algorithm, to construct 1-round connectors, namely, to find message pairs $\left(M, M^{\prime}\right)$ such that the output difference after one round permutation is $\Delta S_{I}$. In the next section, we show an algebraic method to extend this connector to two and three rounds, followed by details of the differential trail search in Section 5.

Without further specification, we assume in this paper the messages used are of one block after padding. To fulfil the KEccak padding rule, one needs to fix the last bit of the padded message to be " 1 ", hence the first $r-1$ bits of the state are under the full control of the attacker through the message bits, and the last $c$ bits of the state are fixed to zero as in the initial state specified by KECCAK. When applied to SHA3- $d, d \in\{224,256,384,512\}$ (resp. SHAKE), there are $r-4$ (resp. $r-6$ ) free bits under control, by setting the last 4 (resp. 6) bits of the padded message to be '1110' (resp. '111111') so to be compatible with the specific padding rule. For convenience, the number of extra fixed bits is denoted by $p$, and only $r-p$ bits are under control. Overall, the constraints of $n_{r_{1}}$-connectors are that the last $c+p$ bits of the initial state are fixed, and that the output difference after $n_{r_{1}}$ rounds is given and fixed (this is determined by the differential trail to be used). We are to utilize the degree of freedom from the first $r-p$ bits of the initial state to find solutions efficiently. As we are aiming at low complexity attacks, finding solutions of connectors should be practical. For
the same reason, the probability of the differential trail should be sufficiently high.

## 4 S-box Linearization and Connectors

We observe that the number of degrees of freedom for KECCAK instances is $r-p$ which is large for instances like Keccak [1440, 160, 5, 160]. One can then choose some message subsets with special properties such that the starting difference of a given trail is fulfilled deterministically. Since we intend to extend the connector to more than one round, it is natural to consider linearizing the nonlinear mapping $\chi$ of the first round by exploiting degrees of freedom, i.e., the expression of each S-box in the first round can be re-written as a linear transformation when the input is restricted to certain subsets. Once $\chi$ is linearized, the entire first round becomes linear and then 2-round connectors are possible by applying the core ideas of Dinur et al.'s target difference algorithm [10] to the second round.

In this section, we elaborate on techniques for linearizing the Keccak S-box, both fully and partially, based on which 2-round and 3-round connectors are achieved.

### 4.1 S-box Linearization

It is obvious that the Keccak S-box is non-linear when the entire $2^{5}$-sized input space is considered. However, affine subspaces of size up to 4 , as to be shown below, could be found so that the S-box can be linearized. Note that the S-box is the only nonlinear mapping of the Keccak round function. Hence, the entire round function becomes linear when all S-boxes are restricted to such subspaces. Formally, we define:
Definition 1 (Linearizable affine subspace). Linearizable affine subspaces are affine input subspaces on which $S$-box substitution is equivalent to a linear transformation. If $V$ is a linearizable affine subspace of an $S$-box operation $\mathrm{S}(\cdot)$, then $\forall x \in V, \mathrm{~S}(x)=A \cdot x+b$, where $A$ is a $5 \times 5$ matrix and $b$ is a 5 -bit constant vector.

For example, when input is restricted to the affine subspace $\{00000,00001,00100$, $00101\}(\{00,01,04,05\}$ in hex $)$, where the bits are written as $x_{4} x_{3} x_{2} x_{1} x_{0}$, the corresponding output set of the KECCAK S-box is $\{00000,01001,00101,01100\}$ ( $\{00,09,05,00\}$ in hex), and the expression of the S-box can be re-written as a linear transformation:

$$
y=\left(\begin{array}{lllll}
1 & 0 & 1 & 0 & 0 \\
0 & 1 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 0 \\
1 & 0 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 1
\end{array}\right) \cdot x
$$

where $x$ and $y$ are bit vector representations of input and output values of the KEccak S-box with $x_{0}$ at the top. By rotation symmetry, four more linearizable affine subspaces can be deduced from the one above.

Exhaustive search for the linearizable affine subspaces of the Keccak S-box shows:

Observation 1 Out of the entire 5-dimensional input space,
a. there are totally 80 2-dimensional linearizable affine subspaces, as listed in Table 7 in Appendix A.
b. there does not exist any linearizable affine subspace with dimension 3 or more.

For completeness, any 1-dimensional subspace is always linearizable.
Since the affine subspaces are to be used together with differential trails, we are especially interested in linearizable affine subspaces with fixed input and output differences, which is more relevant with the differential distribution table (DDT) of the S-box. We observe:

Observation 2 Given a 5-bit input difference $\delta_{\text {in }}$ and a 5-bit output difference $\delta_{\text {out }}$ such that $\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right) \neq 0$, i.e., the solution set $V=\left\{x: \mathrm{S}(x)+\mathrm{S}\left(x+\delta_{\text {in }}\right)=\right.$ $\left.\delta_{\text {out }}\right\}$ is not empty, we have
a. if $\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=2$ or 4 , then $V$ is a linearizable affine subspace.
b. if $\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=8$, then there are six 2-dimensional subsets $V_{i} \subset V, i=$ $0,1, \cdots, 5$ such that $V_{i}(i=0,1, \cdots, 5)$ are linearizable affine subspaces.

It is interesting to note the 2-dimensional linearizable affine subspaces obtained from the analysis of DDT cover all the 80 cases in Observation 1. It is already noted in [15] there is one-to-one correspondence between linearizable affine subspaces and entries with value 2 or 4 in DDT. As for the DDT entries of value 8 , there are 6 choices of 2 -dimensional linearizable affine subspaces. As an example, the 3 -dimensional affine subspace corresponding to $\operatorname{DDT}(01,01)$, i.e., with both input and output differences being 01 , is $\{10,11,14,15,18,19,1 \mathrm{C}, 1 \mathrm{D}\}$ and the six 2 dimensional linearizable affine subspaces from it are

$$
\begin{aligned}
& \{10,11,14,15\},\{10,11,1 \mathrm{C}, 1 \mathrm{D}\},\{14,15,18,19\}, \\
& \{10,11,18,19\},\{18,19,1 \mathrm{C}, 1 \mathrm{D}\},\{14,15,1 \mathrm{C}, 1 \mathrm{D}\} .
\end{aligned}
$$

When projected to the whole KEccak state, the direct product of affine subspaces of each individual S-box form affine subspaces of the entire state with larger dimensions. In other words, when all the S-boxes in the round function are linearized, the entire round function becomes linear.

### 4.2 Non-full S-box Linearization

Suppose we are to construct a connector of two rounds. Given the input difference $\Delta S_{I}$ of the differential trail, i.e., the output difference of the connector $\alpha_{2}$, it may be the case that some output bits of the S-boxes in the first round of the connector do not influence $\Delta S_{I}$. The S-boxes with such output bits do not need full linearizations. That is, it is enough to restrict the input to certain subsets
such that only the output bits of the first round affecting $\Delta S_{I}$ are to be expressed with a linear relation with input bits. Such linearization is called non-full $S$-box linearization and may contribute to a lower consumption of degree of freedom, as to be shown in Observation 3 and 4 below.

Before presenting the two observations, let us introduce some notations. Let the input and output value of $\chi_{0}$ (the $\chi$ mapping of the first round) be $x$ and $y$ respectively. Let $u=\left(u_{0}, u_{1}, \cdots, u_{b-1}\right)$ be a flag vector where $u_{i}=1(0 \leq i<b)$ if $y_{i}$ influences $\Delta S_{I}$, otherwise $u_{i}=0$. Let $U=\left(U_{0}, U_{1}, \cdots, U_{b / 5-1}\right)$ where $U_{i}=u_{5 i} u_{5 i+1} u_{5 i+2} u_{5 i+3} u_{5 i+4}, 0 \leq i<b / 5$. For the $i$-th S-box of $\chi_{0}$, if $U_{i}$ is not 00000 , aka. some bits of the corresponding S-box will influence $\Delta S_{I}$, this S-box should be fully or partially linearized. The value of $u$ is determined by $\beta_{1}$ and $\alpha_{2}$ $\left(\Delta S_{I}\right)$, which will be further illustrated in the next subsection. In this subsection, we suppose $u$ is already known and focus on non-full S-box linearizations.

Observation 3 For a non-active KEccak $S$-box, when $U_{i}$ is not 11111,

```
a. if }\mp@subsup{U}{i}{}=00000, it does not require any linearization
b. if }\mp@subsup{U}{i}{}\in{00001,00010,00100,01000,10000,00011,00110,01100,11000,10
    01}, at least 1 degree of freedom is consumed to linearize the output
    bit(s) of the S-box marked by }\mp@subsup{U}{i}{}\mathrm{ ;
c. otherwise, at least 2 degrees of freedom are consumed to linearize
    the output bits of the S-box marked by }\mp@subsup{U}{i}{}\mathrm{ .
```

This observation comes from the algebraic relation between the input and output of $\chi$. Suppose the 5 -bit input of the $S$-box is $x_{4} x_{3} x_{2} x_{1} x_{0}$ and the 5 -bit output $y_{4} y_{3} y_{2} y_{1} y_{0}$. Then the algebraic normal forms of the S-box are as follows.

$$
\begin{aligned}
y_{0} & =x_{0}+\left(x_{1}+1\right) \cdot x_{2}, \\
y_{1} & =x_{1}+\left(x_{2}+1\right) \cdot x_{3}, \\
y_{2} & =x_{2}+\left(x_{3}+1\right) \cdot x_{4}, \\
y_{3} & =x_{3}+\left(x_{4}+1\right) \cdot x_{0}, \\
y_{4} & =x_{4}+\left(x_{0}+1\right) \cdot x_{1} .
\end{aligned}
$$

Take $U_{i}=00001$ as an example. It indicates that $y_{0}$ should be linearized. As can be seen, the only nonlinear term in the expression of $y_{0}$ is $x_{1} \cdot x_{2}$. Fixing the value of either $x_{1}$ or $x_{2}$ makes $y_{0}$ linear. Without loss of generality, the value of $x_{1}$ is assumed to be fixed to 0 or 1 . When $x_{1}=0$, we have $y_{0}=x_{0}+x_{2}$; otherwise $y_{0}=x_{0}$. When $U_{i}=01111$, it maps to 4 output bits $y_{0}, y_{1}, y_{2}, y_{3}$ which should be linearized. We can fix the value of two bits $x_{2}$ and $x_{4}$ only. Once $x_{2}$ and $x_{4}$ are fixed, the nonlinear terms in the algebraic form of all $y_{0}, y_{1}, y_{2}, y_{3}$ will disappear. Other cases work similarly. If $U_{i}=11111$, a full linearization is required by fixing the value of any three input bits which are not cyclically continuous, e.g., $\left(x_{0}, x_{2}, x_{4}\right)$.

As noted in Observation 1 and 2, it costs at least three degrees of freedom to fully linearize an S-box, even if it has DDT entry of 8. However, Observation 4 shows that two degrees of freedom may be enough to partially linearize an S-box of DDT entry 8 , and thus 1 bit degree of freedom could be saved.

Observation 4 For a 5-bit input difference $\delta_{\text {in }}$ and a 5-bit output difference $\delta_{\text {out }}$ such that $\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=8$, 4 out of 5 output bits are already linear if the input is chosen from the solution set $V=\left\{x: \mathrm{S}(x)+\mathrm{S}\left(x+\delta_{\text {in }}\right)=\delta_{\text {out }}\right\}$.

Take $\operatorname{DDT}(01,01)=8$ as an example (see Table 6 of [24]). The solution set is $V=\{10,11,14,15,18,19,1 \mathrm{C}, 1 \mathrm{D}\}$. We re-write these solutions in the following 5 -bit strings where the bits are written as $x_{4} x_{3} x_{2} x_{1} x_{0}$.

$$
\begin{aligned}
& 10: 10000,11: 10001,14: 10100,15: 10101, \\
& 18: 11000,19: 11001,1 C: 11100,1 D: 11101 .
\end{aligned}
$$

It is easy to see for the values from this set, $x_{1}=0$ and $x_{4}=1$ always hold, making $y_{0}, y_{2}, y_{3}, y_{4}$ linear since their algebraic forms could be rewritten as

$$
\begin{aligned}
& y_{0}=x_{0}+x_{2}, \\
& y_{1}=\left(x_{2}+1\right) \cdot x_{3}, \\
& y_{2}=x_{2}+x_{3}+1, \\
& y_{3}=x_{3}, \\
& y_{4}=1
\end{aligned}
$$

If the only nonlinear bit $y_{1}$ does not influence $\Delta S_{I}$, setting $x_{1}=0$ and $x_{4}=1$ (consuming two degrees of freedom) is enough for linearizing the remaining four bits.

Therefore, for an S-box, both active and non-active, it may consume less than three degrees of freedom for non-full linearizations, which allows to producing relatively large message spaces for the brute-force collision searching stage.

### 4.3 2-Round Connector

The core idea of our 2-round connector is to convert the problem to that of solving a system of linear equations. Note the first two rounds of KECCAK permutation can be expressed as $\chi_{1} \circ L \circ \chi_{0} \circ L$ (omitting the $\iota$ ), as depicted in Fig. 3. Using the techniques discussed above, $\chi_{0}$ which has fixed input and output differences can be (partially) linearized, i.e., the first three operations $L \circ \chi_{0} \circ L$ become (partially) linear. For convenience, the differential transition of the $i$-th round is denoted by $\alpha_{i-1} \xrightarrow{L} \beta_{i-1} \xrightarrow{\chi} \alpha_{i}$, where $i=1,2, \cdots$. We will give details of the method how input and output differences of $\chi_{0}$, i.e., $\beta_{0}$ and $\alpha_{1}$, are selected later. Now, we show how the $\chi_{1}$ can be inverted by adding more linear equations of constraints.

In the setting of a 2 -round connector, the output difference of $\chi_{1}$, i.e., $\alpha_{2}$ is given as $\Delta S_{I}$ - the input difference of the differential trail. It is not necessary that all S-boxes of $\chi_{1}$ are active, i.e., with a non-zero difference. Here only active S-boxes are concerned, and each of them is inverted by randomly choosing an input difference with a non-zero number of solutions which is called compatible input difference. Formally, given the output difference $\delta_{o u t}$ for the KEccak Sbox, its compatible input differences are from the set $\left\{\delta_{\text {in }}: \operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right) \neq 0\right\}$.


Figure 3: The first two rounds of Keccak permutation

As noted previously in $[4,8,10]$, for any pair of $\left(\delta_{\text {in }}, \delta_{\text {out }}\right)$, the solution set $V=\left\{x: \mathrm{S}(x)+\mathrm{S}\left(x+\delta_{\text {in }}\right)=\delta_{\text {out }}\right\}$ forms an affine subspace. In other words, $V$ can be deduced from the set $\{0,1\}^{5}$ by setting up $i$ constraints that turn out to be binary linear equations, when the size of the solution set $V$ is $2^{5-i}$. For example, corresponding to $\operatorname{DDT}(03,02)$ is the 2-dimensional affine subspace $\{14,17,1 \mathrm{C}, 1 \mathrm{~F}\}$ which can be formulated by the following three linear equations as the constraints:

$$
\left(\begin{array}{lllll}
0 & 0 & 1 & 0 & 0 \\
1 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 1
\end{array}\right) \cdot x=\left(\begin{array}{l}
1 \\
0 \\
1
\end{array}\right) .
$$

It is important to note, under the $i$ linear equations or set $V, \delta_{\text {in }}$ propagates to $\delta_{\text {out }}$ deterministically. Hence, each active S-box in $\chi_{1}$ is inverted by a choice of compatible input difference together with the corresponding $i$ linear equations on the input values. Once the input difference and linear equations for all active S boxes of $\chi_{1}$ are enforced and fulfilled together with the linearization of the first round, solutions of the 2 -round connector are found. Note that a compatible input difference of $\chi_{1}$ is a choice of $\beta_{1}$, and $\alpha_{1}$ can be uniquely determined by the relation $\alpha_{1}=L^{-1}\left(\beta_{1}\right)$. In the remaining part of this subsection, more details on the implementation of this idea are given.

Generation of linear equations. As depicted in Fig. 3, the variables of the equation system are the bit values before $\chi_{0}$ denoted by vector $x$. Additionally, $y$ and $z$ are bit vectors of intermediate values for further interpretation where $y$ represents the output after $\chi_{0}$ and $z$ the bits before $\chi_{1}$. The main task is to derive all constraints on differences and affine subspaces into that on the variables $x$. Suppose $\beta_{1}$ and $\beta_{0}$ (details will be given in Section 4.6) are fixed, and $\Delta S_{I}$ (aka. $\alpha_{2}$ ) is given, we show how the system of equations could be set up.

With the input difference $\beta_{1}$ and output difference $\alpha_{2}$ of $\chi_{1}$, all the linear equations on the input affine subspaces of the active S-boxes in the second round can be derived and expressed as

$$
\begin{equation*}
B \cdot z=t_{B} \tag{1}
\end{equation*}
$$

where $B$ is a block-diagonal matrix in which each diagonal block together with corresponding constants in $t_{B}$ formulates the equations of one active S -box.

A similar procedure can be done for input affine subspaces of the first round given $\beta_{0}$ and $\alpha_{1}$, and the corresponding linear equations on $x$ is denoted by

$$
\begin{equation*}
A_{1} \cdot x=t_{A_{1}} . \tag{2}
\end{equation*}
$$

Note an additional constraint $x$ needs to fulfill is that the last $(c+p)$-bit of initial state are pre-fixed, which can be expressed as

$$
\begin{equation*}
A_{2} \cdot x=t_{A_{2}} . \tag{3}
\end{equation*}
$$

where $A_{2}$ is a submatrix of $L^{-1}$ and $t_{A_{2}}$ is the preset value. Apart from Eq. (2) and (3), some extra equations are imposed to linearize $\chi_{0}$. To derive these equations, we need to specify the flag vector $u$ identifying the output bits of $\chi_{0}$ which affect $\Delta S_{I}$ and should be linearized. In fact, the value of $u$ is determined by Eq. (1) which can be re-expressed as

$$
\begin{equation*}
B \cdot L \cdot\left(y+R C_{0}\right)=t_{B} \tag{4}
\end{equation*}
$$

since $z=L \cdot\left(y+R C_{0}\right)$. Then $u_{i}=1$ if $y_{i}$ appears in Eq. (4) with a nonzero coefficient, otherwise $u_{i}=0$. Recall that $U=\left(U_{0}, U_{1}, \cdots, U_{b / 5-1}\right)$ where $U_{i}=u_{5 i} u_{5 i+1} u_{5 i+2} u_{5 i+3} u_{5 i+4}, 0 \leq i<b / 5$. According to Observation 2, S-boxes with DDT entry being 2 or 4 are already linearized due to the fact that the input is confined to the solution set $V=\left\{x: \mathrm{S}(x)+\mathrm{S}\left(x+\delta_{\text {in }}\right)=\delta_{\text {out }}\right\}$ by linear equations in (2). So we only need to handle non-active S-boxes and active S-boxes with DDT entry 8.

Full S-box linearization. $U_{i}=11111$ indicates that fully linearizing the $i$-th $S$-box is required.

- For non-active S-boxes, randomly choose any 2-dimensional linearizable affine subspace from 80 such subspaces by imposing 3 linear equations on $x$.
- For active S-boxes with DDT entry 8, randomly choose any 2-dimensional linearizable affine subspace from 6 such subspaces by imposing an extra linear equation on $x$ (2 linear equations are already included in Eq. (2)).

Non-full $S$-box linearization. $U_{i} \neq 11111$ indicates that only part of the output bits of the $i$-th S-box needs to be linearized.

- For non-active S-boxes, linearize the outputs marked by $u$ by imposing a least possible number of equations according to Observation 3. At most 2 linear equations are required.
- For each active S-box with DDT 8, if its only non-linear output bit is not marked by $U$, then we need not handle it. Otherwise, impose an extra linear equation on $x$ in a similar way as what we do for full S-box linearizations.

We denote these extra linear equations for linearizing $\chi_{0}$ by

$$
\begin{equation*}
A_{3} \cdot x=t_{A_{3}} . \tag{5}
\end{equation*}
$$

Suppose bits in $y$ marked by $u$ now can be computed from $x$ linearly through

$$
\begin{equation*}
y=L_{\chi_{0}} \cdot x+t_{L_{\chi_{0}}} \tag{6}
\end{equation*}
$$

and further an equivalent system of Eq. (4) can be obtained as

$$
\begin{equation*}
B \cdot L \cdot L_{\chi_{0}} \cdot x+B \cdot L \cdot\left(t_{L_{\chi_{0}}}+R C_{0}\right)=t_{B} \tag{7}
\end{equation*}
$$

We merge the four systems of linear equations on $x$ together, i.e., Eq. (2), (3), (5) and (7) and denote the resulted system of linear equations by $E_{M}$ :

$$
\begin{equation*}
A \cdot x=t_{A} . \tag{8}
\end{equation*}
$$

Then, solutions fulfilling $E_{M}$ will be solutions of the 2-round connector.

Algorithm of the 2-round connector. Since Eq. (3) remains static, and given differences ( $\beta_{0}, \alpha_{1}, \beta_{1}, \alpha_{2}$ ), Eq. (2) and (1) are determined, the core part is the generation of Eq. (5), i.e., linearizing $\chi_{0}$, which is sketched in the procedure basicLinearization (see Alg. 2). The procedure mainLinearization (see Alg. 1) invokes basicLinearization and generates the final system of linear equations $E_{M}$ whose solutions are exactly the solutions of the 2 -round connector.

The inputs to mainLineariztion are $\beta_{0}, \alpha_{1}, \beta_{1}, \alpha_{2}\left(\Delta S_{I}\right)$ and $E_{M}$, where $E_{M}$ is initialized with Eq. (2) and (3). Similarly, basicLinearization takes as input the same $\beta_{0}, \alpha_{1}$ and $E_{M}$, as well as a flag vector $U$ generated in mainLinearization. If basicLinearization succeeds, it will return to mainLinearization the (partial) linear mapping of $\chi_{0}$, i.e., $\left(L_{\chi_{0}}, t_{L_{\chi_{0}}}\right)$, and the updated $E_{M}$. Further, the success of mainLinearization will give rise to the final system of equations $E_{M}$.

In basicLinearization, a procedure named preProcess is called on Line 1 which is described more at length in Alg. 3. preProcess identifies all possible S-boxes of $\chi_{0}$ whose output bits marked by $U$ are already linear under the initial $E_{M}$, so the number of equations in Eq. (5) will be minimized. In fact, the conditions of Line 11 and 16 would never hold if $E_{M}$ does not contain Eq. (3), and they hold more likely for large Eq. (3), i.e., large $c+p$. Therefore, preProcess is crucial to the success of solving KECCAK instances with a relatively large capacity, such as SHA3-256.

Note that Alg. 1 and 2 do not succeed all the time. To overcome this problem, we repeat random picks of compatible input differences $\beta_{1}$ ( $\beta_{0}$ changes accordingly) for a given $\Delta S_{I}$, until the main procedure succeeds.

### 4.4 3-Round Connector

We construct 3 -round connectors upon a 2-round connector. Since almost all output bits of the first round affect $\alpha_{3}$ (the difference after the third round), full linearizations are enforced on the first nonlinear layer $\chi_{0}$ while constructing a 2 -round connector for the first two rounds. Once a 2 -round connector succeeds, the resulted system of equations $E_{M}$ on $x$ can be re-expressed as an equivalent

```
Algorithm 1: mainLinearization.
    Input: \(E_{M}, \beta_{0}, \alpha_{1}, \beta_{1}\), and \(\alpha_{2}\)
    Output: updated \(E_{M}\)
    Derive Eq. (1) from ( \(\beta_{1}, \alpha_{2}\) );
    Derive Eq. (4) and \(U\);
    Initialize a counter cnt to a large integer;
    while \(c n t>0\) do
        // Linearize \(\chi_{0}\) and obtain ( \(L_{\chi_{0}}, t_{\chi_{0}}\) ) and update \(E_{M}\).
        if basicLinearization \(\left(E_{M}, \beta_{0}, \alpha_{1}, U\right)\) succeeds then
            Derive Eq. (7) using ( \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\) ) output by basicLinearization;
            if Eq. (7) is consistent with \(E_{M}\) then
                Add Eq. (7) to \(E_{M}\);
                Output \(E_{M}\);
                return success;
        \(c n t=c n t-1 ;\)
    return failure;
```

```
Algorithm 2: basicLinearization.
    Input: \(E_{M}, \beta_{0}, \alpha_{1}\), and \(U\)
    Output: updated \(E_{M}, L_{\chi_{0}}, t_{\chi_{0}}\)
    \(l_{s b}, L_{\chi_{0}}, t_{L_{\chi_{0}}}=\operatorname{preProcess}\left(\beta_{0}, \alpha_{1}, U\right) ; / /\) Generate the primary \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\)
        and a list of S -boxes that need further processing.
    for each \(S\)-box sb in \(l_{s b}\) do
        Initialize an empty list \(l_{l i n}\) of set of equations;
        if \(s b\) is active then
            Load to \(l_{\text {lin }}\) the 6 sets; // Each set has 1 equation.
        else
            Load to \(l_{\text {lin }}\) all sets of least possible equations that allow linearization
            of output bits marked by \(U\);
        while There is any untested set in \(l_{\text {lin }}\) do
            Randomly choose an untested set of equations;
            if The set of equations are consistent with \(E_{M}\) then
                Add it to \(E_{M}\) and update \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\);
                    Break;
            else
                if All sets have been tested then
                    Output "No solution in basicLinearization";
                    return failure;
    Output \(E_{M}, L_{\chi_{0}}, t_{\chi_{0}} ; / / E_{M}\) is updated with Eq. (5).
    return success;
```

```
Algorithm 3: preProcess.
    Input: \(\beta_{0}, \alpha_{1}, U\)
    Output: A list \(l_{s b}\) of S-boxes that need further processing, \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\)
    Initialize an empty list \(l_{s b}\), a matrix \(L_{\chi_{0}}\) and a vector \(t_{L_{\chi_{0}}}\);
    for \(i=0 \rightarrow b / 5-1\) do
        if \(U_{i}>0\) then
            Extract \(\delta_{\text {in }}, \delta_{\text {out }}\) of the \(i\)-th S-box from \(\beta_{0}, \alpha_{1}\);
            if \(\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=2,4\) then
                Update \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\);
            if \(\left.\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)\right)=8\) then
                if The only nonlinear output bit is not marked by \(U_{i}\) then
                    Update \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\);
                else
                    if Among the 6 2-dimensional linearizable subspaces, some one
                        is already specified in \(E_{M}\) then
                    Update \(L_{\chi_{0}}, t_{L_{\chi_{0}}} ;\)
                    else
                            Add this S-box to \(l_{s b}\);
            if the \(S\)-box is not active then
                if Any set of equations for linearizing the output bits marked by \(U_{i}\)
                is already contained in \(E_{M}\) then
                    Update \(L_{\chi_{0}}, t_{L_{\chi_{0}}}\);
                else
                    Add this S-box to \(l_{s b}\);
    return \(l_{s b}, L_{\chi_{0}}, t_{L_{\chi_{0}}} ;\)
```

system of equations $E_{M}^{\prime}$ on $z$, i.e., the input value of the second round. It can be considered that the first round disappeared. Next, given $E_{M}^{\prime}$, a 2-round connector is to construct for the second and the third rounds where the second round is partially linearized. Once the second 2 -round connector succeeds, its solutions will bypass the first three rounds. That is to say, a 3-round connector is obtained.

Due to a great consumption of degree of freedom, the solution space of a 3round connector may have a dimension smaller than the weight of the following differential, making it hard to find a real collision with one solution space. However, this problem can be solved by constructing the second 2 -round connectors repeatedly. In these 3 -round connectors, the degrees of freedom for linearizing the second round can be reused and hence are not consumed. Thus, multiple solution spaces can be generated successively if one is not enough. Such successively generated 3 -round connectors are called adaptive 3 -round connectors, for which a detailed description can be found in Section 7.2 of [26].

### 4.5 Analysis of Degrees of Freedom

Let the degree of freedom of the final $E_{M}$ for the connector be DF. In our collision attacks, DF is a key factor on success. A solution space with DF larger than the weight of the differential trail is possible to suggest a message pair with digest collision. After linearizing the first round, the degree of freedom is $\sum_{i=0}^{b / 5-1} \mathrm{DF}_{i}^{(1)}$ in which $\mathrm{DF}_{i}^{(1)}$ is the degree of freedom of 5 -bit input space of the $i$-th S-box in the first round and calculated according to rules in Table 2. This concerns not only the transition from $\beta_{0}$ to $\alpha_{1}$, but also the S -box linearizations.

Table 2: Value of $\mathrm{DF}_{i}^{(1)}$

|  | $\mathrm{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=32$ | $\mathrm{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=2$ | $\operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=4$ | $\mathrm{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=8$ |
| :--- | :---: | :---: | :---: | :---: |
| $\mathrm{DF}_{i}^{(1) *}$ | $2 \sim 5$ | 1 | 2 | 2 or 3 |

* $\delta_{\text {in }}$ and $\delta_{\text {out }}$ are the input and output differences of the $i$-th S-box in the first round. The exact value of $\mathrm{DF}_{i}^{(1)}$ is based on whether the output bits of the $i$-th S-box influence $\Delta S_{I}$, i.e., the value of $U_{i}$.

The constraints on the initial state reduce $(c+p)$ degrees of freedom for the pre-fixed $(c+p)$-bit. Another decrement on the degree of freedom is due to the constraints on the input values of the S-box layer in the second round. The definition of $\mathrm{DF}_{i}^{(2)}$, the degree of freedom of 5 -bit input values to $S$-boxes in the second round, is

$$
\operatorname{DF}_{i}^{(2)}= \begin{cases}1, & \operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=2  \tag{9}\\ 2, & \operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=4 \\ 3, & \operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=8 \\ 5, & \operatorname{DDT}\left(\delta_{\text {in }}, \delta_{\text {out }}\right)=32\end{cases}
$$

where $\delta_{\text {in }}$ and $\delta_{\text {out }}$ are the input and output differences of the $i$-th S-box in the second round. For the $i$-th $S$-box in the second round, we add ( $5-\mathrm{DF}_{i}^{(2)}$ ) equations to $E_{M}$ and suppose to deduce the degree of freedom by this amount.

Therefore, the degree of freedom of the final $E_{M}$ of our 2-round connector is estimated as

$$
\begin{equation*}
\mathrm{DF}=\sum_{i=0}^{b / 5-1} \mathrm{DF}_{i}^{(1)}-(c+p)-\sum_{i=0}^{b / 5-1}\left(5-\mathrm{DF}_{i}^{(2)}\right) \tag{10}
\end{equation*}
$$

Large DF benefits our search for collisions in rounds beyond the second round and sufficiently large DFs (say > 140) may allow 3-round connectors.

### 4.6 How to choose $\beta_{2}, \beta_{1}$ and $\beta_{0}$

Choosing $\boldsymbol{\beta}_{1}$ in the 2 -round connector. Recall that we randomly choose compatible input differences $\beta_{1}$ according to $\Delta S_{I}\left(\alpha_{2}\right)$ until the 2-round connector succeeds. As the number of active $S$-boxes in the second round is large
enough (range from tens to hundreds in our attacks), there is a huge number of compatible choices of $\beta_{1}$ so that we can only choose those $\beta_{1}$ such that $\beta_{1} \rightarrow \alpha_{2}$ is of the best probability for the given $\alpha_{2}$. This allows a speedup of 2 -round connectors, as well as an increment of DF. Interestingly, the differential probability of $\beta_{1} \rightarrow \alpha_{2}$ does not need to be high because this transition is covered in our 2 -round connector.

Choosing $\boldsymbol{\beta}_{\mathbf{0}}$ in the 2-round connector. So far we have not given details on how $\beta_{0}$ can be selected. We follow Dinur et al.'s work [10] in a more general way to uniquely determine $\beta_{0}$, namely, the difference before $\chi$ layer of the first round. Specifically, the so-called "target difference algorithm" is used which consists of a difference phase and a value phase.

Given $\Delta S_{I}$, we have randomly chosen a compatible input difference $\beta_{1}$. We then build two equation systems $E_{\Delta}$ and $E_{M}$ accordingly. $E_{\Delta}$ is on differences of the message pairs (specifically, on $\beta_{0}$ ) and $E_{M}$ is on the value of one message (specifically, on the value of the message before $\chi_{0}$, i.e., $x$ as shown in Fig. 3). The initialization of $E_{\Delta}$ should abide by (1) the constraints implied by the $(c+p)$-bit pre-fixed value that the corresponding difference should be 0 , and (2) the input difference bits of non-active S -boxes in the first round equal to 0 . The initialization of $E_{M}$ should abide by the pre-fixed value of the last $c+p$ bits. These rules are easy to be implemented as the variable vector $x$ is the image of the initial vector by an invertible linear mapping. Therefore, in the initialization we equate the corresponding bits to their enforced values in $E_{\Delta}$ and $E_{M}$.

For $E_{\Delta}$, we add additional equations so that its solution, i.e., $\beta_{0}$ is compatible with $\alpha_{1}$. Though the obvious way is to equate the 5 input difference bits to a specific value for each active S-box in the first round, this will restrict the solution space significantly. As suggested in [10], we chose one of the 2-dimensional affine subsets of input differences instead of a specific value for each active S-box. This is based on the fact that given any nonzero 5-bit output difference to a KECCAK S-box, the set of possible input differences contains at least five 2-dimensional affine subspaces. After a consistent $E_{\Delta}$ system is constructed, the solution space is an affine subspace of candidates for $\beta_{0}$. Then we continue to maintain $E_{\Delta}$ by iteratively adding two additional equations to uniquely specify a 5 -bit input difference for the active S-boxes. For each active S-box, once the specific input difference is determined, we add equations to $E_{M}$ system to enforce every active 5 -bit of $x$ (input bits to active S-box) to an affine subspace corresponding to the uniquely determined $\delta_{\text {in }}$ and $\delta_{\text {out }}$. In this way, we always find a compatible $\beta_{0}$ for $\alpha_{1}$ that fulfills the constraints imposed by the $(c+p)$-bit pre-fixed value.

Choosing $\boldsymbol{\beta}_{\mathbf{2}}$ in the $\mathbf{3}$-round connector. $\beta_{2}$ is chosen before running the 3 -round connector. Its value fulfills two requirements: (1) given $\alpha_{2}=L^{-1}\left(\beta_{2}\right)$, a 2-round connector for the first two rounds can be obtained with a sufficiently large degree of freedom; and (2) $\beta_{2} \rightarrow \alpha_{3}$ is of high probability where $\alpha_{3}$ is the input difference of the following differential trail. While constructing 3-round connectors, specifically the second 2 -round connector, $\beta_{2}$ is fixed and does not
change. In this way, the second 2-round connector can be constructed efficiently thanks to the high probability of $\beta_{2} \rightarrow \alpha_{3}$.

## 5 Search for Differential Trails with GPU

In this section, we elaborate on searching differential trails of KECCAK. The idea of searching differential trails greatly benefits from previous differential analysis of KECCAK [9, 14, 19, 21]. The associated techniques developed in the previous works are reviewed first, followed by the considerations in finding differential trails that fit in our attacks. Subsequently, the exact searching algorithm that provides differential trails for practical collision attacks against KECCAK[640, 160, 5, 160], 5-round SHAKE128, 5-round SHA3-224, 5-round SHA3 $256, \operatorname{Keccak}[1440,160,5,160]$, and $\operatorname{Keccak}[1440,160,6,160]$ is illustrated. To speed up the searching process, we introduce GPU implementations of KEccak. The differential trails to be used in our collision attacks are listed at the end of this section.

### 5.1 Differential Properties of Keccak

In this subsection, we recall special properties of the linear and nonlinear layer of KECCAK round function which have been identified in previous works. In the following paragraphs, we follow the notations from [9].

Key properties used in differential analysis. The concept of column parity, defined in [4], is the most critical property of $\theta$ operation. Formally, the column parity (or parity for short) $P(A)$ of a value (or a difference) $A$ is defined as the parity of the columns of $A$, i.e., $P(A)[i][k]=\Sigma_{j} A[i][j][k]$. A column is even, if its parity is 0 , otherwise it is odd. A state is in Column Parity kernel (CP-kernel for short) if all of its columns are even. $\theta$ adds a pattern, called the $\theta$-effect, to the state. The $\theta$-effect of a state $A$ is $E(A)[i][k]=P(A)[i-1][k]+P(A)[i+1][k-1]$. Thus the effect of $\theta$ depends only on column parities. Given a state $A$ in CPkernel, the Hamming weight of $A$ remains unchanged after $\theta$. Another interesting property is that $\theta^{-1}$ diffuses much faster than $\theta$. To be precise, a single bit difference can propagate into half state bits through $\theta^{-1}$ on average.

Regarding the nonlinear operation S-box, given a non-zero input difference, all compatible output differences occur with the same probability. Particularly, for input differences with one active bit, the S-box acts as identity with probability $2^{-2}$. However, given an output difference of the S-box, the probabilities of compatible input differences vary even though the best probability is determined. When the best probability is only $2^{-3}$, there are multiple input differences achieving this. Therefore, given an output difference of $\chi$, usually there are multiple input differences fulfilling the best probability.

Representation of trails and their weights. As in previous sections, we denote the differences before and after the $i$-th round by $\alpha_{i-1}$ and $\alpha_{i}$, respectively. Let $\beta_{i}=L\left(\alpha_{i}\right)$, then an $n$-round differential trail starting from the 1 -st round is of the following form

$$
\alpha_{0} \xrightarrow{L} \beta_{0} \xrightarrow{\chi} \alpha_{1} \xrightarrow{L} \cdots \alpha_{n-1} \xrightarrow{L} \beta_{n-1} \xrightarrow{\chi} \alpha_{n} .
$$

For simplicity, a trail can also be represented with only $\beta_{i}$ 's or $\alpha_{i}$ 's.
The weight of a differential $\beta \rightarrow \alpha$ over a function $f$ with domain $\{0,1\}^{b}$ is defined as

$$
w(\beta \rightarrow \alpha)=b-\log _{2}|\{x: f(x) \oplus f(x \oplus \beta)=\alpha\}|
$$

In other words, the weight of a differential $\beta \rightarrow \alpha$ is equal to $-\log _{2} \operatorname{Pr}(\beta \rightarrow \alpha)$. $\alpha$ and $\beta$ are compatible when $\operatorname{Pr}(\beta \rightarrow \alpha)>0$, otherwise the weight of $\beta \rightarrow \alpha$ is undefined.

The weight $w\left(\beta_{i} \rightarrow \alpha_{i+1}\right)$ is denoted by $w_{i}$, and thus the weight of a trail is the sum of the weights of round differentials that constitute the trail. In addition, $\# \mathrm{AS}(\alpha)$ is used to represent the number of active S -boxes in a state difference $\alpha$. According to the properties of $\chi$, given $\beta_{i}$ the weight of $\left(\beta_{i} \rightarrow \alpha_{i+1}\right)$ is determined; also, given $\beta_{i}$ the minimum reverse weight of $\left(\beta_{i-1} \rightarrow L^{-1}\left(\beta_{i}\right)\right)$ is fixed.

As in [4], $n-1$ consecutive $\beta_{i}$ 's, say $\left(\beta_{1}, \cdots, \beta_{n-1}\right)$ is called an $n$-round trail core which defines a set of $n$-round trails $\alpha_{0} \xrightarrow{L} \beta_{0} \xrightarrow{\chi} \alpha_{1} \xrightarrow{L} \beta_{1} \cdots \xrightarrow{L} \beta_{n-1} \xrightarrow{\chi}$ $\alpha_{n}$ where the first round is of the minimal weight determined by $\alpha_{1}=L^{-1}\left(\beta_{1}\right)$, and $\alpha_{n}$ is compatible with $\beta_{n-1}$. The first step of mounting collision attacks against $n$-round KECCAK is to find good $(n-1)$-round trail cores.

### 5.2 Requirements for Differential Trails

Good trail cores are those satisfying all the requirements which will be explained as follows. Firstly, the difference of the output needs to be zero, i.e., $\alpha_{n_{r}}^{d}=0$ $\left(\alpha_{n_{r}}^{d}\right.$ represents the first $d$ bits of $\left.\alpha_{n_{r}}\right)$.

Secondly, the consumption of degree of freedom must be within budget. With the definition of weight, Eq. (10), i.e., the estimation of DF of our 2-round connectors can be represented in an alternative way

$$
\begin{equation*}
\mathrm{DF}=\sum_{i=0}^{b / 5-1} \mathrm{DF}_{i}^{(1)}-(c+p)-w_{1} \tag{11}
\end{equation*}
$$

The first term of the formula depends on the output bits of the first round that need to be linearized and DDT entries of active S-boxes as depicted in Table 2. Empirically, the inputs of most S-boxes are confined to 2-dimensional subsets. Therefore, we heuristically set $\frac{b}{5} \times 2$ as a threshold for the first term in (11), and denote a threshold of the first two terms in (11) for further search conditions by

$$
\mathrm{TDF}=\frac{b}{5} \times 2-(c+p)
$$

Note that this is a pessimistic estimation of the first two terms. The actual value is usually higher due to the technique of non-full S-box linearization and the fact that there may exist dependencies in the system of equations used for constructing connectors.

In order to mount collision attacks against $\operatorname{KECCAK}\left[r, c, n_{r}, d\right]$ with methods described in Section 4, it is necessary that

$$
\begin{equation*}
\mathrm{TDF}>w_{1}+\cdots+w_{n_{r}-2}+w_{n_{r}-1}^{d} \tag{12}
\end{equation*}
$$

where $w_{n_{r}-1}^{d}$ is the portion of $w_{n_{r}-1}$ that relates to the digest. The trail searching phase is performed to provide $\Delta S_{I}$ for the connector. However, the conditions for a good trail core is restrained by results of the connector, i.e., the degree of freedom of the solution space. So we take (12) as a heuristic condition for searching good trail cores which are promising for collision attacks.

Thirdly, the collision attack should be practical. Note that after we obtain a subspace of message pairs that fulfill the first two or three rounds, the complexity of searching a collision is $2^{w}$, where $w=w_{n_{r}-3}+\cdots+w_{n_{r}-1}^{d}$ is the weight of the differential trail over the last three rounds. To make our attacks practical, we restrict $w$ to be small enough, say 55 .

We summarize the requirements for differential trails as follows and list TDFs for different variants of $\operatorname{KEccak}\left[r, c, n_{r}, d\right]$ in Table 3.
(1) $\alpha_{n_{r}}^{d}=0$, i.e., the difference of output must be zero.
(2) TDF $>w_{1}+\cdots+w_{n_{r}-1}^{d}$, i.e., the degree of freedom must be sufficient;
(3) $w=w_{n_{r}-3}+\cdots+w_{n_{r}-1}^{d} \leq 55$, the complexity of finding a collision should be practical.

Table 3: TDFs of different versions of $\operatorname{Keccak}\left[r, c, n_{r}, d\right]$

| KECCAK $\left[r, c, n_{r}, d\right]$ | TDF | Remarks |
| :---: | :---: | :---: |
| KECCAK $[1440,160,5,160]$ | 479 | KECCAK contest |
| KECCAK $[1344,256,5,256]$ | 378 | SHAKE128 |
| KECCAK $[640,160,5,160]$ | 159 | KECCAK contest |
| KECCAK $[1440,160,6,160]$ | 479 | KECCAK contest |
| KECCAK $[1152,448,5,224]$ | 188 | SHA3-224 |
| KECCAK $[1088,512,5,256]$ | 124 | SHA3-256 |

### 5.3 Searching Strategies

In 5-round collision attacks on KECCAK, we are to combine a 2-round connector and a 3 -round differential trail. Given $\alpha_{2}$, i.e., the input difference of the differential trail over the last three rounds, the minimal weight of the second round $w_{1}$
is determined. Note that, the second round is covered by the connector, and the smaller $w_{1}$, the fewer constraints imposed to the connector. In order to make it practical to construct 2 -round connectors, we aim to find 3 -round differential trails that impose a least possible number of constraints to the connector. Namely, $w_{1}$ should be as small as possible. Actually, this means we need to find good 4 -round differential trails cores. Similarly, in 6 -round collision attacks, a 3 -round connector is combined with a 3 -round differential trail. As shown in Section 4.4, $\beta_{2}, \beta_{3}\left(\alpha_{3}\right)$ are known and fixed in the 3 -round connector, so 5 -round trails cores are needed, whose first two rounds will be covered by the connector.

Differential trails with two rounds in CP-kernel. The differential trails with two rounds in CP-kernel are searched to fulfill the requirements discussed above. The 2 -round trail cores, denoted by $\left(\beta_{3}\right)$, that ensure $\alpha_{3}$ and at least one choice of $\alpha_{4}$ in CP-kernel are the starting point of our algorithm for searching differential trails. A detailed analysis of the reason to do so is postponed to Appendix C.1. By extensively making use of the KeccakTools [5] developed by the Keccak Team, we generate such 2-round trails cores, based on which trail cores of 4 or 5 rounds for our collision attacks are obtained it the following way.

Algorithm for searching differential trails. We sketch below our steps for finding 4-round differential trail cores of KECCAK, upon which 5-round collision attacks are mounted, as illustrated in Fig. 4.

1. Using KeccakTools, find special $\beta_{3}$ 's with a low Hamming weight, say 10.
2. For every $\beta_{3}$ obtained, traverse all possible $\alpha_{4}$ using a tree structure, compute $\beta_{4}=L\left(\alpha_{4}\right)$ and test whether there exists a compatible $\alpha_{5}$ where $\alpha_{5}^{d}=$ 0 (Requirement (1)). If so, keep this $\beta_{3}$ and record its forward extension, otherwise discard it.
3. For the remaining $\beta_{3}$ 's, traverse all possible $\beta_{2}$ 's which are compatible with $L^{-1}\left(\beta_{3}\right)$ 's. For the trail core $\left(\beta_{2}, \beta_{3}, \beta_{4}\right)$, check Requirement (2) and (3). If these two requirements are satisfied, then output $\left(\beta_{2}, \beta_{3}, \beta_{4}\right)$.

We provide a description in more details in Appendix C.2. To mount collision attacks on 6 -round Keccak, 5 -round differential trail cores are needed. In this case, we just extend forward the 4 -round trail cores by one more round, as shown in Fig. 5.

### 5.4 GPU Implementations of Keccak

Techniques for GPU implementations of KECCAK are introduced to improve our computing capacity. While one could expect a speed of order $2^{21}$ KECCAK- $f$ evaluations per second on a single CPU core, we show in this section this number could increase to $2^{29}$ per second on NVIDIA GeForce GTX1070 graphic card. The significant speedup will benefit us in two stages: searching for differential trails among larger spaces and brute-force search of collisions following differential trails with lower probability.


Figure 4: Search for differential trails and collision attacks on 5-round Keccak.


Figure 5: Search for differential trails and collision attacks on 6-round Keccak.

Overview of GPU and CUDA. GPUs (Graphics Processing Unit) are intended to process the computer graphics and image originally. With more transistors for data processing, a GPU usually consists of thousands of smaller but efficient ALUs (Arithmetic Logic Unit), which can be used to process parallel tasks efficiently. CUDA is a general purpose parallel computing architecture and programming model that is used in NVIDIA GPUs [23]. One of the programming interfaces of CUDA is CUDA C/C++ which is based on standard C/C++. Here, we mainly focus on CUDA C++.

To better understand the techniques for implementations, some basic concepts are introduced. From the view of hardware architecture, a GPU is comprised of several SMs (Streaming Multiprocessors), which determine the parallelization capability of GPU. In Maxwell architecture, each SM owns 128 SPs (streaming processors) - the basic processing units. Warp is the basic execution unit in SM and each warp consists of 32 threads. All threads in a warp execute the same instructions at the same time. Each thread will be mapped into a SP when it is executed.

Existing implementations and our implementations. Guillaume Sevestre [25] implemented KEccak in a tree hash mode, the nature of which allows each thread to run a copy of Keccak. Unfortunately, there are no implementation details given. In [7], Pierre-Louis Gayrel et al. implemented Keccak-f [1600]
with 25 threads that calculate all 25 lanes in parallel in a warp and these threads cooperate via the shared memory. One disadvantage of this strategy is bank conflict - concurrent access to the shared memory of the same bank by threads from the same warp will be forced to be sequential. Besides, there are two open-source softwares providing GPU implementations of KECCAK: ccminer (ref. http://ccminer.org) and hashcat (ref. https://hashcat.net) in CUDA and OpenCL, respectively.

Having learnt from the existing works and codes, we implemented KECcaK following two different strategies: one thread for one KECCAK or one warp for one Keccak. From experimental results, we find that one thread for one Keccak gives a better number of KECCAK- $f$ evaluations per second. So we adopt this strategy in this paper. More detailed techniques of implementation optimization are introduced in Appendix B.1.

Benchmark. With all the optimization techniques in mind, we implemented KECCAK- $f[1600]$ in CUDA, and have it tested on NVIDIA GeForce GTX1070 and NVIDIA GeForce GTX970 graphics cards. The hardware specifications of GTX1070 and GTX970 are given in Appendix B.2.

Table 4: Benchmark of our KECCAK implementations in CUDA

| Target | KECCAK- $f$ evaluations per second | GPU |
| :--- | :---: | :---: |
| KECCAK- $f[1600] v 1$ | $2^{28.90}$ | GTX1070 |
| KECCAK- $f[1600] v 2$ | $2^{29.24}$ | GTX1070 |
| KECCAK- $f[1600] v 1$ | $2^{27.835}$ | GTX970 |
| KECCAK- $f[1600] v 2$ | $2^{28.37}$ | GTX970 |

Table 4 presents the performance, where KEccak- $f[1600] v 1$ and KEccak$f[1600] v 2$ are our implementations used to search for differential trails and to find real collisions in the brute-force stage, respectively. The difference between the two versions is: KECCAK- $f[1600] v 1$ copies all digests into global memory, and KECCAK- $f[1600] v 2$ only copies the digest into global memory when the resulted digest equals to a given digest value. For both versions we did not include the data transfer time. It can be seen that roughly GTX1070 can be $2^{8}$ times faster than a CPU core. The source codes of these two versions are available freely via http://team.crypto.sg/Keccak_GPU_V1andV2.zip.

### 5.5 Searching results

Some of the best differential trail cores we obtained with the help of GPU are listed in Table 5. As can be seen, Trail cores No. $1 \sim 3$ are all suitable for collision attacks against KECcak[1440,160,5,160] and the 5-round SHAKE128. Trail core

No. 4 is sufficiently good for collision attacks against $\operatorname{Keccak}[640,160,5,160]$. To mount collision attacks on 5-round SHA3-224, 5-round SHA3-256 and KECCak[1440,160,6,160], Trail core No. 3 and 5 are used respectively. Trail core No. 6 is slightly different from the other cores as it leads to an inner collision of the last 160 state bits for KЕССak[1440,160,6,160]. Details of these differential trail cores are provided in Appendix D.

Table 5: Differential trail cores of KECCAK, where $160^{-1}$ stands for an inner collision on the last 160 bits of the state.

| No. | $r+c$ | $\# A S\left(\alpha_{2}-\beta_{2}-\beta_{3}-\beta_{4}^{d}\right)$ | $w_{1}-w_{2}-w_{3}-w_{4}^{d}$ | $w_{2}+w_{3}+w_{4}^{d}$ | $d$ | Trail info. |
| :---: | ---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 1600 | $102-8-8-2$ | $240-19-16-4$ | 39 | 256 |  |
| 2 | 1600 | $88-8-7-0$ | $195-21-15-0$ | 36 | 256 | Table 9 |
| 3 | 1600 | $59-10-9-0$ | $127-24-19-0$ | 43 | 256 |  |
| 4 | 800 | $38-8-8-0$ | $85-20-16-0$ | 36 | 160 | Table 10 |
| No. | $r+c$ | $\# A S\left(\alpha_{2}-\beta_{2}-\beta_{3}-\beta_{4}-\beta_{5}^{d}\right)$ | $w_{1}-w_{2}-w_{3}-w_{4}-w_{5}^{d}$ | $w_{3}+w_{4}+w_{5}^{d}$ | $d$ | Trail info. |
| 5 | 1600 | $127-9-8-8-10$ | $292-25-18-16-16$ | 50 | 160 | Table 11 |
| 6 | 1600 | $135-8-7-8-15$ | $317-17-14-16-20$ | 50 | $160^{-1}$ | Table 12 |

## 6 Experiments and Results

### 6.1 Summary of Collision Attacks

In this section, we employ 4 -round (5-round) trail cores in Table 5 to mount collision attacks against 5-round (6-round) KECcAK. Recall that our collision attack consists of a connecting phase and a brute-force searching stage. Let $T_{c}$ denote the time consumed by the connecting stage and $T_{b}$ by the brute-force searching stage. After the connecting stage, a message space with DF degrees of freedom is returned by the connector. If $D F$ is greater than the weight $w$ of the differential trail over the last rounds beyond the connector, a colliding pair will be found in the brute-force searching stage with a very high probability.

Table 6 summarizes seven collision attacks we obtained (for six KECCAK variants), and the corresponding timings, DFs of the returned message space and $w$ of the differential trail. With the attacks on KECcak $[1440,160,5,160]$, KEc$\operatorname{CAK}[640,160,5,160]$ and $\operatorname{KECCAK}[1440,160,6,160]$, we provide the first solutions to three instances of KECCAK contest [2]. The source codes for verifying the seven collisions are available via http://team.crypto.sg/VerifyCollisions. zip.

Collisions of 5-round instances. Take the first target in Table 6 as an example. We apply Trail core No. 2 to the collision attack on KECCAK[1440, 160, 5, 160].

Table 6: Experimental details of our collision attacks.

| Target $[r, c, d]$ | $n_{r}$ | Trail Core | $T_{c}$ | DF | $w$ | $T_{b}$ | Collision |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| KECCAK $[1440,160,160]$ | 5 | No. 2 | $9.6 \mathrm{~s}^{2}$ | 162 | 36 | 2.48 h | Table 13 |
| KECCAK $[640,160,160]$ | 5 | No. 4 | 0.5 h | 56 | 35 | 2.67 h | Table 14 |
| SHAKE128 | 5 | No. 1 | 0.4 h | 94 | 39 | 0.5 h | Table 15 |
| SHA3-224 | 5 | No. 3 | 11.7 h | 83 | $36.70^{*}$ | 29 h | Table 16 |
| SHA3-256 | 5 | No. 3 | 428.8 h | 37 | $36.70^{*}$ | 45.6 h | Table 17 |
| KECCAK $[1440,160,160]$ | 6 | No. 5 | 4.5 h | $174^{\dagger}$ | $47.81^{*}$ | $112 \mathrm{~h}^{\ddagger}$ | Table 18 |
| KECCAK $\left[1440,160,160^{-}\right]$ | 6 | No. 6 | 7.5 h | $145^{\dagger}$ | $48.83^{*}$ | 60200 h | Table 19 |

${ }^{2}$ Usual computers evaluate around $2^{20} \sim 2^{22}$ full Keccak evaluations per second.

* The weight takes multiple trails of the last two rounds into consideration.
${ }^{\dagger} \mathrm{DF}$ of the message space returned by the 2-round connector for the first two rounds.
${ }^{\ddagger}$ The actual time using three NVIDIA GeForce GTX970 GPUs while elsewhere it refers to the time when one CPU core is used.

After solving the 2-round connector in 9.6 seconds, the resulting solution space has a degree of freedom DF of 162 which is sufficiently larger than 36 , i.e., the weight of the differential trail over the remaining 3 rounds. The time for searching a collision is 2.48 core hours. We give one example of collisions in Table 13. The attacks on other 5 -round targets work similarly.

Collision on Keccak $[1440,160,6,160]$. Our collision attacks on 6 -round instances exploit 3 -round connectors to the first three rounds, where a 2 -round connector for the first two rounds is constructed first, based on which adaptive 3 -round connectors are constructed. The attack on KEccak[1440, 160, 6, 160] uses Trail core No. 5. The 2-round connector of the first two rounds succeeds in 4.5 core hours and returns a message space with 174 degrees of freedom. Every time the second round connector outputs a subspace of messages (i.e., solution of 3 -round connectors) with $\mathrm{DF} \in[40,45]$. In order to find one colliding pair, at least $2^{47.81}$ pairs of messages are required. This could be achieved by repeating the second round connector for $2^{2.81} \sim 2^{7.81}$ times. By running our CUDA implementation on three NVIDIA GeForce GTX970 GPUs, the first collision is found in 112 hours, which equals to $2^{49.07}$ message pair evaluations ${ }^{1}$. An example of collision is given in Table 18.

Inner collision on the last 160-bit of Keccak[1440, 160, 6, 160] When an inner collision on the last 160 bits of the state is obtained (i.e., a collision on capacity bits), we could then construct state collisions of messages of any block length more than 2 by choosing the next message block properly. Specifically, we

[^1]choose certain values for the next message block such that the $r$-bit difference of the state is cancelled after absorption. Thus, in the next permutation the whole states become identical. This property maintains if all subsequent message blocks are identical, and in such situations we can obtain a collision on the final digest with certainty. With this in mind, a 6-round collision attack on the last 160 bits of KECCAK[1440, 160, 6,160 ] is mounted. Similar to the normal collision attack, a 3-round connector is constructed based on Trail core No. 6. The size of the solution space we obtained is around $2^{58} \sim 2^{64}$ which exceeds the data complexity required. To compare with the efficiency of GPU implementations, this collision is obtained with 172 CPU cores consuming approximately 60200 core hours in total. The first collision occurs after enumerating approximately $2^{47.4}$ message pairs. An example of a colliding pair is given in Table 19.

### 6.2 Technical Analysis of the Results

Choice of $\boldsymbol{\beta}_{\boldsymbol{1}}$. In our 2-round connector, $\beta_{1}$ is randomly chosen such that $\beta_{1} \rightarrow \alpha_{2}$ is of the best probability, except KECCAK[1440,160,5,160] for which the connector randomly chooses any compatible $\beta_{1}$. The DF of the message space returned by the connector for $\operatorname{KECCAK}[1440,160,5,160]$ is 162 . In comparison, the first 2-round connector for KECCAK $[1440,160,6,160]$ returns a message space with $\mathrm{DF}=174$, even though its weight of the second round $w_{1}$ which is covered by the connector is much larger than that of $\operatorname{KECCAK}[1440,160,5,160]$, as can be seen in Table 5. This confirms the effect of the choice of $\beta_{1}$ on the resulted DF.

S-box linearization. Full S-box linearizations are enforced in 2-round connectors for $\operatorname{KEccak}[1440,160,5,160]$, $\operatorname{KECCAK}[640,160,5,160]$ and SHAKE128, as well as the first 2-round connector for KECCaK[1440, 160, 6, 160]. Namely, the first round is fully linearized no matter whether it is necessary or not. For SHA3224 , SHA3-256, S-boxes of the first round are fully linearized only when necessary, otherwise non-full S-box linearizations are used. It is the same case for S-boxes in the second round while constructing the second round connector for KECCAK[1440, 160, 6, 160].

It is worth noting that our collision attacks on 5-round SHA3-256 and KECCAK $[1440,160,6,160]$ are not possible without the techniques of non-full S-box linearization. Take the 2-round connector for SHA3-256 as an example. While partially linearizing the first round, (besides the S-boxes with DDT entry 2 or 4 ) there are 26 S -boxes in the first round exempted from consuming extra degrees of freedom due to techniques of non-full S-box linearization. Finally, our 2-round connector returns a space of messages with DF almost as large as the weight of the differential trail. Moreover, in this space of messages, we find only one collision. This in turn proves the usefulness and effectiveness of the non-full S-box linearization techniques.

## 7 Conclusion

We observed that connectors of KECCAK can be extended to multiple rounds using S-box linearizations. In this paper, two types of S-box linearization are proposed, i.e., full S-box linearization and non-full S-box linearization. By linearizing S-boxes in the first round, we extend Dinur et al.'s 1-round connector to two rounds. Further, by applying linearization to the first two rounds, 3-round connectors became possible. We combined our connectors with suitable differential trails searched with dedicated strategies, and then obtained seven practical collisions on six round-reduced SHA-3 and KECCAK variants, including 5-round SHAKE128, 5 -round SHA3-224, 5-round SHA3-256, a 6 -round instance of KECCAK contest and a variant of it. So far, these are the best collision attacks on roundreduced SHA-3 and Keccak contest instances.

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## A Linearizable Affine Subspaces

Table 7: The 80 2-dimensional linearizable affine subspaces of Keccak S-box


## B GPU Implementation

## B. 1 Techniques for GPU implementation optimization

The techniques commonly used to optimize the CUDA program include memory optimizations, execution configuration optimizations, and instruction-level parallelism (ILP).

Memory optimizations. Usually registers have the shortest access latency compared with other memory, so keeping data in registers as much as possible improves the efficiency in general. However, dynamically indexed arrays cannot be stored in registers, so we define some variables for the 25 lanes by hand in order to have them stored in registers. Constant memory is a type of read-only memory. When it is necessary for a warp of threads read the same location of memory, constant memory is the best choice. So we store 24 round constants on it. When the threads in a warp read data which is physically adjacent to each other, the texture memory provides better performance than global memory, and it reduces memory traffic as well. So we can bind input data and some frequent accessed read-only data with texture memory.

Execution configuration. With resources like registers and shared memory limited in each graphic card, the number of threads running in each block will affect the performance since too many threads running in parallel will cause a shortage of registers and shared memory allocated to each thread, while too few parallel threads reduce the overall performance directly. According to our experiments, one block with 128 threads gives the best performance.

Instruction-level parallelism. From [28], hashcat, and ccminer, we see that forcing adjacent instructions independent gives better performance. Without prejudice to the functions of the program, we can adjust the order of instructions to improve the efficiency of the operations. In addition, loop unrolling [20] is also a good practice to obtain ILP.

## B. 2 Hardware specification of GPU

Table 8: The hardware specification sheet of GTX1070 and GTX970

|  | GTX1070 | GTX970 |
| :---: | :---: | :---: |
| Core Clock Rate | 1645 MHz | 1228 MHz |
| Multiprocessors | 16 | 13 |
| Regs Per Block | 65536 | 65536 |
| Total Global Memory | 8105.06 MiB | 4036.81 MiB |
| Bus Width | 256 bits | 256 bits |
| Memory Clock Rate | 4004 MHz | 3505 MHz |
| L2 Cache Size | 48 KiB | 48 KiB |
| Shared Memory Per Block | 48 KiB | 48 KiB |
| Total Constant Memory | 64 KiB | 64 KiB |

## C Details of Differential Trail Search

## C. 1 Analysis of the Starting Point of the Search

The following paragraphs describe how specific attributes of differential trails are settled down. We take the 5 -round KECCAK collision situation as an example to explain why those kinds of trails are necessary.

Searching from light $\boldsymbol{\beta}_{3}$ 's. Our initial goal is to find collisions for 5-round Keccak. To facilitate a 5 -round collision of KECCAK, we need to find 4-round differential trails satisfying the three requirements mentioned in Section 5.2. However, it is difficult to meet all of them simultaneously even though each of them can be fulfilled solely.

We explain as follows. Since we aim for practical attacks, $w_{2}+w_{3}+w_{4}^{d}$ must be small enough, say 55 . That is to say, the last three rounds of the trail must be light and sparse. When we restrict a 3-round trail to be lightweight and extend it backwards for one round, we almost always unfortunately get a heavy state $\alpha_{2}$ (usually $\# A S\left(\alpha_{2}\right)>120$ ) whose weight may exceed the TDF. We take KECCAK-224 as an example. The TDF of KECCAK-224 is 191, which indicates $\# \operatorname{AS}\left(\alpha_{2}\right)<92$ as the least weight for an S-box is 2 . For a lightweight 3 -round trail, it satisfies Requirement (1) occasionally. The greater $d$ is, the fewer trails satisfy Requirement (1).

With these requirements in mind, we search for 4-round differential trail cores from light middle state differences $\beta_{3}$ 's. From light $\beta_{3}$ 's we search forwards and backwards, and check whether Requirement(1) and (2) are satisfied respectively; once these two requirements are satisfied, we compute the weight $w_{2}+w_{3}+w_{4}^{d}$ for brute force, hoping it is small enough for practical attacks.
$\boldsymbol{\alpha}_{\mathbf{3}}, \boldsymbol{\alpha}_{\boldsymbol{4}}$ in CP-kernel. The designers of KECCAK show in [4] that it is not possible to construct 3 -round low weight differential trails which stay in CPkernel. However, 2-round differential trails in CP-kernel are possible, as studied in $[9,14,21]$.

We restrict $\alpha_{3}$ in CP-kernel. If $\rho^{-1} \circ \pi^{-1}\left(\beta_{3}\right)$ is outside the CP-kernel and sparse, say 8 active bits, the active bits of $\alpha_{3}=L^{-1}\left(\beta_{3}\right)$ will increase due to the strong diffusion of $\theta^{-1}$ and the sparseness of $\beta_{3}$. When $\# A S\left(\alpha_{3}\right)>11$, the complexity for searching backwards for one $\beta_{3}$ is greater than $2^{34.87}$ which is too time-consuming. On the other hand, we had better also confine $\alpha_{4}$ to the CP-kernel. If not, the requirement $\alpha_{n_{r}}^{d}=0$ may not be satisfied. As can be seen from the lightest 3 -round trail for Keccak- $f$ [1600] [14], after $\theta$ the none-zero difference bits are diffused among the state making a 224 -bit collision impossible (a 160 -bit collision is still possible). So our starting point is special $\beta_{3}$ which makes sure $\alpha_{3}=L^{-1}\left(\beta_{3}\right)$ lies in CP-kernel, and for which there exists a compatible $\alpha_{4}$ in CP-kernel. Fortunately, such kind of $\beta_{3}$ 's can be obtained with KeccakTools [5].

## C. 2 Detailed Algorithm for Searching Differential Trails

In this section, we describe more at length about the algorithm for finding differential trails. Firstly, light $\beta_{3}$ 's, namely, 2-round in CP-kernel trail cores are generated with KeccakTools [5], and then extended one round forwards and backwards respectively to find suitable 4 -round trail cores. Note that all extensions should be traversed. Given a $\beta_{3}$, suppose there are $C_{1}$ possible one-round forward extensions and $C_{2}$ one-round backward extensions. These two numbers are mainly determined by the active S-boxes of $\beta_{3}$. If the number of active S boxes is $A S$, then roughly $C_{1} \geq 4^{A S}$ and $C_{2} \geq 9^{A S}$ according to the DDT. In the search for 4 -round trail cores, $C_{2}$ is the dominant time complexity, while for 5 round trail cores of KECcaK $[1440,160,6,160]$, we start from $\left(\beta_{3}, \beta_{4}\right)$ generated by KeccakTools, and $C_{1}$ is almost as large as $C_{2}$.

- Generate $\beta_{3}$ such that $\alpha_{3}=L^{-1}\left(\beta_{3}\right)$ lies in CP-kernel, and that there exists a compatible $\alpha_{4}$ in CP-kernel, using TrailCoreInKernelAtC of KeccakTools [5] where the parameter aMaxWeight is set to be 60 . We obtain more than 3000 such cores.
- For each $\beta_{3}$, if $C_{1} \leq 2^{36}$, we traverse all possible $\alpha_{4}$, compute $\beta_{4}$, and check whether the collision is possible for $\beta_{4}$. If yes, keep this $\beta_{3}$ and record this forward extension, otherwise, discard this $\beta_{3}$.
- For remaining $\beta_{3}$, if $C_{2} \leq 2^{35}$, try all possible $\beta_{2}$ which are compatible with $\alpha_{3}=L^{-1}\left(\beta_{3}\right)$, and compute $A S\left(\alpha_{2}\right)$ where $\alpha_{2}=L^{-1}\left(\beta_{3}\right)$. If $A S\left(\alpha_{2}\right) \leq 110$, check whether this trail core $\left(\beta_{2}, \beta_{3}, \beta_{4}\right)$ is practical for the collision attack.

To find a 5 -round trail core for $\operatorname{Keccak}[1440,160,6,160]$, we adapt the second step as follows.

- For each $\beta_{3}$, extend forwards for one round using KeccakFTrailExtension of KeccakTools [5] with weight up to 45 . For each generated 2 -round core $\left(\beta_{3}, \beta_{4}\right)$, if $C_{1} \leq 2^{36}$ for $\beta_{4}$, traverse all possible $\alpha_{5}$ and compute $\beta_{5}$. Check whether there exists an $\alpha_{6}$ such that $\alpha_{6}^{d}=0$. If yes, record the three-round core $\beta_{3}, \beta_{4}, \beta_{5}$, otherwise, discard the $\beta_{3}$.


## D Differential Trails

In this section, we give details of differential trails of Keccak mentioned in Section 5. Actually, we present trail cores. For example, a 4 -round tail core $\left(\beta_{2}, \beta_{3}, \beta_{4}\right)$ consisting of three state differences represents a set of 4 -round differential trails

$$
\alpha_{1} \xrightarrow{L} \beta_{1} \xrightarrow{\chi} \alpha_{2} \xrightarrow{L} \beta_{2} \xrightarrow{\chi} \alpha_{3} \xrightarrow{L} \beta_{3} \xrightarrow{\chi} \alpha_{4} \xrightarrow{L} \beta_{4} \xrightarrow{\chi} \alpha_{5}
$$

where $\alpha_{5}$ is compatible with $\beta_{4}$ and $\beta_{1} \rightarrow \alpha_{2}$ is of the least weight determined by $\beta_{2}$. In our collision attacks on 5 -round ( 6 -round) КЕссак, 4 -round (5-round) trail cores are needed.

The 1600 -bit state is displayed as a $5 \times 5$ array, ordered from left to right, where ' $\mid$ ' acts as the separator; each lane is denoted in hexadecimal using littleendian format; ' 0 ' is replaced with ' - ' for differential trails.

## E Instances of Collisions

In this section, we give instances of collisions against $\operatorname{Keccak}[1440,160,5,160]$, Keccak[640, 160, 5, 160], Keccak [1440, 160, 6, 160], 5 -round ShaKE128, 5 -round SHA3-224, 5 -round SHA3- 256 respectively. Note that we denote two colliding messages with $M_{1}, M_{2}$.

Table 9: Trail core No. $1 \sim 3$ used in the collision attacks

| Trail core No. 1, used in the collision attack of 5-round SHAKE128 |  |  |
| :---: | :---: | :---: |
| $\beta_{2}$ |  | $2^{-19}$ |
| $\beta_{3}$ |  | $2^{-16}$ |
| $\beta_{4}$ |  | $2^{-4}$ |


| Trail core No. 2, used in the collision attack of Keccak[1440, 160, 5, 160] |  |  |
| :---: | :---: | :---: |
| $\beta_{2}$ |  | $2^{-21}$ |
| $\beta_{3}$ |  | $2^{-15}$ |
| $\beta_{4}$ |  | 1 |


| Trail core No. 3, used in the collision attack of 5-round SHA3-224 and 5-round SHA3-256. The probability is $2^{-36.70}$ considering multiple trails of the last two rounds. |  |  |
| :---: | :---: | :---: |
| $\beta_{2}$ |  | $2^{-24}$ |
|  |  |  |
| $\beta_{3}$ |  | $2^{-19}$ |
|  | -1\|-------------1--|-----------------|------4- |  |
| $\beta_{4}$ |  | 1 |

Table 10: Trail core No. 4 , used in the collision attack of Keccak[640,160,5,160]. $\beta_{4}$ has two choices.

| $\beta_{2}$ |  | $2^{-20}$ | $\beta_{4}^{1}$ |  |
| :---: | :---: | :---: | :---: | :---: |
| 3 |  | $2^{-16}$ | $\beta_{4}^{2}$ |  |

Table 11: Trail core No. 5, used in the collision attack of Keccak[1440, 160, 6, 160]. The total probability is $2^{-72.81}$ considering multiple trails of last two rounds. The probability of last three rounds is $2^{-47.81}$.

| $\beta_{2}$ |  | $2^{-25}$ |
| :---: | :---: | :---: |
| $\beta_{3}$ |  | $2^{-18}$ |
| $\beta_{4}$ |  | $2^{-16}$ |
| $\beta_{5}$ |  | $2^{-16}$ |

Table 12: Trail core No. 6, used in the last 160-bit collision attack of $\operatorname{KECCAK}[1440,160,6,160]$. The total probability is $2^{-65.83}$ considering multiple trails of last two rounds. The probability of last three rounds is $2^{-48.83}$.

| $\beta_{2}$ |  | $2^{-17}$ |
| :---: | :---: | :---: |
| $\beta_{3}$ |  | $2^{-14}$ |
| $\beta_{4}$ |  | $2^{-16}$ |
| $\beta_{5}$ |  | $2^{-20}$ |

Table 13: Collision for the contest instance Keccak[1440, 160, 5, 160]

| $M_{1}$ | FE04ABD5B6CB82B0\|61CC2361C8649AFA|8192015AE5A4CD17|18CA3F02D74C2A1E|2FD4F65B9B8954B7 |
| :---: | :---: |
|  | C3E83AFE2554E96C\|D40EA8CEA0A5D897 | 13F77204F374C0FA | C50800E587690A4A | A818DB7455FCOEEC |
|  | D165BE70A75C79BD \|A5E719D73D740232 |3129EFB27EE7D766 | CBC2BA892C29908E|67723AF6944A3147 |
|  | 5BC2E4ECA7B4EA3A\| 1BCF8762A0E3E233|18A28C2474950C18|DB52DE89434D26B2 | A642C6B06F111CAC |
|  | 7866F86D04A61505\|B2B98F0702FE80C1 100000000F8514DDO|0000000000000000|0000000000000000 |
| $M_{2}$ | E8AC1434F0FEBE8EI85FDE653956EF372 \|7B1CEE0DB775BFC0|F80D568E8D0A97CCI9E3E49ADCFFC18BF |
|  | 8DC76DBCC537ED14\|AD6CCD45B0990F18|A9CF7A6399115588|970C9BC4ED512782 |08A0B2CA4DDD83BF |
|  | 9F13F101BF5BE7EC\|7B533F4848528805|21F6E6F54C30B798|OBDA3E2038C5FAA6|84808A532E7E1407 |
|  | 8D2DE036C7989316\|2778E5A9A1717231|4CC28A17BA036E68|42DA44B9D77DE6F6|56C2556B467D1BBC |
|  | AD99AD3F868AD505\|2F4EB3D410EC316E|0000000092856EAD |0000000000000000|0000000000000000 |
| digest | 0887B202077B082F \|E6D2B9B1BA9B51AC| 7BF7B1AF |

Table 14: Collision for the contest instance Keccak[640, 160, 5, 160].

| $M_{1}$ | 297DB73F \| CE5FB46D | 63EFD5AB | AB75DBB2 | 020119E7 | $M_{2}$ | 5B150BCB\|C0F3F2FC|5907B5A5|22736DC3|914CF0C |
| :---: | :---: | :---: | :---: |
|  | 06927773\|A645A6A4|68E6E3F8|15282462|633AAB83 |  | 87477D63\|A675A649|8BBEA96F | 52EB8AE3|19402D41 |
|  | 96C7A5FB\|5E4CBEB5|92614C96|DD9647DA|D4B0094F |  | D9FB4CC3\|669FD630|D8C9FC71|57558554|0662F64A |
|  | 4C68376F\|D3B63751|6286AB56|DE577A52|9003EA0F |  | 64B4B5C5\|7F12BF56|2BEADBF0|F6207B10|F2FD9787 |
|  | 00000000100000000100000000100000000100000000 |  | 00000000100000000100000000100000000100000000 |
| digest | F90B5ABA \| 7430682D | 85668C62 |66E1B0AD |B052AC35 |  |  |

Table 15: Collision for 5-round SHAKE128

| $M_{1}$ | 0A3E44EBE62104A0\|1E8617C352E80FBC|B69A38114369962F|1237F5EEA8045DAB|D4144AC64E22044C 1240A93D79FCCB2E|C8C63A830CBACFFC|B36B34C0E1719824|F94803ECC5586680|ACF133FE29839CAD CA5F88F260DEFAA7|972FE7E882A4AB03|D11344BE12431A54|814488EBAE68F93B|56D10CF0251FAED3 77A665FCC5F52D9F|D50EF69FAB128ACC|87F3F1816E740894|770D4D55489234B0|737134B1243F3A3D FE0E2AE7F23D8E40|0000000000000000|0000000000000000|0000000000000000|0000000000000000 |
| :---: | :---: |
| $M_{2}$ | 0CDDD5D25A8BD7CA\|F71D259EE445A4D8|EB84F177D51C9D45|0A70C1FC50024C24|7108096E3F024F63 3B8D1EEBC5E9150E|EADCC9FB19824E75|B8A97CB74697BAAD|5988E2CD64063AD1|FB55123185E2E4A4 E74FF74033CA1486|915F016B41BDAF6B|145441AAC9EFA342|D9A609CF15E6C626|5609C4F58F5DEE0A AB4E178C43BA8687|3774B01D78F2ABE4 |AA35E3D371664594|A26EAD50F73069A7 | DE4E25F8A0F8E928 FE431BE34F8371D8|0000000000000000|0000000000000000|0000000000000000|0000000000000000 |
| digest | 9D2E953AD7C6A939\|326F59A68A6016EF |A71EAFEE371700D7 | 3C463D5D098D9B76 |

Table 16: Collision for 5-round SHA3-224

| $M_{1}$ | BA651BB077352C22\|020878C86F47A777|A68806B74E37F538|FC14E1158751958F|38364A327DE40704 |
| :---: | :---: |
|  | F24920D7259B9A3B \| 98E0602DA4CF65CB | A0CD3329C8DC6E96| 08050DBA7BFFB106|3B74AE3D29AA1F74 |
|  | E0606BA4A9E5C\|E7E83EOC5A8D05EE |78E2095685EEABBC | 114AD7006B5B4905|2FFD9CF449950256 |
|  | EAB168AEC88D6917\|294C016FD4B8C424|EA95CDFF2F0B0939 |0000000000000000|0000000000000000 |
|  | $0000000000000000\|0000000000000000\| 0000000000000000\|0000000000000000\| 0000000000000000$ |
| $M_{2}$ | DF3E25D454129D90\|743A8BB224F5D398|803A36FD69A1590C|26E9CC61513C3B2C|52777F2E82CBABF9 |
|  |  |
|  | A88F4E0FF407024C \| 4102F764F58A1C70|68D868FC2E2EE351 |554ED55BF9A456A7 | CCD9BE774A51 |
|  | C1C74E8F2CF70106\| DAC93A2C7E587301|E4294D74AD68A5F6|0000000000000000|000000000000 |
|  | 000000000000000010000000000000000100000000000000001000000000000000010000000000000000 |
| digest | 79D8E749B6633429\|8006840F0FA7F0B1|4FF9CCCF110BDOD4| FB35AD94 |

Table 17: Collision for 5-round SHA3-256


Table 18: Collision for the contest instance $\operatorname{Keccak}[1440,160,6,160]$

| $M_{1}$ | DA27ABE5B7EC359D\|328A2AB4CD0E256A|00DBDEECA184390E|3843F66481C745F4|DDF83BEF39D4F594 46BA2A960272C97A | 8CC8CE3E13185558|2D7C6CC662546532|4D8DCDC25DC7F4B8|574252F43F85BF94 BDCFA2D6B04CBDEE|208D7A02168A7596|AFE7C652F0A68792|467C04748D85916F|F1BFEAF63C4B97C3 C2B0AAEA35887CD4|72A3D23F9D84434D|97A5D9A090590B61|BBE1EC62DBD4327E|64284BCB9BE462C5 8843CBC8B55E106A |DD3DD96A1AC48100|00000000E9151D67|0000000000000000|0000000000000000 |
| :---: | :---: |
| $M_{2}$ | 5A0C640730278910\|32C1A7D724790C0B | 8BCE75C46404A83A | 7FCE23E92ECE7E31|1BEE08F9F932C785 3969BA55EB6B17F9|E82948B06C21C6A8|AF42ACEF22202C1F |A9C1BD90BF96FB60|0F98E27C36B57BDA A02B26453D88C70F | 5EC5F74DC919C7E6|31391D7A23A3C8DD | C0BECDAD0AC7F275| 14FA28F6B2C9D390 69F67EEAEF258217|159B7FEDCED37178|DA89C2B0291CCA7D | 7BDDE79F989414AE|3088CBE192E15B4B 138617865C48CEA9|2A917CE5E3AD1374|0000000098425E60|0000000000000000|0000000000000000 |
| digest | 602133DD97109089\|611B5125914B0F05| 532B96C0 |

Table 19: Collision on the last 160 bits of Keccak[1440, 160, $6,160^{-1}$ ]
6FDB2A20F8992431|2087B01388592A01|D114527BE00F3C5A | 2CCAE47DF6025BBB| 276C0E5A2B64FD12
F7C10ACF1A83B66B| 9558BC260011465E | 06C34BD57E6F00CA |81BF6D44927DA86A |F1A1EE2D0F329641
$M_{1}$ 78367DOBOCOA12BF | A932C8A9906D8577 | 78116644C91F9867 | O510A128067B884D | 63F5D9D184C7D6E2 02BEA4FBB2F9DA4A | CED47DD4D59E41F8|DDA6E9AC3E28B45A|FD12804E77348FB8|557A955C5CD88D02 96E21F07B25C1F2E | 104767 C63F7C2AB4 | 00000000C60B57C1 $0000000000000000 \mid 0000000000000000$ 033C171CD7A65128|45F8C2FB4052939B| 55A9BD5D6B4533A6|69DF5701873D16E7 | ADC98EEA28325D2C 8EF732C6DAED91BE | 1A7AF5636039685D | 84915390F1F832B6| 92677C0DFB5DB15B |998A4A254F9C88EO F810289754AAOC71|28ACE4F7585781D4|9400E644963033D7|3432AEC4E5231119|23E1FD9183D58AOC CFAA789629A11457 |E7681880B18D4DA6|6A2C40394F8E1172|E9562446427C4AAC |D97ABBDB097B07E2 73D4472B79731977 | 37EE2EC25F6706B6|00000000AF85B4FB | $0000000000000000 \mid 0000000000000000$ |1BF0161E867C399C |DE5348CD351076D5


[^0]:    * This paper is prepared based mainly on [24] and [26]. The work was done when all the authors were working with Nanyang Technological University in Singapore.

[^1]:    ${ }^{1}$ Our experiment shows $2{ }^{28.87}$ pairs of 5-round KECCAK could be evaluated per second on NVIDIA GeForce GTX970 graphic card.

