

RIPEMD with Two-Round Compress Function Is Not Collision-Free

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Abstract. In 1990 Rivest introduced the cryptographic hash function MD4. The compress function of MD4 has three rounds. After partial attacks against MD4 were found, the stronger mode RIPEMD was designed as a European proposal in 1992 (RACE project). Its compress function consists of two parallel lines of modified versions of MD4-compress. RIPEMD is currently being considered to become an international standard (ISO/IEC Draft 10118-3). However, in this paper an attack against RIPEMD is described, which leads to comparable results with the previously known attacks against MD4: The reduced versions of RIPEMD, where the first or the last round of the compress function is omitted, are not collision-free. Morcover, it turns out that the methods developed in this note can be applied to find collisions for the full MD4.

Key words. Dedicated hash functions, RIPEMD, MD4, RACE project, ISO/IEC 10118-3.

1. Introduction and Summary

In 1990 Rivest [4] introduced the hash function MD4. The MD4 algorithm is defined as an iterative application of a three-round compress function.

In view of an attack against the last two rounds of the compress function of MD4, which was found by den Boer and Bosselaers [2], the stronger mode RIPEMD [1] was designed as a European proposal in 1992 (RACE project). The compress function of RIPEMD consists of two parallel lines of a modified version of the MD4 compress function.

In what follows we show that if the first or the last round of the RIPEMD compress function is omitted, then collisions can be found by an attack starting with a basically very simple idea.

Thus we describe an attack against RIPEMD leading to results which are comparable

with those previously known for MD4 (see [2] and [6]), although our attack requires more computational effort.

Any attack against RIPEMD has to overcome the problem that the two parallel lines of the compress algorithm have to be handled simultaneously. However, as we shall see this can be managed if we cancel the first or the last round. This weakness is caused mainly by the fact that in both lines the message blocks are applied in exactly the same ordering; while our attack in principle does not depend on the particular choices of Boolean functions, shifts, and constants (see the Appendix for the definition of RIPEMD).

Therefore, although RIPEMD is certainly stronger than MD4, in our opinion the intention of the design has not been reached. The effect of combining two parallel chains of a modified MD4-compress, as is realized in the present version of RIPEMD, is not as strong as should be expected when the computational effort is doubled.

It has turned out that the methods developed in this note can be applied to find collisions for the full MD4 (see Section 6). This attack is explained in [3].

It remains a challenging task trying to attack MD5, a strengthened version of MD4 due to Rivest [5], with the techniques presented here (see "Note Added in Proof", p. 68).

Terminology and Basic Notation. Using the term "collision of a compress function" we assume that the corresponding initial values coincide for both inputs. For "pseudocollisions" this is not required. However, the latter are of much less practical importance and are not considered here.

Throughout, all occurring variables and constants are 32-bit quantities, and accordingly the value of an expression is its remainder modulo 2^{32} . The symbols \land , \lor , and \oplus are used for bitwise AND, OR, and XOR, respectively. For a 32-bit word X, let $X^{\ll s}$ denote the 32-bit value obtained by circularly shifting (rotation) X left by s bit positions for $0 \le s < 32$. To complete this definition set $X^{\ll (-s)} = X^{\ll (32-s)}$. If X is an expression then, of course, evaluate it before shifting.

2. Main Result and Plan of the Attack

By RIPEMD^[12] (resp. RIPEMD^[23]) we denote the hash functions, which are obtained as reductions of RIPEMD by canceling the last (resp. first) round of the compress function. (See the Appendix for details.) We can state our main result as follows:

 $RIPEMD^{[12]}$ and $RIPEMD^{[23]}$ are not collision-free.

Empirical observations have shown that the attack described below, which leads to collisions, requires an average of about the same computational effort as 2³¹ computations of a (two-round) compress function. Concretely this means that it takes on average about 1 day on a 486-PC (66 MHz) to compute collisions.

Our attack is separated into three parts:

Part I: Inner Collisions. The basic idea is to find collisions for compress^[12] or compress^[23] by taking two collections X_i , \tilde{X}_i (i < 16) of words such that, for some i_0 , we have $X_{i_0} \neq \tilde{X}_{i_0}$, but $X_i = \tilde{X}_i$ for $i \neq i_0$. When the compress values of the inputs X

and \tilde{X} are computed, then in the first round everything coincides until we come to step i_0 . At this point the computations for X and \tilde{X} become different. However, in the second round, X_{i_0} (resp. \tilde{X}_{i_0}) has to be applied again at a certain step. After that, in the remaining steps, all inputs coincide again. Thus it is natural to try to control the computation in such a way that after the second application of X_{i_0} and \tilde{X}_{i_0} the contents in the registers coincide.

Therefore we first concentrate on the steps between the first and second application of X_{i_0} (resp. \tilde{X}_{i_0}). In this part of the attack we try to find suitable values for the contents of the registers after step $i_0 - 1$ and suitable values for X_{i_0} , \tilde{X}_{i_0} , and for the $X_i = \tilde{X}_i$ occurring in the considered segment of the compress function.

Note that the ordering, in which the variables X_i are applied, is exactly the same for compress^[12] and compress^[23]. In both cases we take $i_0 = 13$, since for this choice we get the shortest corresponding segment of the compress function. We have enough variables to find a simultaneous collision for the considered segment of the left and right line ("inner collision"), and, on the other hand, enough variables remain free for the second part of the attack.

Part II: Backward Collisions. Suppose we have found an inner collision. Then, in order to find a collision of the two-round compress function, we have to determine values for the remaining free variables such that computing backward and starting with the values of the registers after step $i_0 - 1$ (these values are given according to Part I) leads to the same initial value for the left and right line ("backward collision").

Experience has shown that we can even find collisions with 2^{100} of the 2^{128} possible initial values (see Section 4).

Part III: Right Initial Value ("Meet-in-the-Middle"). Finally, the problem remains that the initial value IV_0 specified by the definition of RIPEMD^[12] and RIPEMD^[23] (see the Appendix) will usually not be among the admissible initial values found in Part II. Therefore we randomly choose 16 input words for the compress function starting with IV_0 , until the output is admissible. This approach works, since we have a very fast test whether the output is admissible or not (checking a small set of equations). Thus nothing has to be stored and compared in lists. The computational effort necessary for this part is usually much smaller than for Part II.

Since two messages found by this attack both have length $2 \times 16 \times (length \ of \ words)$, they give a collision no matter whether the padding rule is applied or not.

The next sections contain a detailed description of Parts I–III of our attack.

3. Inner Collisions

In this section we show how to find inner collisions for compress^[12]. For compress^[12] this is much more difficult and requires many technical tricks. (The difficulties seem to be caused by the asymmetry of the Boolean function *F*, which is involved here.) We restrict ourselves to the easy case, and we only give an inner collision for compress^[12] at the end of this section without proof.

By L-compress^[23] and R-compress^[23] we denote the left and right line of compress^[23], respectively. We now consider the sequence of those steps of L-compress^[23] and R-compress^[23] between which X_{13} occurs the first and the second time, i.e., steps 13–18. In these steps, X_{13} , X_{14} , X_{15} together with the Boolean vector function

$$G(X, Y, Z) = (X \wedge Y) \vee (X \wedge Z) \vee (Y \wedge Z),$$

and then X_7 , X_4 , and again X_{13} together with the Boolean vector function

$$H(X, Y, Z) = X \oplus Y \oplus Z$$

are applied. The constants used in compress[23] are

 $K_1 = 0x5a827999,$

 $K_2 = 0$ x6ed9eba1,

 $K_3 = 0x5c4dd124.$

Lemma A. We obtain a simultaneous collision for steps 13–18 of L-compress^[23] and R-compress^[23] for

$$X_{13} = 0$$
xa57d8666 = $-K_1 - 1$,

 $X_{14} = 0$ xa57d8666 = $-K_1 - 1$,

 $X_{15} = 0$ xa57d8666 = $-K_1 - 1$,

 $X_7 = 0 \times 9126145 f = -K_2$

 $X_4 = arbitrary$

$$\tilde{X}_{13} = X_{13} + 1 = 0$$
xa57d8667,

$$\tilde{X}_j = X_j \quad (j = 14, 15, 7, 4),$$

if we use the following "initial values" for step 13 of the left and right line, respectively:

$$(A_{L}, B_{L}, C_{L}, D_{L}) = (1, 0, 0, 0),$$

 $(A_{R}, B_{R}, C_{R}, D_{R}) = (Q, Q + 1, K_{1} - Q, K_{1} - Q - 1),$

where Q is even and satisfies the equation

$$(2Q - K_1)^{\ll 1} + (2Q - K_1)^{\ll 12} - Q + K_2 - K_3 + 1 = 0.$$

In fact, this equation has precisely the even solutions

$$Q = 2 * 0 \times 110 d8 f04,$$

$$Q = 2 * 0 \times 5 dda 5 bd1.$$

Proof. First we consider the left line. Table 1 shows the contents A_i , B_i , C_i , D_i of the registers of the left line after the application of steps $i = 13, \ldots, 18$ for the inputs X_{13} , X_{14} , X_{15} , X_7 , X_4 as specified above, and for

$$(A_{12}, B_{12}, C_{12}, D_{12}) = (A_{1}, B_{1}, C_{1}, D_{1}) = (1, 0, 0, 0).$$

Step i	A_i	B_i	C_i	D_i	Z_{j}	X_{j}	constant	Boolean function	Shift
12	1	0	0	0	*	*	*	*	*
13	1	0	0	-1	-1	$-K_1 - 1$	K_1	\boldsymbol{G}	11
14	1	0	0	<u></u>	-1	$-K_1 - 1$	K_1	G	13
15	1	0	0	-1	-1	$-K_{1}-1$	K_1	\boldsymbol{G}	12
16	0	0	0	!	0	$-K_2$	K_2	Н	11
17	0	0	0	D_{17}	$X_4 + K_2$	X_4	K_2	Н	13
18	0	0	C_{18}	D_{17}	$K_2 - K_1 - 1$	$-K_{1}-1$	K_2	Н	14

Table 1

Here we set

$$Z_i = X_i + \text{constant}$$
 $(j = 13, 14, 15, 7, 4, 13)$

for the "actual input," where **constant** denotes the constant of the particular step to be used according to the definition of the algorithm (see the Appendix). The boxed entries are those which have been modified in the respective steps.

We have

$$D_{18} = D_{17} = (X_4 + K_2 - 1)^{\ll 13},$$

 $C_{18} = (D_{18} + K_2 - K_1 - 1)^{\ll 14}.$

In order to verify Table 1 compute

$$\begin{split} D_{13} &= (0 + G(1, 0, 0) - 1)^{\ll 11} = (-1)^{\ll 11} = (0 \times \text{ffffffff})^{\ll 11} = -1, \\ C_{14} &= (0 + G(-1, 1, 0) - 1)^{\ll 13} = 0^{\ll 13} = 0, \end{split}$$

and so on.

We have to compare this with Table 2 for the inputs \tilde{X}_{13} , \tilde{X}_{14} , \tilde{X}_{15} , \tilde{X}_{7} , \tilde{X}_{4} , where $\tilde{X}_{13} = X_{13} + 1$ and $\tilde{X}_{j} = X_{j}$ (j = 14, 15, 7, 4).

Step i	$ ilde{A}_i$	$ ilde{B}_i$	$ ilde{C}_i$	$ ilde{D}_i$	Z_i	$ ilde{X}_i$	constant	Boolean function	Shift
12	1	0	0	0	*	*	*	*	*
13	1	0	0	0	0	$-K_1$	K_1	G	11
14	1	0	-1	0	- i	$-K_1 - 1$	K_1	G	13
15	1	0	-1	0	-1	$-K_1 - 1$	K_1	G	12
16	0	o	-1	0	0	$-K_2$	K_2	Н	11
17	0	0	-1	\tilde{D}_{17}	$X_4 + K_2$	X_4	K ₂	Н	13
18	0	0	\tilde{C}_{18}	\tilde{D}_{17}	$K_2 - K_1$	$-K_1$	K_2	Н	14

Table 2

п	r.	L	ı	2

Step i	A_i	B_i	C_i	D_i	Z_j	X_j	const.	Boolean function	Shift
12	Q	Q + 1	$K_1 - Q$	K_1-Q-1	*	*	*	*	*
13	Q	Q + 1	$K_1 - Q$	-1	$-K_1 - 1$	$-K_{1}-1$	0	\boldsymbol{G}	11
14	Q	Q+1	0	-1	$-K_1 - 1$	$-K_1 - 1$	0	\boldsymbol{G}	13
15	Q	B ₁₅	0	-1	$-K_{1}-1$	$-K_1 - 1$	0	\boldsymbol{G}	12
16	A ₁₆	B ₁₅	0	-1	$K_3 - K_2$	$-K_2$	K_3	H	11
17	A ₁₆	B_{15}	0	D_{17}	$X_4 + K_3$	X_4	K_3	Н	13
18	A 16	B_{15}	C ₁₈	D_{17}	$K_3 - K_1 - 1$	$-K_1 - 1$	K_3	Н	14

We have

$$\tilde{D}_{18} = \tilde{D}_{17} = (X_4 + K_2 - 1)^{\ll 13},$$

 $\tilde{C}_{18} = (\tilde{D}_{18} + K_2 - K_1 - 1)^{\ll 14}.$

Thus we see that $(A_{18}, B_{18}, C_{18}, D_{18}) = (\tilde{A}_{18}, \tilde{B}_{18}, \tilde{C}_{18}, \tilde{D}_{18})$ as desired.

For the right line, Tables 3 and 4 show the contents of the registers for steps 12–18.

For verification of Tables 3 and 4 we need the assumption that Q is even and we use the fact that K_1 is odd, for instance:

$$D_{13} = (K_1 - Q - 1 + G(Q, Q + 1, K_1 - Q) - K_1 - 1)^{\ll 11}$$

$$= (-Q - 2 + ((Q \land (Q + 1)) \lor (Q \land (K_1 - Q)) \lor ((Q + 1) \land (K_1 - Q))))^{\ll 11}$$

$$= (-Q - 2 + (Q + 1))^{\ll 11} = (-1)^{\ll 11} = -1.$$

Further, we have

$$B_{18} = B_{15} = (2Q - K_1)^{\ll 12},$$

$$A_{18} = A_{16} = (Q - B_{18} - 1 + K_3 - K_2)^{\ll 11},$$

$$D_{18} = D_{17} = (X_4 + K_3 - 1 + (A_{18} \oplus B_{18}))^{\ll 13},$$

$$C_{18} = (K_3 - K_1 - 1 + (A_{18} \oplus B_{18} \oplus D_{18}))^{\ll 14},$$

Table 4

Step i	$ ilde{A_i}$	$ ilde{B}_i$	$ ilde{C}_i$	$ ilde{D}_i$	Z_j	$ ilde{X}_j$	const.	Boolean function	Shift
12	Q	Q+1	$K_1 - Q$	K_1-Q-1	*	*	*	*	*
13	Q	Q+1	$K_1 - Q$	0	$-K_1$	$-K_1$	0	\boldsymbol{G}	11
14	Q	Q+1	-1	0	$-K_1 - 1$	$-K_1 - 1$	0	G	13
15	Q	\tilde{B}_{15}	-1	0	$-K_{1}-1$	$-K_1 - 1$	0	\boldsymbol{G}	12
16	\bar{A}_{16}	\tilde{B}_{15}	-1	0	$K_3 - K_2$	$-K_2$	K_3	Н	11
17	\tilde{A}_{16}	\tilde{B}_{15}	-1	$ ilde{D}_{17} $	$X_4 + K_3$	<i>X</i> ₄	K_3	Н	13
18	\tilde{A}_{16}	\tilde{B}_{15}	\tilde{C}_{18}	\tilde{D}_{17}	$K_3 - K_1$	-K ₁	K ₃	Н	14

$$\tilde{B}_{18} = \tilde{B}_{15} = (2Q - K_1)^{\ll 12},
\tilde{A}_{18} = \tilde{A}_{16} = (Q - \tilde{B}_{18} - 1 + K_3 - K_2)^{\ll 11},
\tilde{D}_{18} = \tilde{D}_{17} = (X_4 + K_3 - 1 - (\tilde{A}_{18} \oplus \tilde{B}_{18}))^{\ll 13},
\tilde{C}_{18} = (K_3 - K_1 - 1 + (\tilde{A}_{18} \oplus \tilde{B}_{18} \oplus \tilde{D}_{18}))^{\ll 14}.$$

Thus we conclude that $(A_{18}, B_{18}, C_{18}, D_{18}) = (\tilde{A}_{18}, \tilde{B}_{18}, \tilde{C}_{18}, \tilde{D}_{18})$ if and only if

$$A_{18} \oplus B_{18} = -(A_{18} \oplus B_{18}),$$

i.e., $A_{18} = B_{18}$ or $A_{18} = B_{18} \oplus 2^{31}$. The first case leads to the equation

$$(Q - (2Q - K_1)^{\ll 12} - 1 + K_3 - K_2)^{\ll 11} = (2Q - K_1)^{\ll 12},$$

or equivalently

$$(2Q - K_1)^{\ll 1} + (2Q - K_1)^{\ll 12} - Q + K_2 - K_3 + 1 = 0.$$

Simply testing all even $Q < 2^{32}$ shows that this equation has precisely the two solutions given in the lemma. It takes about 30 minutes on a 486-PC. (The corresponding equation for $A_{18} = B_{18} \oplus 2^{31}$ has no solution.) This completes the proof.

Lemma B. We obtain a simultaneous collision for steps 13-18 of L-compress^[12] and R-compress^[12] for

$$X_{13} = 0 \times b6c474bc,$$

 $X_{14} = 0 \times 1e575831,$
 $X_{15} = 0 \times 767f3bbb,$
 $X_{7} = 0 \times 3a456372,$
 $X_{4} = arbitrary,$

$$\tilde{X}_{13} = X_{13} + \Delta$$
 with $\Delta = 0$ xa954a955,
 $\tilde{X}_{i} = X_{i}$ $(j = 14, 15, 7, 4),$

if we use the following "initial values" for step 13 of the left and right line, respectively:

$$(A_{L}, B_{L}, C_{L}, D_{L}) = (0x50421004, 0xbff56adf, 0x00000000, 0x00000000),$$

 $(A_{R}, B_{R}, C_{R}, D_{R}) = (0x1021040a, 0xfb5eaffd, 0x3830a91b, 0x21485a45).$

As already mentioned we do not describe the approach leading to the inner collision given in Lemma B, since it is too complicated to be presented here. The computational effort for finding these inner collisions is, also in this case, much smaller than the effort for finding backward collisions.

Remark on almost collisions. For the inner collision of compress^[23] given in Lemma A, we have $\Delta = \tilde{X}_{13} - X_{13} = 1$. This Δ has Hamming weight 1. Hence the corresponding input pairs, which yield collisions of compress^[23], give "almost collisions" for compress^[231] (first round of compress put at the end, see end of the Appendix), in the sense that their outputs differ only at few bit positions. The reason is that the difference Δ between two inputs affects only the very end of the last round, namely steps 45, 46, and 47. About a portion of 1/70 (empirical observation) of the collisions for compress^[23] found by our attack even have outputs under compress^[231] different only at two bits. (See the end of Example A; unfortunately, a two-bit difference is the best result we can achieve in this way.)

Thus it is an interesting question whether Lemma B can also be shown for some Δ with Hamming weight 1. In that case, corresponding collisions of compress^[12] would form almost collisions for the full compress function of RIPEMD. However, the best result that could be achieved in this way is a seven-bit difference in the hash values. The reason for this restriction is that the XOR operation H, which is applied in the last round, inherits differences.

4. Backward Collisions

The way to find backward collisions is the same for compress^[12] and compress^[23]. Just to fix the notation, we consider compress^[23] in what follows.

 X_{13} does not occur in steps 0–12 and 19–31. Thus, in order to derive collisions of compress^[23] from Lemma A we have to find "backward collisions" for the first 13 steps of the left and right line. That is, we have to compute $X_0, X_1, \ldots, X_6, X_8, \ldots, X_{12}$ and *common* initial values A, B, C, D such that the computation of L-compress^[23] and R-compress^[23] arrive after step 12 at (A_L, B_L, C_L, D_L) and (A_R, B_R, C_R, D_R) as specified in Lemma A, respectively, where $X_7 = -K_2$.

Lemma 1. There is an algorithm which allows the computation of backward collisions in the above sense. When the algorithm is successful, about 2^{36} classes consisting of backward collisions, for which the initial values A and D can be chosen freely, are obtained. The algorithm requires on average about the same computational effort as 2^{31} computations of a (two-round) compress function.

Proof. First we suppose that we have chosen X_8, \ldots, X_{12} . (Recall that X_7 is already fixed.) Then the contents of the registers after the application of step 6 for the left and the right line, respectively, say

$$(A_{L}^{*}, B_{L}^{*}, C_{L}^{*}, D_{L}^{*})$$
 and $(A_{R}^{*}, B_{R}^{*}, C_{R}^{*}, D_{R}^{*})$,

are fixed and can be found easily by computing backward. It remains to determine X_0, \ldots, X_6 and A, B, C, D in order to find a backward collision. Table 5 shows the computation of steps 0–6.

Let s_i denote the number of bit positions to be shifted in step i. Moreover we use the notations

$$\Delta U = U_{\rm R} - U_{\rm L}$$

Initial values	Α	В	С	D	Initial values	Α	B	C	D
Step 0	U_{L}	В	С	D	Step 0	U_{R}	В	C	D
Step 1	$U_{\rm L}$	В	C	$V_{\rm L}$	Step 1	U_{R}	В	C	V_{R}
Step 2	$U_{\rm L}$	В	$W_{\rm L}$	$V_{\rm L}$	Step 2	U_{R}	В	W_{R}	V_{R}
Step 3	U_{L}	<i>B</i> *	$W_{\rm L}$	$V_{\rm L}$	Step 3	U_{R}	B_{R}^*	W_{R}	V_{R}
Step 4	A_1^*	$B^{\overline{*}}$	W_{L}	$V_{\rm L}$	Step 4	$A_{ m R}^*$	$B_{R}^{\widehat{\star}}$	W_{R}	V_{R}
Step 5	A_1^*	B_{i}^{*}	W_{L}	D_1^*	Step 5	$A_{\rm R}^{\widehat{*}}$	$B_{\rm R}^{\widehat{*}}$	W_{R}	D_{R}^{\star}
Step 6	A_{L}^{*}	$B_{\rm L}^{\stackrel{\leftarrow}{*}}$	C_{L}^*	$D_{L}^{\!$	Step 6	$A_{\mathrm{R}}^{\widehat{*}}$	$B_{R}^{\overset{\mathbf{r}}{*}}$	C_{R}^{*}	$D_{R}^{\mathbf{\hat{r}}}$

Table 5

$$\Delta V = V_{R} - V_{L},$$

$$\Delta W = W_{R} - W_{L}.$$

The following equations are easily derived:

$$0 = U_{R}^{\ll(-s_0)} - U_{L}^{\ll(-s_0)} + K_1, \tag{1}$$

$$G(U_{R}, B, C) - G(U_{L}, B, C) = V_{R}^{\ll (-s_{1})} - V_{L}^{\ll (-s_{1})} + K_{1},$$
 (2)

$$G(V_{R}, U_{R}, B) - G(V_{L}, U_{L}, B) = W_{R}^{\ll (-s_{2})} - W_{L}^{\ll (-s_{2})} + K_{1},$$
 (3)

$$G(W_{R}, V_{R}, U_{R}) - G(W_{L}, V_{L}, U_{L}) = B_{R}^{* \ll (-s_{3})} - B_{L}^{* \ll (-s_{3})} + K_{1},$$
(4)

$$G(B_{R}^{*}, W_{R}, V_{R}) - G(B_{L}^{*}, W_{L}, V_{L}) = A_{R}^{* \ll (-s_{4})} - A_{L}^{* \ll (-s_{4})} - \Delta U + K_{1},$$
 (5)

$$G(A_{R}^{*}, B_{R}^{*}, W_{R}) - G(A_{L}^{*}, B_{L}^{*}, W_{L}) = D_{R}^{* \ll (-s_{5})} - D_{L}^{* \ll (-s_{5})} - \Delta V + K_{1},$$
 (6)

$$G(D_{R}^{*}, A_{R}^{*}, B_{R}^{*}) - G(D_{L}^{*}, A_{L}^{*}, B_{L}^{*}) = C_{R}^{* \ll (-s_{0})} - C_{L}^{* \ll (-s_{0})} - \Delta W + K_{1}.$$
 (7)

In fact, these equations follow by elimination of X_i from the two equations defining step i. As an example, step 1 is defined by the equations

$$V_{L} = (D + G(U_{L}, B, C) + X_{1} + K_{1})^{\ll s_{1}},$$

$$V_{R} = (D + G(U_{R}, B, C) + X_{1})^{\ll s_{1}},$$

which imply (2).

Equations (1)–(7) must hold when we have found a backward collision. Conversely, if B, C, U_L , V_L , W_L , U_R , V_R , W_R satisfy (1)–(7), then we obtain 2^{64} backward collisions by setting, for arbitrary A and D:

$$X_0 = -A - G(B, C, D) + U_{\mathsf{R}}^{\ll (-s_0)}, \tag{8}$$

$$X_1 = -D - G(U_R, B, C) + V_R^{\ll (-s_1)},$$
 (9)

$$X_2 = -C - G(V_R, U_R, B) + W_R^{\ll (-s_2)}.$$
 (10)

$$X_3 = -B - G(W_{\rm R}, V_{\rm R}, U_{\rm R}) + B_{\rm R}^{* \ll (-5)}, \tag{11}$$

$$X_4 = -U_R - G(B_R^*, W_R, V_R) + A_R^{* \ll (-s_4)}, \tag{12}$$

$$X_5 = -V_{\rm R} - G(A_{\rm R}^*, B_{\rm R}^*, W_{\rm R}) + D_{\rm R}^{*\ll(-s_5)}, \tag{13}$$

$$X_6 = -W_{\mathsf{R}} - G(D_{\mathsf{R}}^*, A_{\mathsf{R}}^*, B_{\mathsf{R}}^*) + C_{\mathsf{R}}^{* \ll (-s_6)}. \tag{14}$$

Hence we try to find solutions $B, C, U_L, V_L, W_L, U_R, V_R, W_R$ of (1)–(7). To this end we make use of the following obvious fact:

There is a very fast algorithm which allows the computation of all solutions of an equation of the form

$$G(a_0, b_0, x + \Delta x) - G(a_1, b_1, x) = c$$
 with given $\Delta x, a_0, b_0, a_1, b_1$, and c. (15)

In fact, simply solve this equation recursively and bitwise starting from the lowest bit. Empirical observations have shown that there exist solutions of (15) with a probability of about 2^{-11} , and in this case we obtain an average of 2^{11} solutions.

The basic idea is to derive a sequence of equations from (1)–(7), such that whenever the preceding equations are solved, the next equation is of the form (15). To manage this we make two additional settings, which in many cases will reduce the set of solutions, but on the other hand allow us to determine all remaining solutions very quickly:

$$\Delta U = -K_1^{\ll s_0} \quad (\text{or } (-K_1)^{\ll s_0}),$$

$$\Delta V = -K_1^{\ll s_1} \quad (\text{or } (-K_1)^{\ll s_1}).$$
(16)

$$\Delta V = -K_1^{\ll s_1} \quad (\text{or } (-K_1)^{\ll s_1}). \tag{17}$$

The reason for (16) is (1), which suggests these guesses for the value of ΔU without knowing U_L and U_R . Similarly (17) hopefully implies

$$V_{\rm R}^{\ll(-s_1)} - V_{\rm L}^{\ll(-s_1)} + K_1 = 0,$$

such that (2) would have at least the trivial solution B = C. Further observe that ΔW is fixed by (7):

$$\Delta W = G(D_{R}^{*}, A_{R}^{*}, B_{R}^{*}) - G(D_{L}^{*}, A_{L}^{*}, B_{L}^{*}) - C_{R}^{* \ll (-s_{6})} + C_{L}^{* \ll (-s_{6})} + K_{1}.$$

Equations (6), (5), ..., (2), in this ordering, are now used to compute W_L , V_L , U_L , B, and finally C:

$$G(A_{R}^{*}, B_{R}^{*}, W_{L} + \Delta W) - G(A_{L}^{*}, B_{L}^{*}, W_{L}) = D_{R}^{* \ll (-s_{5})} - D_{L}^{* \ll (-s_{5})} - \Delta V + K_{1}, \quad (I)$$

$$G(B_{R}^{*}, W_{R}, V_{L} + \Delta V) - G(B_{L}^{*}, W_{L}, V_{L}) = A_{R}^{* \ll (-s_{4})} - A_{L}^{* \ll (-s_{4})} - \Delta U + K_{1}, \quad (II)$$

$$G(W_{R}, V_{R}, U_{L} + \Delta U) - G(W_{L}, V_{L}, U_{L}) = B_{R}^{* \ll (-s_{3})} - B_{L}^{* \ll (-s_{3})} + K_{1}, \quad (III)$$

$$G(V_{R}, U_{R}, B) - G(V_{L}, U_{L}, B) = W_{R}^{* \ll (-s_{2})} - W_{L}^{* \ll (-s_{2})} + K_{1}, \quad (IV)$$

$$G(U_{R}, B, C) - G(U_{L}, B, C) = V_{R}^{* \ll (-s_{1})} - V_{L}^{* \ll (-s_{1})} + K_{1}. \quad (V)$$

$$G(B_{R}^{*}, W_{R}, V_{L} + \Delta V) - G(B_{L}^{*}, W_{L}, V_{L}) = A_{R}^{* \ll (-s_{4})} - A_{L}^{* \ll (-s_{4})} - \Delta U + K_{1}, \quad (II)$$

$$G(W_{R}, V_{R}, U_{L} + \Delta U) - G(W_{L}, V_{L}, U_{L}) = B_{R}^{* \ll (-s_{3})} - B_{L}^{* \ll (-s_{3})} + K_{1},$$
(III)

$$G(V_{R}, U_{R}, B) - G(V_{L}, U_{L}, B) = W_{R}^{\ll (-s_{2})} - W_{L}^{\ll (-s_{2})} + K_{1},$$
 (IV)

$$G(U_{R}, B, C) - G(U_{L}, B, C) = V_{R}^{(\alpha(-s_{1}))} - V_{L}^{(\alpha(-s_{1}))} + K_{L}.$$
 (V)

After these preparations we can describe how the algorithm for the search of backward collisions works in its simplest form:

- 1. Choose X_8, \ldots, X_{12} randomly and compute the associated values for A_L^*, B_L^*, C_L^* $D_{\rm L}^*$ and $A_{\rm R}^*$, $B_{\rm R}^*$, $C_{\rm R}^*$, $D_{\rm R}^*$. Determine the solutions $W_{\rm L}$ of (I). If they exist, insert each of them into (II) in order to determine all associated V_L . In case of success, the next step is to consider (III) in order to compute U_L , and so on. Refuse each possibly obtained U_L if it does not satisfy (1).
- 2. If X_8, \ldots, X_{12} have been found such that there are solutions up to (II), then take these as "basic values." Change randomly one bit of each of the basic values for X_8, \ldots, X_{12} . (The idea is that if a set of values of X_8, \ldots, X_{12} gives solutions to

steps (I) and (II), then values close to them again give solutions to steps (I) and (II) with a relatively high probability.) Only if you again come to solutions up to (II) take the changed value as new basic values and continue until you reach (III), and so on.

Practice has shown that this leads to solutions up to (IV), and then, in most cases, automatically to solutions of (V).

Observe that the set of solutions has the structure of a tree. We have a set of solutions W_L of (I), such that to each W_L there corresponds a set of solutions V_L of (II), and so on. This "avalanche" effect has the consequence that in case we can reach (V), we even find 2^{36} or more solutions.

The combination of Lemmas A and B with Lemma 1 implies directly:

Lemma 2. There is an algorithm which allows the computation of collisions of compress^[12] and compress^[23] with about 2¹⁰⁰ different initial values. It requires on average about the same computational effort as 2³¹ computations of a (two-round) compress function.

In next example one class of 2^{64} collisions for compress^[23] is given. It has been found by the previously described attack (Lemmas A and 1).

Example A (Class of Collisions for compress^[23]). For arbitrary A and D set

```
(B, C) = (0x828a0950, 0x98080110),
    X_0 = 0 \times 12481059 + G(B, C, 0) - G(B, C, D) - A
    X_1 = 0xa9368b18 - D,
    X_2 = 0xdf3dae71,
    X_3 = 0 \times 88 \text{bacd2a},
    X_4 = 0 \times 618 \text{ec} 5 \text{d} 2
    X_5 = 0x53b59054,
    X_6 = 0 \times 16 \text{b} 396 \text{fc}.
    X_7 = 0 \times 9126145 f.
    X_8 = 0 \times 972 a 229 d,
    X_9 = 0xe9098eee,
   X_{10} = 0 \times f \cdot 0 f \cdot 1721 d
   X_{11} = 0 \times f8 dbb 766,
   X_{12} = 0x753ed5cb,
   X_{13} = 0xa57d8666,
   X_{14} = 0xa57d8666,
   X_{15} = 0xa57d8666,
```

$$\tilde{X}_i = X_i$$
 for $i < 16$, $i \neq 13$, $\tilde{X}_{13} = X_{13} + 1 = 0 \times a57 d8667$.

Then we have

compress^[23]
$$(A, B, C, D; X_0, ..., X_{15}) = \text{compress}^{[23]}(A, B, C, D; \tilde{X}_0, ..., \tilde{X}_{15}).$$

Explicitly, if for instance we set $A = 0 \times 03 d06 da3$ and $D = 0 \times 6a645 c74$, then the common output under compress^[23] is

0x7f733e3b 0x3bac842a 0x061c7cac 0x0ca41089.

Moreover, we have

compress^[231](
$$A, B, C, D; X$$
)
= 0x9b888272 0x66483211 0x0692b464 0x758f548b,
compress^[231]($A, B, C, D; \tilde{X}$)
= 0x9b8882f2 0x66483291 0x0692b464 0x758f548b.

That is, the outputs under **compress**^[231] differ only at two bits.

5. Right Initial Value

By Lemma 2 we find large classes of collisions for the compress functions of RIPEMD^[12] and RIPEMD^[23], but not yet for these hash functions, because the required fixed initial value IV_0 will usually not be among the admissible initial values for collisions found. However, as we shall see below, this problem is easily avoided by a straightforward meet-in-the-middle attack.

Example B (Collision for RIPEMD^[12]). The first message M is the sequence of the following words:

```
M_{16} = 0 \times 61 \text{dfb} 182,
M_0 = 0 \times 179 = 2 \times 9,
M_1 = 0x6e14b784,
                                     M_{17} = 0 \times 1808647 d
M_2 = 0x2fa520b7,
                                     M_{18} = 0 \times \text{fd} 84 \text{f2e1},
M_3 = 0 \times 09 = 5 \times 84 a,
                                     M_{19} = 0 \times b8647 a 90,
M_4 = 0 \times 6 = 81 = 3 \times 5,
                                     M_{20} = 0xc3dd8441,
                                     M_{21} = 0xalfeflel,
M_5 = 0 \times 50 \text{c8fbfe},
M_6 = 0 \times 6 d3 90 c47
                                     M_{22} = 0 \times 7 d15 c061,
                                     M_{23} = 0x3a456372,
M_7 = 0 \times 7 \times 5292  b4,
                                     M_{24} = 0 \times 020828 dd,
M_8 = 0 \times 50  f 0 5 9 3 4,
M_9 = 0x2acd6dd7,
                                     M_{25} = 0 \times 2 \text{be} 0 \cdot 14 \text{ea},
M_{10} = 0x4afbae78,
                                     M_{26} = 0 \times 321373 f7,
M_{11} = 0 \times 28 \text{eda5c3},
                                     M_{27} = 0 \times 0 b 2266 e 7,
M_{12} = 0 \times 3217765 e
                                     M_{28} = 0 \times 87 \text{ e} 9 \text{ d} \text{d} 2,
                                     M_{29} = 0 \times 6 \times 474 \text{bc}
M_{13} = 0 \times 16 = 164,
M_{14} = 0 \times 54 \text{b} 57 \text{b} \text{b} 0,
                                    M_{30} = 0 \times 1e575831,
                                    M_{31} = 0 \times 767 \, \text{f3bbb}.
M_{15} = 0 \times 46 \text{cbe} 2 \text{b6},
```

The second message \tilde{M} is defined by setting $\tilde{M}_i = M_i$ ($i < 32, i \neq 29$) and

$$\tilde{M}_{29} = 0 \times 60191 \text{e}11.$$

RIPEMD^[12] associates to M and \tilde{M} the same hash value, namely

0x7b4d3b7f 0x792ae282 0xfee3cfe0 0xb8cee276.

where the padding rule is also considered. It requires that the 16 words

$$0 \times 80, 0, 0, \dots, 0, 0 \times 400, 0$$

have to be added to M and \tilde{M} (see [1]).

This example has been found as follows. First, based on Lemma B, the algorithm described in the proof of Lemma 1 is applied (i.e., $X_{13} = M_{29}$, $X_{14} = M_{30}$, $X_{15} = M_{31}$, $X_7 = M_{23}$ are given according to Lemma B). In this way we find $X_8 = M_{24}, \ldots, X_{12} = M_{28}$ such that we have a complete tree of solutions for W_L , W_R , V_L , V_R , U_L , U_R , B, and C satisfying (I)–(V). (Note that here we have to modify the equations in Section 4 by replacing K_1 by $-K_0$, because we are now dealing with the first two instead of the last two rounds.)

From this tree we take a collection of fixed values for W_L , W_R , V_L , V_R , U_L , and U_R , i.e., (I)–(III) are satisfied. In view of (12), (13), (14) the words $X_4 = M_{20}$, $X_5 = M_{21}$, $X_6 = M_{22}$ are now fixed.

Finally choose values for M_0, \ldots, M_{15} randomly, compute

compress^[12]
$$(IV_0; M_0, ..., M_{15}) = (A, B, C, D)$$

with

 $IV_0 = 0x67452301$ 0xefcdab89 0x98badcfe 0x10325476

(see the Appendix) and test, for B and C, the following equations:

$$F(V_{R}, U_{R}, B) - F(V_{L}, U_{L}, B) = W_{R}^{\ll(-s_{2})} - W_{L}^{\ll(-s_{1})} - K_{0},$$

$$F(U_{R}, B, C) - F(U_{L}, B, C) = V_{R}^{\ll(-s_{1})} - V_{L}^{\ll(-s_{1})} - K_{0}.$$
(IV)

If the test is passed, then (A, B, C, D) is an admissible initial value for the second application of compress^[12], and we define $X_0 = M_{16}$, $X_1 = M_{17}$, $X_2 = M_{18}$, and $X_3 = M_{19}$ using (8)–(11).

Concretely in our above example, we have chosen

$$W_{\rm L} =$$
 0xcef895d6,
 $W_{\rm R} =$ 0x140279c4,
 $V_{\rm L} =$ 0x0d5e7745,
 $V_{\rm R} =$ 0xb0580b6d,
 $U_{\rm L} =$ 0x83901a28,
 $U_{\rm R} =$ 0x97ef4cad

from a complete tree of solutions of (I)–(V) which have been found. There are 2^{32} pairs (B, C) satisfying (IV) and (V) in this case.

We can improve this attack by selecting not only one, but several collections of values for W_L , W_R , V_L , V_R , U_L , U_R from the tree of solutions of (I)–(III). Another trick reducing the effort is taking M_0, \ldots, M_{14} fixed and changing only M_{15} . Then only one round of the compress function has to be computed.

6. MD4 Is Not Collision-Free

We have found that the methods developed in this note can be applied to MD4 very effectively [3]. In fact, collisions can be found for the full MD4 in less than 1 minute on a PC.

Example C (Collision for MD4). The first message M is the sequence of the following 32-bit words:

```
M_0 = 0 \times 13985 = 12, M_8 = 0 \times abe17be0, M_1 = 0 \times 748a810b, M_9 = 0 \times ed1ed4b3, M_2 = 0 \times 4d1df15a, M_{10} = 0 \times 4120abf5, M_3 = 0 \times 181d1516, M_{11} = 0 \times 20771029, M_4 = 0 \times 2d6e09ac, M_{12} = 0 \times 20771027, M_5 = 0 \times 4b6dbdb9, M_{13} = 0 \times fdfffbff, M_6 = 0 \times 6464b0c8, M_{14} = 0 \times ffffbffb, M_7 = 0 \times fba1c097, M_{15} = 0 \times 6774bed2.
```

The second message \tilde{M} is defined by setting $\tilde{M}_i = M_i$ ($i < 16, i \neq 12$) and

$$\tilde{M}_{12} = M_{12} + 1.$$

MD4 associates to M and \tilde{M} the same hash value

0x711ad51b 0xbbab5e22 0x618b1c76 0x17c15892.

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Appendix

The hash function RIPEMD is defined as the iteration of a certain compress function, which we specify below. The computation starts with the initial value

```
IV_0 = 0x67452301 0xefcdab89 0x98badcfe 0x10325476.
```

Each application of the compress function uses four words as initial values and 16 words of the message as input, and it gives four words output, which are then used as initial

values for the next application. The final output is the hash value. This works, since there is a padding rule (addition of bits to the message such that its length is a multiple of $512 = 16 \times (length \ of \ words)$). A complete description of RIPEMD can be found in [1].

A.1. Compress Function of RIPEMD

Define the constants

 $K_0 = 0x50a28be6,$ $K_1 = 0x5a827999,$ $K_2 = 0x6ed9eba1,$ $K_3 = 0x5c4dd124$

and the Boolean vector functions

$$F(X, Y, Z) = (X \wedge Y) \vee (\neg X \wedge Z),$$

$$G(X, Y, Z) = (X \wedge Y) \vee (X \wedge Z) \vee (Y \wedge Z),$$

$$H(X, Y, Z) = X \oplus Y \oplus Z.$$

Further, let FF(a, b, c, d, Z, s), GG(a, b, c, d, Z, s), and HH(a, b, c, d, Z, s) denote the operations

$$a := (a + F(b, c, d) + Z)^{\ll s},$$

 $a := (a + G(b, c, d) + Z)^{\ll s},$
 $a := (a + H(b, c, d) + Z)^{\ll s}.$

respectively. In order to define compress for RIPEMD suppose now that initial values A, B, C, D and inputs X_0, \ldots, X_{15} are given. Copy A, B, C, D into the registers aa, bb, cc, dd of the left line and the registers aaa, bbb, ccc, ddd of the right line. Then apply the following steps:

First round

```
step 0
       FF(aa, bb, cc, dd, X_0, 11)
                                     FF(aaa, bbb, ccc, ddd, X_0 + K_0, 11)
step 1
        FF(dd, aa, bb, cc, X_1, 14)
                                     FF(ddd, aaa, bbb, ccc, X_1 + K_0, 14)
        FF(cc, dd, aa, bb, X_2, 15)
                                     FF(ccc, ddd, aaa, bbb, X_2 + K_0, 15)
step 2
step 3
       FF(bb, cc, dd, aa, X_3, 12)
                                     FF(bbb, ccc, ddd, aaa, X_3 + K_0, 12)
step 4
       FF(aa, bb, cc, dd, X_4, 5)
                                     FF(aaa, bbb, ccc, ddd, X_4 + K_0, 5)
       FF(dd, aa, bb, cc, X_5, 8)
step 5
                                     FF(ddd, aaa, bbb, ccc, X_5 + K_0, 8)
step 6 FF(cc, dd, aa, bb, X_6, 7)
                                     FF(ccc, ddd, aaa, bbb, X_6 + K_0, 7)
step 7
        FF(bb, cc, dd, aa, X_7, 9)
                                     FF(bbb, ccc, ddd, aaa, X_7 + K_0, 9)
step 8
        FF(aa, bb, cc, dd, X_8, 11)
                                     FF(aaa, bbb, ccc, ddd, X_8 + K_0, 11)
```

```
step 9
          FF(dd, aa, bb, cc, X_9, 13)
                                           FF(ddd, aaa, bbb, ccc, X_0 + K_0, 13)
step 10
          FF(cc, dd, aa, bb, X_{10}, 14)
                                           FF(ccc, ddd, aaa, bbb, X_{10} + K_0, 14)
          FF(bb, cc, dd, aa, X_{11}, 15)
                                           FF(bbb, ccc, ddd, aaa, X_{11} + K_0, 15)
step 11
          FF(aa, bb, cc, dd, X_{12}, 6)
                                           FF(aaa, bbb, ccc, ddd, X_{12} + K_0, 6)
step 12
          FF(dd, aa, bb, cc, X_{13}, 7)
                                           FF(ddd, aaa, bbb, ccc, X_{13} + K_0, 7)
step 13
step 14
          FF(cc, dd, aa, bb, X_{14}, 9)
                                           FF(ccc, ddd, aaa, bbb, X_{14} + K_0, 9)
          FF(bb, cc, dd, aa, X_{15}, 8)
                                           FF(bbb, ccc, ddd, aaa, X_{15} + K_0, 8)
step 15
```

Second round

```
GG(aa, bb, cc, dd, X_7 + K_1, 7)
                                               GG(aaa, bbb, ccc, ddd, X_7, 7)
step 16
step 17
          GG(dd, aa, bb, cc, X_4 + K_1, 6)
                                               GG(ddd, aaa, bbb, ccc, X_4, 6)
                                               GG(ccc, ddd, aaa, bbb, X_{13}, 8)
          GG(cc, dd, aa, bb, X_{13} + K_1, 8)
step 18
          GG(bb, cc, dd, aa, X_1 + K_1, 13)
                                               GG(bbb, ccc, ddd, aaa, X_1, 13)
step 19
                                               GG(aaa, bbb, ccc, ddd, X_{10}, 11)
step 20
          GG(aa, bb, cc, dd, X_{10} + K_1, 11)
          GG(dd, aa, bb, cc, X_6 + K_1, 9)
                                               GG(ddd, aaa, bbb, ccc, X_6, 9)
step 21
          GG(cc, dd, aa, bb, X_{15} + K_1, 7)
                                               GG(ccc, ddd, aaa, bbb, X_{15}, 7)
step 22
          GG(bb, cc, dd, aa, X_3 + K_1, 15)
                                               GG(bbb, ccc, ddd, aaa, X_3, 15)
step 23
          GG(aa, bb, cc, dd, X_{12} + K_1, 7)
                                                GG(aaa, bbb, ccc, ddd, X_{12}, 7)
step 24
step 25
          GG(dd, aa, bb, cc, X_0 + K_1, 12)
                                               GG(ddd, aaa, bbb, ccc, X_0, 12)
                                                GG(ccc, ddd, aaa, bbb, X_9, 15)
          GG(cc, dd, aa, bb, X_9 + K_1, 15)
step 26
          GG(bb, cc, dd, aa, X_5 + K_1, 9)
                                                GG(bbb, ccc, ddd, aaa, X_5, 9)
step 27
                                                GG(aaa, bbb, ccc, ddd, X_{14}, 7)
step 28
          GG(aa, bb, cc, dd, X_{14} + K_1, 7)
          GG(dd, aa, bb, cc, X_2 + K_1, 11)
                                                GG(ddd, aaa, bbb, ccc, X_2, 11)
step 29
          GG(cc, dd, aa, bb, X_{11} + K_1, 13)
                                               GG(ccc, ddd, aaa, bbb, X_{11}, 13)
step 30
          GG(bb, cc, dd, aa, X_8 + K_1, 12)
                                                GG(bbb, ccc, ddd, aaa, X_8, 12)
step 31
```

Third round

```
HH(aa, bb, cc, dd, X_3 + K_2, 11)
                                               HH(aaa, bbb, ccc, ddd, X_3 + K_3, 11)
step 32
         HH(dd, aa, bb, cc, X_{10} + K_2, 13)
                                               HH(ddd, aaa, bbb, ccc, X_{10} + K_3, 13)
step 33
                                               HH(ccc, ddd, aaa, bbb, X_2 + K_3, 14)
         HH(cc, dd, aa, bb, X_2 + K_2, 14)
step 34
         HH(bb, cc, dd, aa, X_4 + K_2, 7)
                                               HH(bbb, ccc, ddd, aaa, X_4 + K_3, 7)
step 35
step 36
         HH(aa, bb, cc, dd, X_9 + K_2, 14)
                                               HH(aaa, bbb, ccc, ddd, X_9 + K_3, 14)
                                               HH(ddd, aaa, bbb, ccc, X_{15} + K_3, 9)
step 37
         HH(dd, aa, bb, cc, X_{15} + K_2, 9)
step 38
         HH(cc, dd, aa, bb, X_8 + K_2, 13)
                                               HH(ccc, ddd, aaa, bbb, X_8 + K_3, 13)
         HH(bb, cc, dd, aa, X_1 + K_2, 15)
                                               HH(bbb, ccc, ddd, aaa, X_1 + K_3, 15)
step 39
         HH(aa, bb, cc, dd, X_{14} + K_2, 6)
                                               HH(aaa, bbb, ccc, ddd, X_{14} + K_3, 6)
step 40
                                               HH(ddd, aaa, bbb, ccc, X_7 + K_3, 8)
         HH(dd, aa, bb, cc, X_7 + K_2, 8)
step 41
                                               HH(ccc, ddd, aaa, bbb, X_0 + K_3, 13)
step 42
         HH(cc, dd, aa, bb, X_0 + K_2, 13)
                                               HH(bbb, ccc, ddd, aaa, X_6 + K_3, 6)
step 43
         HH(bb, cc, dd, aa, X_6 + K_2, 6)
         HH(aa, bb, cc, dd, X_{11} + K_2, 12)
                                               HH(aaa, bbb, ccc, ddd, X_{11} + K_3, 12)
step 44
         HH(dd, aa, bb, cc, X_{13} + K_2, 5)
                                               HH(ddd, aaa, bbb, ccc, X_{13} + K_3, 5)
step 45
         HH(cc, dd, aa, bb, X_5 + K_2, 7)
                                               HH(ccc, ddd, aaa, bbb, X_5 + K_3, 7)
step 46
          HH(bb, cc, dd, aa, X_{12} + K_2, 5)
                                               HH(bbb, ccc, ddd, aaa, X_{12} + K_3, 5)
step 47
```

Finally compute the output AA, BB, CC, DD as follows:

$$AA = B + cc + ddd,$$

$$BB = C + dd + aaa,$$

$$CC = D + aa + bbb,$$

$$DD = A + bb + ccc.$$

That is one sets

compress
$$(A, B, C, D; X_0, X_1, \dots, X_{15}) = (AA, BB, CC, DD).$$

A.2. Compress Functions of RIPEMD^[12] and RIPEMD^[23]

By compress^[12] we denote the reduced version of compress, where the last round (steps 32–47) is omitted. Similarly, compress^[23] denotes the reduced version of compress, where the first round (steps 0–15) is omitted and—for the sake of convenience—the application of the X_i is permuted such that they occur in their natural ordering in the first round of compress^[23] (i.e., the second round of compress). Explicitly the steps of compress^[23] are therefore:

First round of compress^[23]

```
step 0
         GG(aa, bb, cc, dd, X_0 + K_1, 7)
                                               GG(aaa, bbb, ccc, ddd, X_0, 7)
                                               GG(ddd, aaa, bbb, ccc, X_1, 6)
step 1
         GG(dd, aa, bb, cc, X_1 + K_1, 6)
step 2
         GG(cc, dd, aa, bb, X_2 + K_1, 8)
                                               GG(ccc, ddd, aaa, bbb, X_2, 8)
         GG(bb, cc, dd, aa, X_3 + K_1, 13)
                                               GG(bbb, ccc, ddd, aaa, X_3, 13)
step 3
step 4
         GG(aa, bb, cc, dd, X_4 + K_1, 11)
                                               GG(aaa, bbb, ccc, ddd, X_4, 11)
         GG(dd, aa, bb, cc, X_5 + K_1, 9)
                                               GG(ddd, aaa, bbb, ccc, X_5, 9)
step 5
          GG(cc, dd, aa, bb, X_6 + K_1, 7)
step 6
                                               GG(ccc, ddd, aaa, bbb, X_6, 7)
step 7
         GG(bb, cc, dd, aa, X_7 + K_1, 15)
                                               GG(bbb, ccc, ddd, aaa, X_7, 15)
step 8
          GG(aa, bb, cc, dd, X_8 + K_1, 7)
                                               GG(aaa, bbb, ccc, ddd, X_8, 7)
          GG(dd, aa, bb, cc, X_9 + K_1, 12)
step 9
                                               GG(ddd, aaa, bbb, ccc, X_9, 12)
         GG(cc, dd, aa, bb, X_{10} + K_1, 15)
step 10
                                               GG(ccc, ddd, aaa, bbb, X_{10}, 15)
step 11
          GG(bb, cc, dd, aa, X_{11} + K_1, 9)
                                               GG(bbb, ccc, ddd, aaa, X_{11}, 9)
step 12
          GG(aa, bb, cc, dd, X_{12} + K_1, 7)
                                               GG(aaa, bbb, ccc, ddd, X_{12}, 7)
step 13
          GG(dd, aa, bb, cc, X_{13} + K_1, 11)
                                               GG(ddd, aaa, bbb, ccc, X_{13}, 11)
          GG(cc, dd, aa, bb, X_{14} + K_1, 13)
step 14
                                               GG(ccc, ddd, aaa, bbb, X_{14}, 13)
          GG(bb, cc, dd, aa, X_{15} + K_{1}, 12)
step 15
                                               GG(bbb, ccc, ddd, aaa, X_{15}, 12)
```

Second round of compress^[23]

```
step 16 HH(aa, bb, cc, dd, X_7 + K_2, 11) HH(aaa, bbb, ccc, ddd, X_7 + K_3, 11) step 17 HH(dd, aa, bb, cc, X_4 + K_2, 13) HH(ddd, aaa, bbb, ccc, X_4 + K_3, 13) step 18 HH(ccc, dd, aa, bb, X_{13} + K_2, 14) HH(ccc, ddd, aaa, bbb, X_{13} + K_3, 14)
```

```
step 19
         HH(bb, cc, dd, aa, X_1 + K_2, 7)
                                               HH(bbb, ccc, ddd, aaa, X_1 + K_3, 7)
step 20
         HH(aa, bb, cc, dd, X_{10} + K_2, 14)
                                               HH(aaa, bbb, ccc, ddd, X_{10} + K_3, 14)
         HH(dd, aa, bb, cc, X_6 + K_2, 9)
                                               HH(ddd, aaa, bbb, ccc, X_6 + K_3, 9)
step 21
         HH(cc, dd, aa, bb, X_{15} + K_2, 13)
step 22
                                               HH(ccc, ddd, aaa, bbb, X_{15} + K_3, 13)
step 23
         HH(bb, cc, dd, aa, X_3 + K_2, 15)
                                               HH(bbb, ccc, ddd, aaa, X_3 + K_3, 15)
                                               HH(aaa, bbb, ccc, ddd, X_{12} + K_3, 6)
step 24
         HH(aa, bb, cc, dd, X_{12} + K_2, 6)
         HH(dd, aa, bb, cc, X_0 + K_2, 8)
step 25
                                               HH(ddd, aaa, bbb, ccc, X_0 + K_3, 8)
step 26
         HH(cc, dd, aa, bb, X_9 + K_2, 13)
                                               HH(ccc, ddd, aaa, bbb, X_9 + K_3, 13)
step 27
         HH(bb, cc, dd, aa, X_5 + K_2, 6)
                                               HH(bbb, ccc, ddd, aaa, X_5 + K_3, 6)
step 28
         HH(aa, bb, cc, dd, X_{14} + K_2, 12)
                                               HH(aaa, bbb, ccc, ddd, X_{14} + K_3, 12)
         HH(dd, aa, bb, cc, X_2 + K_2, 5)
                                               HH(ddd, aaa, bbb, ccc, X_2 + K_3, 5)
step 29
step 30
         HH(cc, dd, aa, bb, X_{11} + K_2, 7)
                                               HH(ccc, ddd, aaa, bbb, X_{11} + K_3, 7)
step 31
          HH(bb, cc, dd, aa, X_8 + K_2, 5)
                                               HH(bbb, ccc, ddd, aaa, X_8 + K_3, 5)
```

The reduced version of RIPEMD, where compress is replaced by compress^[12] (resp. compress^[23]), is denoted by RIPEMD^[12] (resp. RIPEMD^[23]).

Because we refer to it in a remark concerning almost collisions (see the end of Section 3), we finally define the three-round compress function compress^[231] as a modification of the original RIPEMD compress, where the first round is put at the end. The first two rounds of compress^[231] coincide with compress^[23]. The ordering in which the blocks X_i are applied in the single steps of compress^[231] is the same as for compress, since this ordering is a fundamental design principle of RIPEMD. The last round of compress^[231] is therefore defined as follows:

```
FF(aaa, bbb, ccc, ddd, X_3 + K_0, 11)
step 32
          FF(aa, bb, cc, dd, X_3, 11)
                                         FF(ddd, aaa, bbb, ccc, X_{10} + K_0, 14)
         FF(dd, aa, bb, cc, X_{10}, 14)
step 33
                                         FF(ccc, ddd, aaa, bbb, X_2 + K_0, 15)
          FF(cc, dd, aa, bb, X_2, 15)
step 34
                                         FF(bbb, ccc, ddd, aaa, X_4 + K_0, 12)
step 35
          FF(bb, cc, dd, aa, X_4, 12)
step 36
          FF(aa, bb, cc, dd, X_9, 5)
                                         FF(aaa, bbb, ccc, ddd, X_9 + K_0, 5)
          FF(dd, aa, bb, cc, X_{15}, 8)
                                         FF(ddd, aaa, bbb, ccc, X_{15} + K_0, 8)
step 37
          FF(cc, dd, aa, bb, X_8, 7)
                                         FF(ccc, ddd, aaa, bbb, X_8 + K_0, 7)
step 38
                                         FF(bbb, ccc, ddd, aaa, X_1 + K_0, 9)
step 39
          FF(bb, cc, dd, aa, X_1, 9)
                                         FF(aaa, bbb, ccc, ddd, X_{14} + K_0, 11)
step 40
          FF(aa, bb, cc, dd, X_{14}, 11)
          FF(dd, aa, bb, cc, X_7, 13)
                                         FF(ddd, aaa, bbb, ccc, X_7 + K_0, 13)
step 41
                                         FF(ccc, ddd, aaa, bbb, X_0 + K_0, 14)
          FF(cc, dd, aa, bb, X_0, 14)
step 42
step 43
                                         FF(bbb, ccc, ddd, aaa, X_6 + K_0, 15)
          FF(bb, cc, dd, aa, X_6, 15)
                                         FF(aaa, bbb, ccc, ddd, X_{11} + K_0, 6)
step 44
          FF(aa, bb, cc, dd, X_{11}, 6)
          FF(dd, aa, bb, cc, X_{13}, 7)
                                         FF(ddd, aaa, bbb, ccc, X_{13} + K_0, 7)
step 45
          FF(cc, dd, aa, bb, X_5, 9)
                                         FF(ccc, ddd, aaa, bbb, X_5 + K_0, 9)
step 46
                                         FF(bbb, ccc, ddd, aaa, X_{12} + K_0, 8)
          FF(bb, cc, dd, aa, X_{12}, 8)
step 47
```

Note Added in Proof (July 1996). As a reaction to the presented attack, RIPEMD has meanwhile been replaced in the ISO/IEC Draft 10118-3 by its strengthened successors RIPEMD-160 and RIPEMD-128 (H. Dobbertin, A. Bosselaers, and B. Preneel, RIPEMD-160: A strengthened version of RIPEMD, Fast Software Encryption (Proceed-

ings of the 1996 Cambridge Workshop on Cryptographic Algorithms), Lecture Notes in Computer Science, vol. 1039, Springer-Verlag, Berlin, 1996, pp. 71–82).

Very recently it has been demonstrated that collisions of the compress function of MD5 can be found (H. Dobbertin, The status of MD5 after a recent attack, *CryptoBytes*, *The technical newsletter of RSA Laboratories*, vol. 2, Summer issue, 1996).

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