# Modular Construction of a Byzantine Agreement Protocol with Optimal Message Bit Complexity* 

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#### Abstract

This paper presents a new Byzantine agreement protocol that tolerates $t$ processor faults using $3 t+1$ processors, $t+o(t)$ rounds, $O\left(t^{2}\right)$ total message bits, and $O\left(t^{2}\right)$ maximum message size, for any $\varepsilon>0$. The protocol is optimal or near optimal in all cost measures: the number of processors is optimal, the message bit complexity is optimal, the number of rounds exceeds the lower bound by $o(t)$, and the maximum message size exceeds the lower bound by $O\left(t^{t}\right)$. The round complexity is uniformly better than $2 \cdot(t+1)$ and thus is reasonable even for small $t$. This is the first Byzantine agreement protocol to have optimal message bit complexity. The new protocol is constructed by recursively applying a simple, yet general, transformation that changes the number of rounds, total message bits, and maximum message size required by a Byzantine agreement protocol, but preserves correctness, number of processor faults tolerated, and total number of processors. Each application of this new transformation reduces the number of message bits sent-at the expense of adding rounds of communication. Surprisingly, the base case of the recursive construction is the agreement protocol of Lamport, Shostak, and Pease, which has a number of message bits exponential in $t$. © 1992 Academic Press, Inc.


## 1. Introduction

In the field of distributed computing, Byzantine agreement is a fundamental problem which was first introduced by Pease, Shostak, and Lamport (1980). Many variants of this problem have been studied. In this

[^0]paper we consider a system of deterministic processors that communicate by message passing in a sequence of synchronous rounds. Some processors are correct, following their protocol exactly. The rest experience Byzantine faults, deviating from their protocols in arbitrary ways. Each processor begins with an input bit and eventually each nonfaulty processor must irrevocably decide on an output bit satisfying two conditions: (1) all the output bits are the same, and (2) if all the input bits are the same, then all the output bits must equal the input bits. For this version of the problem, there is currently a gap between the known lower bounds on resources and the most efficient known protocols. The results in this paper help narrow that gap.

There are five cost measures that interest us: number of processors, rounds of communication, total number of message bits, size of the largest message, and amount of local computation. If $t$ is an upper bound on the number of processor faults, then the following lower bounds are known: $3 t+1$ processors (Lamport, Shostak, and Pease, 1982; Fischer, Lynch, and Merritt, 1986), $t+1$ rounds (Fischer and Lynch, 1982), $\Omega\left(t^{2}\right)$ message bits (Dolev and Reischuk, 1985), and maximum message size of 1 . The lower bounds on processors, message size, and rounds were known to be achievable. For example, the original protocol of Lamport, Shostak, and Pease (1982) achieves the bounds on processors and rounds (but requires a number of message bits that is exponential in $t$ ). Protocols of Bar-Noy and Dolev (1991) and Coan (1988) achieve the lower bound on message size and rounds (but exceed the lower bounds on processors and total message bits). The best previous upper bound on message bits is achieved by the protocol of Dolev et al. (1982), requiring $O\left(t^{3} \log t\right)$ total message bits. Our new protocol achieves a message bit complexity of $O\left(t^{2}\right)$, which is optimal. Most protocols, including ours, achieve polynomial local computation; we will not discuss this measure further.

Our technique consists of the recursive application of a general two-step transformation that reduces the number of message bits and the size of the largest message sent at the expense of a small increase in the number of rounds. The transformation leaves unchanged the number of processors and the number of faults tolerated. The first step transforms any Byzantine agreement protocol that has a known upper bound on rounds-essentially all existing protocols have such a bound-into a protocol for the Byzantine broadcast problem. The Byzantine broadcast problem is a new problem first defined in this paper. In a Byzantine broadcast protocol, a subset of the processors, called the "committee," attempts to reach agreement and to communicate the decision to the rest of the processors. The resulting Byzantine broadcast protocol operates correctly as long as there are enough nonfaulty processors in its committee. The second step of our transformation produces a Byzantine agreement protocol from a collection
of Byzantine broadcast protocols that have disjoint committees. For the combined protocol to work correctly, it is sufficient that the fault-tolerance assumption of any onc of its constituent Byzantinc broadcast protocols be satisfied-it does not impair the correct operation of the combined protocol if the committees of the remaining Byzantine broadcast protocols have "too many" faulty processors. Thus, we can combine $B$ Byzantine broadcast protocols, tolerating $t_{1}, \ldots, t_{B}$ faults, respectively, to produce a Byzantine agreement protocol tolerant of $t_{1}+\cdots+t_{B}+B-1=t$ faults overall. The resulting protocol satisfies the condition referred to above, and thus our transformation can be applied recursively.

One application of our transformation takes $B$, the number of committees, as a parameter. In applying the transformation recursively, we introduce two additional parameters, $l$ and $d$. The parameter $l$ is a lower bound on the number of faults to be tolerated by protocol instances at the base of the recursion. (There is a trade-off between the message size complexity and the round complexity in the choice of $l$ : a smaller $l$ means a smaller message size but more rounds.) Finally, we let the depth of the recursion be a function $f$ of $t$, the number of faults to be tolerated, and $d$, our third parameter. (In sufficiently large systems, the parameter $d$ determines a small constant increment in the depth of the recursion, thereby reducing the message size; it is chosen to be max $\{4,\lceil 1 / \varepsilon\rceil\}$, where $O\left(t^{\varepsilon}\right)$ is the desired upper bound on message size.) By using the protocol of Lamport, Shostak, and Pease (1982) at the base of the recursion and by defining $f$ appropriately, we obtain a Byzantine agreement protocol that uses $3 t+1$ processors, $t+o(t)$ rounds, $O\left(t^{2}\right)$ message bits, and $O\left(t^{2}\right)$ message size, for any $\varepsilon>0$. Judicious choices for $B$ and $l$, discussed in Section 6, cause the round complexity to be uniformly better than $2 \cdot(t+1)$, even for small values of $t$.

A different attempt at narrowing the gap in resource requirements for Byzantine agreement is the protocol of Moses and Waarts (1992), which uses $6 t+1$ processors, $O\left(t^{8}\right)$ message bits, and exactly $t+1$ rounds. Their protocol is an ingenious synthesis of several complicated techniques; our approach is simpler. The protocol of Berman, Garay, and Perry (1989a) has further reduced the number of processors required to $4 t+1$ while leaving the other cost measures essentially unchanged. Compared to these two protocols, our protocol uses fewer processors and message bits, at the expense of using $o(t)$ more rounds and of scarificing early stopping (Dolev, Reischuk, and Strong, 1990).

Our technique resembles the "shifting gears" work of Bar-Noy et al. (1992) in that it allows us to combine several Byzantine agreement protocols into one that is more efficient than any of the originals. However, our technique treats the Byzantine agreement protocols completely abstractly and works for any collection of Byzantine agreement protocols
(that terminate in a known bounded number of rounds). The technique of Bar-Noy et al. (1992) only works for protocols of a specific form, a form that excludes some known protocols. There is also a similarity between the structure of our new protocol and the structure of the randomized agreement protocols of Ben-Or (1983), Rabin (1983), and Chor and Coan (1985): the skeleton is the same, while the distributed coin flip in the randomized protocols is replaced by Byzantine broadcast in our (deterministic) protocol.

In a preliminary version of this paper (Coan and Welch, 1989a), we gave a modular construction of a class of protocols with $3 t+1$ processors, $t+o(t)$ rounds, $O(t)$ message size, and $O\left(t^{2+\varepsilon}\right)$ total message bits, for all $\varepsilon>0$. Simultaneously and independently Berman and Garay (1989) used a single application of a transformation similar to ours to obtain a class of protocols with $4 t+1$ processors, $(1+\varepsilon)(t+1)$ rounds, $t^{O(1 / \varepsilon)}$ message size, and $t^{O(1 / \varepsilon)}$ total message bits, for all $\varepsilon, 0<\varepsilon \leqslant 1$. Subsequently, it was observed independently by Berman, Garay, and Perry (1989b) and by Coan and Welch (1989b) that a slight variant of the recursive construction in Coan and Welch (1989a) yields a single protocol with optimal message bit complexity-the protocol presented in this paper.

By a result of Lamport, Shostak, and Pease (1982), no Byzantine agreement protocol for $t$ faults can exist unless there are at least $3 t+1$ processors. In this paper we only consider the case where there are exactly $3 t+1$ processors. Since processors are an expensive resource, it is reasonable to minimize the total number of processors to achieve a certain level of fault tolerance, as represented by $t$. The easiest way to generalize our results for more than $3 t+1$ processors is to utilize the simple and elegant transformation given by Dolev et al. (1982). Details are left to the reader.

In Section 2 we review the model. In Section 3 we define the Byzantine agreement and Byzantine broadcast problems. In Section 4 we describe the two components of our general transformation and we combine these to give the general transformation. In Section 5 we construct our new protocol by recursively applying our general transformation. In Section 6 we bound the constant in the round complexity of our protocol. In Section 7 we discuss other applications of our transformation.

## 2. Model

We consider a computer system consisting of a collection $P$ of processors that interact by sending messages over links. Each link provides a bidirectional communication path between a pair of processors. The collection of links constitutes the communication network. It is known that no agree-
ment protocol is possible unless the connectivity of the communication network is at least $2 t+1$ (Dolev, 1982; Fischer, Lynch, and Merritt, 1986). Throughout this paper we assume that the communication network is fully connected and reliable.

We assume a synchronous timing model, meaning that all processors take steps in unison and all messages are received in the next step after they are sent. An execution of a protocol consists of a series of rounds. In each round each processor sends messages, whose contents depend on its internal state, receives all the messages just sent to it, and acts on the messages received by altering its internal state. The internal state of a processor is simply the values of its variables and program counter. A protocol for $P$, where $P$ is a set of processors, prescribes what the processors' internal states are, what the initial internal states are, what messages should be sent, and how internal states should be altered. Each processor in $P$ is assumed to have a unique name known to all the processors, and each processor can reliably determine the name of the sender of each message that it receives.

Each processor's state set contains a subset of initial states, partitioned into $v$-initial states, and a subset of final states, partitioned into $v$-final states, with the $v$-initial state of each processor being unique, $v \in\{0,1\}$. In an execution, if processor $p$ starts in a $v$-initial state, then $v$ is $p$ s input; if $p$ enters a $v$-final state, then $v$ is $p$ 's output. At the beginning of an execution, all nonfaulty processors must be in initial states. A protocol can only map $v$-final states to $v$-final states, to model the irrevocability of deciding on an output.

In an execution of a protocol, some processors are nonfaulty, and the rest are faulty. A nonfaulty processor sends messages and changes state according to the protocol. A faulty processor can deviate from the protocol. We are interested in protocols that tolerate Byzantine processor faults. In the Byzantine fault model there are no constraints on the contents of messages sent by a faulty processor; however, the recipient of any message knows the identity of the sender. Although a faulty processor may send different messages to each of the other processors, it may not send multiple messages to any single recipient in a round.

Our protocols will call other protocols as subroutines, using the following syntax and semantics. Let $\mathscr{P}$ and $\mathscr{P}^{\prime}$ be protocols for $P$ and $P^{\prime}$, respectively, where $P^{\prime}$ is a subset of $P$. Processor $p$ in $P^{\prime}$, during an execution $e$ of protocol $\mathscr{P}$, may execute in some (constant, i.e., fixed before the start of execution and therefore not computed by $p$ ) round, say $r$,

$$
\text { call } \mathscr{P}^{\prime} \text { (rounds: } R \text {, input: } X \text {, output: } Y \text { ). }
$$

We require that every processor in $P^{\prime}$ call $\mathscr{P}^{\prime}$ in round $r$ with the same (constant, i.e., fixed before the start of execution and therefore not
computed by that processor) value of $R$. (This is a requirement on the code, not a requirement on the behavior of faulty processors.) The effect of the call is that $p$ gets a new set of variables, as specificd by the protocol for $p$ in $\mathscr{P}^{\prime}$, initialized to a $v$-initial state, where $v$ is the value of variable $X$ at the beginning of round $r ; p$ computes according to $\mathscr{P}^{\prime}$ for the $R$ rounds indexed from $r$ to $r+R-1$; and $p$ returns value $w$ in variable $Y$, where $p$ ends in a $w$-final state after the $R$ rounds ( $Y=$ uninitialized if $p$ is not in a final state). The value of $Y$ is available to the compute phase of round $r+R-1$.

The processor complexity of a protocol for $P$ is $|P|$. The round complexity of a protocol is the maximum, over all executions $e$, of the number of rounds of $e$ required for all nonfaulty processors to be in final states. The message bit complexity of a protocol is the maximum, over all executions $e$, of the total number of bits sent in messages by nonfaulty processors in $e$. The message size complexity of a protocol is the maximum, over all executions $e$, of the number of bits sent in any single message by any nonfaulty processor in $e$.

## 3. Problem Statements

Our Byzantine agreement protocol uses as subroutines solutions to a closely related problem, which we call the Byzantine broadcast problem. We now define these two problems.

### 3.1. Byzantine Agreement

A protocol for $P$ is a Byzantine agreement protocol for $P$ and $t, t<|P|$, if there exists an integer $R \geqslant 0$ such that every execution at least $R$ rounds long satisfies the following three conditions.

- Termination. At the end of $R$ rounds every nonfaulty processor in $P$ is in a final state.
- Agreement. If at most $t$ processors in $P$ are faulty, then there is a $v$ such that any nonfaulty processor in $P$ that enters a final state enters a $v$-final state.
- Validity. If at most $t$ processors in $P$ are faulty and there is a $v$ such that all nonfaulty processors in $P$ begin in a $v$-initial state, then any nonfaulty processor in $P$ that enters a final state enters a $v$-final state.

This definition imposes the nonstandard requirement that a nonfaulty processor must eventually enter a final state, even if more than $t$ processors are faulty. The more standard termination requirement, used elsewhere in
the literature, is that a nonfaulty processor must eventually enter a final state in any execution in which $t$ or fewer processors are faulty. Most, but not all, protocols that satisfy the standard termination requirement can trivially be transformed into protocols that satisfy our termination requirement. In particular, this transformation is possible for any protocol for which there is a known upper bound, possibly a function of $|P|$ and $t$, on the number of rounds needed for termination.

We have defined a Byzantine agreement protocol for a fixed $P$ and $t$. There is a natural sense in which we would like to consider two Byzantine agreement protocols, for different sets of processors and different $t$ 's, to be instances of the same "protocol." We thus assume an infinite set of processor names $U$ and define a Byzantine agreement protocol family to be an infinite set $\{\mathrm{BA}(n, t): n \geqslant 1$ and $t \geqslant 0\}$ satisfying the following condition. For all $n$ and $t$, either $\mathrm{BA}(n, t)$ is the empty set, or $\mathrm{BA}(n, t)$ is an infinite set of Byzantine agreement protocols, one protocol $\mathrm{BA}(P, t)$ for each $n$-element subset $P$ of $U$. Each $\mathrm{BA}(P, t)$ must be a Byzantine agreement protocol for $P$ and $t$. We require that for all $n$-element sets $P$ and $P^{\prime}$, $\mathrm{BA}(P, t)$ and $\mathrm{BA}\left(P^{\prime}, t\right)$ are copies of each other (i.e., they are the same protocol except for renaming of processors).

We now define complexity measures for Byzantine agreement protocol families. We do not need analogous definitions for Byzantine broadcast.

The processor complexity, $P(t)$ of protocol family BA is the minimum $n$ such that $\mathrm{BA}(n, t) \neq \varnothing$, i.e., the minimum number of processors needed to tolerate $t$ faults. The rest of the complexity measures are defined assuming the minimum number of processors. The round complexity, $R(t)$, of protocol family BA is the function describing the round complexity of protocols in the set $\mathrm{BA}(n, t)$, where $n=P(t)$. The message bit complexity, $M(t)$, of protocol family BA is the function describing the message bit complexity of protocols in the set $\mathrm{BA}(n, t)$, where $n=P(t)$. The message size complexity, $S(t)$, of protocol family BA is the function describing the message size complexity of protocols in the set $\operatorname{BA}(n, t)$, where $n=P(t)$.

### 3.2. Byzantine Broadcast

The Byzantine broadcast problem is essentially the problem of having a known, fixed subset of the processors, called the committee, perform Byzantine agreement and communicate the result to the rest of the processors. Thus the committee causes each of the nonfaulty processors to choose an element of $\{0,1\}$ in such a way that if enough members of the committee are nonfaulty then (1) all the nonfaulty processors choose the same element, and (2) if there is a $v$ in $\{0,1\}$ such that all the members of the committee begin with the input $v$, then all the nonfaulty processors choose $v$. The identity of each member of the committee is known to all processors.

A protocol for $P$ is a Byzantine broadcast protocol for $P, C$, and $t^{\prime}, C \subseteq P$, $t^{\prime}<|C|$, if there exists an integer $R \geqslant 0$ such that every execution at least $R$ rounds long satisfies the following three conditions. ( $C$ is the committee.)

- Termination. At the end of $R$ rounds every nonfaulty processor in $P$ is in a final state.
- Agreement. If at most $t^{\prime}$ processors in $C$ are faulty, then there is a $v$ such that any nonfaulty processor in $P$ that enters a final state enters a $v$-final state.
- Validity. If at most $t^{\prime}$ processors in $C$ are faulty, and if there is a $v$ such that all nonfaulty processors in $C$ begin in a $v$-initial state, then any nonfaulty processor in $P$ that enters a final state enters a $v$-final state.


## 4. Transformations

Our general transformation is the composition of two transformations, the first from a Byzantine agreement protocol to a Byzantine broadcast protocol, and the second from a collection of Byzantine broadcast protocols to a Byzantine agreement protocol. In this section we define the component transformations and the general transformation.

### 4.1. Constructing a Byzantine Broadcast Protocol from a Byzantine Agreement Protocol

This subsection describes a simple way to transform any Byzantine agreement protocol (with a known, fixed upper bound on rounds) into a Byzantine broadcast protocol. The processors in the committee perform the Byzantine agreement protocol and then send the result to the rest of the processors, each of which decides on a value that it receives "often enough."

The transformation is presented formally as Transformation 1, where $P$ is a sct of processors, $C \subseteq P$ is thc committce, $t^{\prime}$ is a nonncgative integer with $|C| \geqslant 2 t^{\prime}+1$, and BA is a Byzantine agreement protocol for $C$ and $t^{\prime}$ with round complexity $R$, message bit complexity $M$, and message size complexity $S$. The requirement that $|C| \geqslant 2 t^{\prime}+1$ is sufficient for the correctness of our transformation; although, in our model, the existence of a Byzantine agreement protocol to be transformed requires $|C| \geqslant 3 t^{\prime}+1$.

Transformation 1: Changing Byzantine Agreement to Byzantine Broadcast
Code for processor $p$ in $P$ :
Initialization code:
$b b-i n(p)$ is initialized to the input of processor $p$
$b b-o u t(p)$ is initialized to UNINTILALIzED; it will hold $p$ 's output

Code for round 1 :
if $p$ is in $C$
then call BA(rounds; $R$, input: $b b-i n(p)$, output: $b a-o u t(p)$ )
else do nothing
Code for round $r, 2 \leqslant r \leqslant R$ :
comment: processors in $C$ continue the subprotocol
processors in $P-C$ continue to do nothing
Code for round $R+1$ :
send: if $p$ is in $C$ then send $b a$-out $(p)$ to each processor in $P-C$
receive: if $p$ is in $P-C$ then receive messages from processors in $C$
compute: decisions $(p):=$ the multiset of the messages just received,
discarding any message that is not an element of $\{0,1\}$
if $p$ is in $C$
then $b b-o u t(p):=b a-o u t(p)$
else $b b$-out $(p):=$ the most common element in decisions $(p)$ (break ties by picking 0 as a default choice)

Let BB be the result of applying Transformation 1 to BA .

Theorem 1. BB is a Byzantine broadcast protocol for $P, C$, and $t^{\prime}$, with complexities $|P|$ processors, $R+1$ rounds, $M+(|P|-|C|) \cdot|C|$ message bits, and $\max \{1, S\}$ message size.

Proof. Choose any execution of BB at least $R+1$ rounds long.
Termination. We show that after $R+1$ rounds, each nonfaulty processor $p$ in $P$ has some output value for $b b-\operatorname{out}(p)$. If $p$ is in $C$, then since BA terminates after $R$ rounds, $b a$-out $(p)$ has some output value at the end of round $R$, and hence $b b-o u t(p)$ is assigned an output value in round $R+1$. If $p$ is in $P-C$, then the code clearly assigns an output value to $b b-o u t(p)$ in round $R+1$.

Agreement. Suppose at most $t^{\prime}$ processors in $C$ are faulty. Then by agreement of BA , after $R$ rounds there is some $v$ in $\{0,1\}$ such that $b a$-out $(p)=v$, for all nonfaulty $p$ in $C$. Then in round $R+1$, each nonfaulty $p$ in $C$ sets $b b-o u t(p)$ to be $v$, and each nonfaulty $p$ in $P-C$ receives at least $|C|-t^{\prime}$ messages for $v$ and at most $t^{\prime}$ messages for any $w \neq v$. Since $|C| \geqslant 2 t^{\prime}+1$, a majority of the messages are for $v$, and thus each nonfaulty $p$ in $P-C$ sets $b b$-out $(p)$ to be $v$ in round $R+1$.

Validity. Suppose at most $t^{\prime}$ processors in $C$ are faulty, and for some $v$ in $\{0,1\}$ and for all nonfaulty $p$ in $C, b b-\operatorname{in}(p)=v$. By validity of BA , after $R$ rounds $b a$-out $(p)=v$, for all nonfaulty $p$ in $C$. By the same argument as for agreement, $b b-o u t(p)=v$ in round $R+1$, for all nonfaulty $p$ in $P$.

Complexities. Obviously, the protocol uses $|P|$ processors. One extra round is added by Transformation 1, giving a total of $R+1$ rounds. The message bit complexity is equal to the message bit complexity $M$ of the BA subroutine, plus the maximum number of message bits sent by nonfaulty processors in the extra round, in any execution. The number of additional message bits is maximized in any execution with no faults: in the extra round, each of the $|C|$ processors in $C$ sends one bit to each of the $|P|-|C|$ processors not in $C$, for a total of $(|P|-|C|) \cdot|C|$. The size of the largest message sent is the maximum of the size $S$ of the largest message sent in the BA subroutine, and the size of the largest message sent in the extra round, which is one.

### 4.2. Constructing a Byzantine Agreement Protocol from Several Byzantine Broadcast Protocols

This subsection describes a simple way to transform a collection of Byzantine broadcast protocols into a Byzantine agreement protocol for $P$ and $t$.

During the agreement protocol, each correct processor establishes and maintains a favored value that it currently prefers as the decision. This favored value can be adjusted as the protocol proceeds. At any time the favored value can be 0 indicating a preference for 0,1 indicating a preference for 1 , or ? indicating no preference. The goal is to bring the favored values of the various processors into agreement on either 0 or 1 while not violating the validity condition.

A collection of disjoint subsets of processors is chosen (when the protocol is written, not executed). For each subset in the collection, all the processors in $P$ execute the following block of rounds. First, two rounds of preliminary message exchanges are performed, during which each processor in $P$ selects an element of $\{0,1\}$ to use as input to a Byzantine broadcast protocol and computes its current favored value. These preliminary rounds ensure that no two nonfaulty processors favor different elements of $\{0,1\}$, and if any nonfaulty processor favors a $v$ in $\{0,1\}$, then $v$ is the common input of all nonfaulty processors in the Byzantine broadcast protocol. Then the processors execute a Byzantine broadcast protocol using the inputs just calculated and using the current subset as the committee. The output of the Byzantine broadcast protocol replaces the favored value for any nonfaulty processor that lacked a preference for an element of $\{0,1\}$. The number and size of the subsets is chosen in such a way that at least one of the subsets includes sufficiently many nonfaulty processors. After the block of rounds using such a subset, all the processors have the same favored value, an element of $\{0,1\}$; this favored value persists until the end of the protocol, at which time it is chosen as the output for the Byzantine agreement.

Fix the following for the rest of Section 4:

- an integer $t$ (the number of faults to be tolerated overall);
- a set of processors $P,|P|=n=3 t+1$ (the collection of processors to reach agreement);
- an integer $B, 2 \leqslant B \leqslant t+1$ (the number of committees);
- integers $t_{b}$, where $t_{b}=\lfloor(t+1-b) / B\rfloor$, for $1 \leqslant b \leqslant B$ (the number of faults to be tolerated by the $b$ th committee);
- $B$ disjoint subsets of $P, C_{b}, 1 \leqslant b \leqslant B$ (the set of processors in the $b$ th committee), chosen arbitrarily such that $\left|C_{b}\right|=3 t_{b}+1$; and
- Byzantine broadcast protocols $\mathrm{BB}_{b}$ for $P, C_{b}$, and $t_{b}$, with round complexity $R_{b}$, message bit complexity $M_{b}$, and message size complexity $S_{h}, 1 \leqslant b \leqslant B$.

A few comments are in order. The $t_{b}$ 's are approximately equal integers whose sum is $t+1-B$, as is shown in Lemma 1. Thus, $P$ is large enough to choose the disjoint $C_{b}$ 's (i.e., $\sum_{b=1}^{B}\left(3 t_{b}+1\right) \leqslant 3 t+1$, since $\left.B \geqslant 2\right)$ and, as shown in Lemma 2, as long as there are at most $t$ faulty processors in $P$, there will be at least one $\mathrm{BB}_{b}$ that operates correctly (i.e., enough processors in $C_{b}$ are nonfaulty).

We now construct a Byzantine agreement protocol for $P$ and $t$, using the $\mathrm{BB}_{b}$ 's. Rounds are numbered using ordered pairs $(b, i)$, where $1 \leqslant b \leqslant B$ and $1 \leqslant i \leqslant R_{b}+2$; the right-hand element of the pair increases faster, giving a lexicographic ordering of the pairs. Let block $b$ be rounds $(b, 1)$ through ( $b, R_{b}+2$ ) inclusive.

## Transformation 2: Changing Byzantine Broadcasts to Byzantine Agreement

Code for processor $p$ in $P$ :
Initialization code:
favor $(p)$ is initialized to the input to processor $p$
$b a-o u t(p)$ is initialized to UNINITIALIZED; it will hold $p$ 's output
Code for rounds $(b, 1)$ and $(b, 2), 1 \leqslant b \leqslant B$ :
send: if $\operatorname{favor}(p) \in\{0,1\}$ then send $\operatorname{favor}(p)$ to each processor in $P$
else do nothing
receive: receive messages from processors in $P$
compute: values $(p):=$ the multiset of the messages just received, discarding any message that is not an element of $\{0,1\}$
common $(p):=$ the most common element in values $(p)$
(break ties by picking 0 as a default choice)
number $(p):=$ the cardinality of common $(p)$ in values $(p)$
if number $(p) \geqslant n-t$
then $\operatorname{favor}(p):=\operatorname{common}(p)$ else favor $(p):=$ ?

Code for round $(b, 3), 1 \leqslant b \leqslant B$ :
call $\mathrm{BB}_{b}$ (rounds: $R_{b}$, input: common $(p)$, output: $b b-$ out $(p)$ )
Code for rounds $(b, 4)$ through $\left(b, R_{b}+2\right), 1 \leqslant b \leqslant B$ :
comment: all processors continue the subprotocol
Code added to the end of the compute phase of round $\left(b, R_{b}+2\right)$, $1 \leqslant b \leqslant B$ :

$$
\begin{aligned}
& \text { if } \operatorname{favor}(p)=? \text { then } \operatorname{favor}(p):=b b-o u t(p) \\
& \text { if } b=B \text { then } b a-o u t(p):=\operatorname{favor}(p)
\end{aligned}
$$

Let BA be the protocol obtained by applying Transformation 2 to $\mathrm{BB}_{b}$, $1 \leqslant b \leqslant B$.

Theorem 2. BA is a Byzantine agreement protocol for $P$ and $t$, with complexities

- $n$ processors,
- $\sum_{b=1}^{B}\left(R_{b}+2\right)$ rounds,
- $\sum_{b=1}^{B}\left(M_{b}+2 n^{2}\right)$ message bits, and
- $\max \left\{1, S_{1}, \ldots, S_{B}\right\}$ message size.

Five lemmas are needed for the proof of Theorem 2. Lemma 1 establishes a relationship between the total number of faults and the fault tolerance of the committees. This relationship is used in the proof of Lemma 2, which states that there is at least one committee whose fault tolerance is not exceeded. Lemma 3 is a technical result used in the proof of Lemma 4. Lemma 4 states that there is a block such that all the nonfaulty processors will favor the same element of $\{0,1\}$ at the end of that block. Lemma 5 states that once all the nonfaulty processors favor the same $v$ in $\{0,1\}$, then they will continue to favor $v$.

Lemma 1. $\sum_{b=1}^{B} t_{b}=t+1-B$.
Proof. Recall that $t_{b}=\lfloor(t+1-b) / B\rfloor$ for all $b, 1 \leqslant b \leqslant B$. Choose nonnegative integers $x$ and $y$ such that $t+1=x B+y$ and $y<B$. Then

$$
t_{b}= \begin{cases}x & \text { if } 1 \leqslant b \leqslant y \\ x-1 & \text { otherwise }\end{cases}
$$

Thus

$$
\sum_{b=1}^{B} t_{b}=y \cdot x+(B-y)(x-1)
$$

Substituting $(t+1-y) / B$ for $x$ and simplifying yields the result.
Lemma 2. Consider any execution of BA at least B blocks long, in which at most $t$ processors in $P$ are faulty. Then there is some $b, 1 \leqslant b \leqslant B$, such that at most $t_{b}$ processors in $C_{b}$ are faulty.

Proof. Suppose not. Then since the $C_{b}$ are disjoint, there are at least $\sum_{b=1}^{B}\left(t_{b}+1\right)$ faulty processors in $P$. We then calculate that

$$
\begin{aligned}
\sum_{b=1}^{B}\left(t_{b}+1\right) & =B+\sum_{b=1}^{B} t_{b} \\
& =B+t+1-B \quad \text { by Lemma } 1 \\
& =t+1
\end{aligned}
$$

Thus we have contradicted the choice of $t$ as the upper bound on the number of faulty processors in $P$.

We use the following notation (assuming a particular execution of BA). Let $X(p)[b]$ be the value of variable $X(p)$ at the end of block $b$ (i.e., at the end of round ( $b, R_{b}+2$ )). Let $X(p)[0]$ be the value of variable $X(p)$ initially. Let $X(p)[b, r]$ be the value of variable $X(p)$ at the end of round (b, r).

Lemma 3. Consider any execution of $B A$ at least $B$ blocks long, in which at most $t$ processors in $P$ are faulty. For all nonfaulty $p$ and $q$ in $P$, and all $b, 1 \leqslant b \leqslant B$, if $p$ sends a message with value $v \in\{0,1\}$ in round $(b, 2)$ and $q$ sends a message with value $w \in\{0,1\}$ in round $(b, 2)$, then $v=w$.

Proof. Suppose $p$ sends $v$ in round $(b, 2)$ and $q$ sends $w$ in round $(b, 2)$. Assume in contradiction $v \neq w$. Then $\operatorname{favor}(p)[b, 1]=v$ and favor $(q)[b, 1]=w$. Thus $p$ receives at least $n-t$ messages for $v$ in round $(b, 1)$, and $q$ receives at least $n-t$ messages for $w$ in round $(b, 1)$. Thus at least $n-2 t$ nonfaulty processors send messages for $v$ in round $(b, 1)$, and $q$ receives at least $n-2 t$ messages for $v$ in round $(b, 1)$. Then $q$ recevies at most $2 t$ messages for $w$ in round $(b, 1)$. Since $n>3 t$, it follows that $n-t>2 t$ and $q$ receives less than $n-t$ messages for $w$ in round $(b, 1)$, a contradiction.

Lemma 4. Consider any execution of $B A$ at least $B$ blocks long, in which at most $t$ processors in $P$ are faulty. Then there exists $a b, 1 \leqslant b \leqslant B$, and $a$ value $v$ in $\{0,1\}$ such that favor $(p)[b]=v$ for all nonfaulty $p$ in $P$.

Proof. By Lemma 2, there is some $b$ such that at most $t_{b}$ processors in $C_{b}$ are faulty. Fix the smallest such $b$. There are three cases.

Case 1. All nonfaulty $p$ in $P$ set $\operatorname{favor}(p)$ to be $b b-o u t(p)$ at the end of block $b$. By agreement of $\mathrm{BB}_{b}$, we are done.

Case 2. All nonfaulty $p$ in $P$ set favor $(p)$ to be $\operatorname{common}(p)$ at the end of round $(b, 2)$. Suppose that $\operatorname{common}(p)[b, 2]=v$ and common $(q)[b, 2]=w$, for nonfaulty $p$ and $q$ in $P$. In round $(b, 2)$ processor
$p$ receives at least $n-t$ messages for $v$, at least $n-2 t>t$ of which are from nonfaulty processors. Similarly, in round ( $b, 2$ ) processor $q$ receives more than $t$ mcssagcs for $w$ from nonfaulty processors. By Lemma $3, v=w$.

Case 3. Some nonfaulty $p$ in $P$ sets $\operatorname{favor}(p)$ to be $b b-\operatorname{out}(p)$ at the end of block $b$ and some nonfaulty $q$ in $P$ sets favor $(q)$ to be common $(q)$ at the end of round ( $b, 2$ ). Suppose favor $(p)[b]=b b-o u t(p)[b]=v$, and favor $(q)[b]=\operatorname{common}(q, 2)[b]=w$. We show $v=w$. In round $(b, 2), q$ receives at least $n-t$ messages for $w$, at least $n-2 t>t$ of which are from nonfaulty processors. By Lemma 3, no nonfaulty processor sends a message for $w^{\prime} \neq w$ in round ( $b, 2$ ). Thus every nonfaulty $r$ receives at least $n-2 t$ messages for $w$ in round ( $b, 2$ ), and at most $t$ messages for $w^{\prime} \neq w$, causing it to set common(r) to $w$ before calling $\mathrm{BB}_{b}$; thus, common $(r)[b, 2]=w$. So all nonfaulty processors $r$ in $C_{b}$, of which there is at least one, have common $(r)[b, 2]=w$. By validity of $\mathrm{BB}_{h}, b b$-out $(r)\lceil b\rceil=w$ for all nonfaulty $r$, including $p$.

Lemma 5. Consider any execution of BA at least B blocks long, in which at most $t$ processors in $P$ are faulty. Then for all $b, 1 \leqslant b \leqslant B$, if for some $v$ in $\{0,1\}$ and for all nonfaulty $p$ in $P$, favor $(p)[b-1]=v$, then for all nonfaulty $p$ in $P$, favor $(p)[b]=v$.

Proof. All nonfaulty $p$ in $P$ send messages for $v$ in round ( $b, 1$ ). All nonfaulty $p$ in $P$ receive at least $n \quad t$ messages for $v$ in round $(b, 1)$, and sct favor $(p)$ to $v$ at the end of round $(b, 1)$. Thus in round $(b, 2)$, all nonfaulty $p$ in $P$ send messages for $v$, and all nonfaulty $p$ in $P$ receive at least $n-t$ messages for $v$. Since $n>3 t$, it follows that $n-t>n / 2$, implying $v$ is the most common element of $\{0,1\}$ received. Thus for all nonfaulty $p$ in $P$, number $(p)[b, 2] \geqslant n-t$, and $p$ uses common $(p)$ to set $\operatorname{favor}(p)$ to $v$.

Proof of Theorem 2. Choose any execution of BA at least $B$ blocks long.

Termination. By the code and termination of $\mathrm{BB}_{b}$, every nonfaulty $p$ in $P$ has an output value for $b a-o u t(p)$ after $B$ blocks.

Agreement. Suppose at most $t$ processors in $P$ are faulty. By Lemma 4, there is a block $b$ and a $v$ in $\{0,1\}$ such that $\operatorname{favor}(p)[b]=v$ for all nonfaulty $p$ in $P$. By Lemma 5, favor $(p)\left[b^{\prime}\right]=v$ for all nonfaulty $p$ in $P$, and all $b^{\prime} \geqslant b$. Thus $\operatorname{favor}(p)[B]=v$ for all nonfaulty $p$ in $P$, and thus $b a$-out $(p)[B]=v$ for all nonfaulty $p$ in $P$.

Validity. Suppose at most $t$ processors in $P$ are faulty. Suppose for some $v$ in $\{0,1\}$ and for all nonfaulty $p$ in $P$, the input to $p$ is $v$. Then favor $(p)[0]=v$ for all nonfaulty $p$ in $P$, by the code. As in the argument for agreement, $b a-o u t(p)[B]=v$ for all nonfaulty $p$ in $P$.

Complexities. By the definition of the protocol, $|P|=n$. Each block $b$, $1 \leqslant b \leqslant B$, consists of two overhead rounds and $R_{b}$ Byzantine broadcast rounds. Each block $b, 1 \leqslant b \leqslant B$, contributes $n^{2}$ message bits per overhead round (each processor sends one bit to each processor) and $M_{b}$ message bits from the Byzantine broadcast subroutine. The largest message sent in an overhead round is one bit, and the largest message sent in the $b$ th Byzantine broadcast subroutine has size $S_{b}, 1 \leqslant b \leqslant B$.

### 4.3. The General Transformation

In this subsection we give our general transformation. It is the composition of Transformations 1 and 2.

Recall that $B$ is the number of committees in Transformation 2. Let $\mathrm{BA}^{\prime}$ be a Byzantine agreement protocol family with processor complexity $3 t+1$, round complexity $R(t)$, message bit complexity $M(t)$, and message size complexity $S(t)$. Given $B$ and $\mathrm{BA}^{\prime}$, we define another Byzantine agreement protocol family BA as follows. (This is Transformation 3.)

We define $\mathrm{BA}(P, t)$ for any $P$ and $t$ with $|P|=3 t+1$. Let $t_{b:}=$ $\lfloor(t+1-b) / B\rfloor, 1 \leqslant b \leqslant B$. Choose $B$ disjoint subsets of $P, C_{b}, 1 \leqslant b \leqslant B$, such that $\left|C_{b}\right|=3 t_{b}+1$. Apply Transformation 1 to $P$ and $\mathrm{BA}^{\prime}\left(C_{b}, t_{b}\right)$ to obtain Byzantine broadcast protocol $\mathrm{BB}_{b}$ for $P, C_{b}$, and $t_{b}$, for all $b$, $1 \leqslant b \leqslant B$. Apply Transformation 2 to $P, t$, and $\mathbf{B B}_{b}, 1 \leqslant b \leqslant B$, to obtain BA $(P, t)$. Thus Transformation 3 is simply a composition of Transformations 1 and 2.

Theorem 3. BA is a Byzantine agreement protocol family with complexities

- $3 t+1$ processors,
- $\sum_{b=1}^{B}\left(R\left(t_{b}\right)+3\right)$ rounds,
- $\sum_{b=1}^{B}\left(M\left(t_{b}\right)+3\left(t-t_{b}\right)\left(3 t_{b}+1\right)+2(3 t+1)^{2}\right)$ message bits, and
- $\max \left\{1, S\left(t_{1}\right), \ldots, S\left(t_{B}\right)\right\}$ message size.

Proof. Choose any $P$ and $t$ with $|P|=3 t+1$.
Correctness. By Theorem 1, each $\mathrm{BB}_{b}$ is a Byzantine broadcast protocol for $P, C_{b}$, and $t_{b}, 1 \leqslant b \leqslant B$. Then by Theorem $2, \mathrm{BA}(P, t)$ is a Byzantine agreement protocol for $P$ and $t$.

Complexities. By definition, $\mathrm{BA}(P, t)$ uses $3 t+1$ processors. The stated number of rounds is correct, since by Theorem 2 the number of rounds is $\sum_{b=1}^{B}\left(R_{b}+2\right)$, and by Theorem 1 each $R_{b}$ is $R\left(t_{b}\right)+1$. The stated number of message bits is correct, since by Theorem 2 the number of message bits is $\sum_{b-1}^{B}\left(M_{b}+2 n^{2}\right)$, and by Theorem 1 each $M_{b}$ is $M\left(t_{b}\right)+\left(|P|-\left|C_{b}\right|\right) \cdot\left|C_{b}\right|$, where $|P|=n=3 t+1$ and $\left|C_{b}\right|=3 t_{b}+1$ for all b. By Theorem 2 the maximum message size is $\max \left\{1, S_{1}, \ldots, S_{B}\right\}$, and by Theorem 1 each $S_{b}$ is $\max \left\{1, S\left(t_{b}\right)\right\}$, giving the stated bound.

## 5. The New Byzantine Agreement Protocol

In this section we recursively apply our general transformation to construct our efficient new Byzantine agreement protocol.

Begin by fixing integers $B \geqslant 2$ and $l \geqslant 0$. The integer $B$ is the number of committees for Transformation 3. The integer $l$ is a lower bound on the number of faults to be tolerated by each instance of the base protocol. Now we give an inductive definition of a class of protocols $\mathrm{BA}_{i}$. Each successive member of the class is obtained by an additional application of Transformation 3 (using $B$ as a parameter).

First we define the base protocol family, $\mathrm{BA}_{0}$. It is a slight variant of the protocol family presented by Lamport, Shostak, and Pease (1982). In the problem solved there, a single processor has an input bit on which the rest must agree; we consider the obvious modification of their protocol to the version of Byzantine agreement that we are studying - a copy of Lamport, Shostak, and Pease's protocol is run for each processor and its input, and processors decide on the majority value in the resulting vector. We call this modified Byzantine agreement protocol family LSP. (Since it terminates in $t+1$ rounds no matter how many faults there are, it satisfies our nonstandard termination condition.) Choose any $P$ and $t$ with $|P|=3 t+1$ and $t \geqslant 0$. Let $\mathrm{BA}_{0}(P, t)$ be $\operatorname{LSP}(P, t)$.
Assume protocol family $\mathrm{BA}_{i-1}$ has been defined, where $i-1 \geqslant 0$. We now define protocol family $\mathrm{BA}_{i}$. Choose any $P$ and $t$ with $|P|=3 t+1$. Let $\mathrm{BA}_{i}(P, t)$ be Transformation 3 applied to $B$ and $\mathrm{BA}_{i-1}$ if $t \geqslant(l+1) \cdot B^{i}-1$; otherwise, let $\mathrm{BA}_{i}(P, t)$ be $\mathrm{BA}_{i-1}(P, t)$. Thus, when $t$ is big enough for $i$ applications of Transformation 3 to be well defined and for each instance of the base protocol to tolerate at least $l$ faults, $\mathrm{BA}_{i}(P, t)$ is defined to be the $\mathrm{BA}_{0}$ protocol transformed $i$ times; otherwise, it is defined to be the $\mathrm{BA}_{0}$ protocol transformed as many times as is possible and still have the base protocols tolerate at least $l$ faults. Lemma 6 below and inspection of the transformations show that $\mathrm{BA}_{i-1}$ has the necessary processor complexity, namely $3 t+1$, for Transformation 3 to be applicable.

We are now ready to define the protocol family that is the main result of this paper. We define it as one of the $\mathrm{BA}_{i}$ protocols, ehosen based on $t$, the number of faults to be tolerated. First, fix $\varepsilon>0 ; \varepsilon$ is to be the exponent in the message size complexity. Then let $d=\max \{4,\lceil 1 / \varepsilon\rceil\}$ and let

$$
f(t)= \begin{cases}\left\lceil\log _{B} \frac{d \cdot t \cdot \log \log t}{\log t}\right\rceil & \text { if } t \geqslant 4 \\ 0 & \text { otherwise }\end{cases}
$$

(The default base for logarithms in this paper is 2 .) Now we define the

Byzantine agreement protocol family BA as follows. Given $P$ and $t$, with $|P|=3 t+1$, let $\mathrm{BA}(P, t)$ be $\mathrm{BA}_{i}(P, t)$, where $i=f(t)$. The motivation for this definition is that it yields committees at the lowest level of recursion that are of size $O(\log t / \log \log t)$. Given that an exponential message bit algorithm is used in these committees, this is the largest committee size that will still yield an overall message bit complexity of $O\left(t^{2}\right)$. We want committees as large as possible because increasing the size of committees decreases the number of rounds, but we do not want them to be too big because increasing the size of committees increases the size of messages. The role played by $d$ is to reduce the committee size by a factor of $d$, thereby saving a factor of $d$ in the exponent of the message size complexity. We now state our main result.

Theorem 4. BA is a Byzantine agreement protocol family with

- processor complexity $P(t)=3 t+1$,
- round complexity $R(t)=t+o(t)$,
- message bit complexity $M(t)=O\left(t^{2}\right)$, and
- message size complexity $S(t)=O\left(t^{\varepsilon}\right)$.

Five lemmas are used in the proof of Theorem 4. Lemma 6 gives the complexity of the base protocol $\mathrm{BA}_{0}$. Lemma 7 gives the round complexity of $\mathrm{BA}_{i}$, for any $i$. Lemma 8 gives an upper bound on the message size complexity for $\mathrm{BA}_{i}$, for any $i$. Lemma 9 gives an upper bound on the message bit complexity of $\mathrm{BA}_{i}$, for any $i$. Lemmas 7, 8, and 9 hold only when $t \geqslant(l+1) \cdot B^{i}-1$, i.e., when $t$ is large enough that the protocol consists of $i$ transformations of $\mathrm{BA}_{0}$. However, this will be sufficient for our asymptotic analysis. Lemma 10 is a technical result used to bound the portion of the message bit complexity of BA caused by execution of the LSP protocol at the base level of the recursive construction.

Let $\mathrm{BA}_{i}$ have processor complexity $P_{i}(t)$, round complexity $R_{i}(t)$, message bit complexity $M_{i}(t)$, and message size complexity $S_{i}(t)$, for all $i \geqslant 0$.

Lemma 6. $\mathrm{BA}_{0}$ is a Byzantine agreement protocol family, and

- $P_{0}(t)=3 t+1$,
- $R_{0}(t)=t+1$,
- $M_{0}(t) \leqslant(3 t+1)^{t+3}$, and
- $S_{0}(t) \leqslant(3 t)^{2}$ if $t \geqslant 1$, and $S_{0}(0)=1$.

Proof. By Lamport, Shostak, and Pcasc (1982).
$M_{0}$ and $S_{0}$ are only defined for nonnegative integers. It will be helpful in the analysis to define nondecreasing functions of nonnegative real
arguments that upper-bound them. Let $M_{*}(t)=(3 t+1)^{t+3}$ be defined for all real $t \geqslant 0$; let $S_{*}(t)=\max \left\{1,(3 t)^{t}\right\}$ be defined for all real $t>0$ and let $S_{*}(0)=1$. Note that $M_{*}$ is nondecreasing for all arguments and $M_{0}(t) \leqslant M_{*}(t)$ for all nonnegative integers $t$, and similarly for $S_{*}$.
The next lemma gives an exact expression for the round complexity of $\mathrm{BA}_{i}$. The expression is equal to the number of rounds in instances of $\mathrm{BA}_{0}$, which is $t+1$, plus the number of overhead rounds added by our transformation, which is the last term.

Lemma 7. $R_{i}(t)=t+1+3 \cdot \sum_{j=1}^{i} B^{j}$ for all $i \geqslant 0$, and all $t \geqslant$ $(l+1) \cdot B^{i}-1$.

Proof. We proceed by induction on $i$. The basis, $i=0$, is true by Lemma 6. Suppose the result is true for $i-1 \geqslant 0$. We show it for $i$. Pick any $t \geqslant(l+1) \cdot B^{i}-1$. By Theorem 3,

$$
R_{i}(t)=\sum_{b=1}^{B}\left(R_{i-1}\left(t_{b}\right)+3\right) .
$$

Note that for all $b$

$$
\begin{aligned}
t_{b} & =\lfloor(t+1-b) / B\rfloor & & \text { by the definition of } t_{b} \\
& \geqslant\left\lfloor\left((l+1) \cdot B^{i}-b\right) / B\right\rfloor & & \text { because } t \geqslant(l+1) \cdot B^{i}-1 \\
& \geqslant\left\lfloor\left((l+1) \cdot B^{i}-B\right) / B\right\rfloor & & \text { because } b \leqslant B \\
& =(l+1) \cdot B^{i-1}-1 . & &
\end{aligned}
$$

Since $t_{b} \geqslant(l+1) \cdot B^{i-1}-1$, we can apply the inductive hypothesis to the above expression for $R_{i}(t)$ to get

$$
\begin{aligned}
R_{i}(t) & =\sum_{b=1}^{B}\left(t_{b}+1+3 \cdot \sum_{j=1}^{i-1} B^{j}+3\right) \\
& =\sum_{b=1}^{B} t_{b}+B+3 B \cdot \sum_{j=1}^{i=1} B^{j}+3 B \\
& =t+1-B+B+3 B \cdot \sum_{j=1}^{i-1} B^{j}+3 B \quad \text { by Lemma } 1 \\
& =t+1+3 \cdot \sum_{j=1}^{i} B^{j} .
\end{aligned}
$$

The next lemma gives an upper bound on the message size complexity of $\mathrm{BA}_{i}$. The bound is the message size complexity for the instances of $\mathrm{BA}_{0}$, each of which tolerates approximately $t / B^{i}$ faults.

Lemma 8. $\quad S_{i}(t) \leqslant S_{*}\left(t / B^{i}\right)$ for all $i \geqslant 0$, and all $t \geqslant(l+1) \cdot B^{i}-1$.
Proof. We proceed by induction on $i$. The basis, $i=0$, follows from Lemma 6 and the definition of $S_{*}$. Suppose the lemma is true for $i-1 \geqslant 0$. Choose $t \geqslant(l+1) \cdot B^{i}-1$. As shown in the proof of Lemma 7, $t_{b} \geqslant$ $(l+1) \cdot B^{i-1}-1$ for all $b$, allowing use of the inductive hypothesis.

By Theorem 3,

$$
S_{i}(t)=\max \left\{1, S_{i-1}\left(t_{1}\right), \ldots, S_{i-1}\left(t_{B}\right)\right\}
$$

By induction, since each $t_{b} \geqslant(l+1) \cdot B^{i-1}-1$,

$$
\leqslant \max \left\{1, S_{*}\left(t_{1} / B^{i-1}\right), \ldots, S_{*}\left(t_{B} / B^{i-1}\right)\right\}
$$

Since $S_{*}$ is nondecreasing and each $t_{b} \leqslant t / B$,

$$
\begin{aligned}
& \leqslant \max \left\{1, S_{*}\left(t / B^{i}\right)\right\} \\
& =S_{*}\left(t / B^{i}\right)
\end{aligned}
$$

The next lemma gives an upper bound on the message bit complexity of $\mathrm{BA}_{i}$. The expression is the sum of two terms. The first term is the message bit complexity of the $\mathrm{BA}_{0}$ instances: there are $B^{i}$ instances and each is of a size to tolerate approximately $t / B^{i}$ faults. The second term is the cost of the overhead rounds. There are $B$ sets of overhead rounds involving all the processors, i.e., $O\left(B t^{2}\right)$ bits. There are $B^{2}$ sets involving a $1 / B$ fraction of the processors, i.e., $O\left(t^{2}\right)$ bits, and so on, up to $B^{i}$ sets involving a $1 / B^{i-1}$ fraction of the processors, i.e., $O\left(t^{2} / B^{i-2}\right)$ bits. The sum of all these expressions is $O\left(B \cdot t^{2}\right)$, which is $O\left(t^{2}\right)$ since $B$ is fixed.

There exists a constant $c$ such that $3\left(t-t_{b}\right)\left(3 t_{b}+1\right)+2(3 t+1)^{2} \leqslant c t^{2}$ for all $b$. In fact, letting $c=44$ is sufficient. Use of this constant shortens the statement of the bound on message bit complexity given in Theorem 3 and thus simplifies the statement of the next lemma.

Lemma 9. $\quad M_{i}(t) \leqslant B^{i} \cdot M_{*}\left(t / B^{i}\right)+c \cdot t^{2} \cdot \sum_{k=1}^{i} 1 / B^{k-2}$ for all $i \geqslant 0$, and all $t \geqslant(l+1) \cdot B^{i}-1$.

Proof. We proceed by induction on $i$. The basis, $i=0$, follows from Lemma 6 and the definition of $M_{*}$. Suppose the lemma is true for $i-1 \geqslant 0$. Pick any $t \geqslant(l+1) \cdot B^{i}-1$. As shown in the proof of Lemma $7, t_{b} \leqslant$ $(l+1) \cdot B^{i-1}-1$ for all $b$, allowing use of the inductive hypothesis.

By Theorem 3 and the definition of $c$,

$$
M_{i}(t) \leqslant \sum_{b=1}^{B}\left(M_{i-1}\left(t_{b}\right)+c \cdot t^{2}\right)
$$

By induction since each $t_{b} \geqslant(l+1) \cdot B^{i-1}-1$,

$$
\leqslant \sum_{b-1}^{B}\left(B^{i-1} \cdot M_{*}\left(t_{b} / B^{i-1}\right)+c \cdot t_{b}^{2} \cdot \sum_{k=1}^{i-1} \frac{1}{B^{k-2}}+c \cdot t^{2}\right)
$$

Since $M_{*}$ is nondecreasing and each $t_{b} \leqslant t / B$,

$$
\begin{aligned}
& \leqslant \sum_{b=1}^{B}\left(B^{i-1} \cdot M_{*}\left(t / B^{i}\right)+c \cdot(t / B)^{2} \cdot \sum_{k=1}^{i-1} \frac{1}{B^{k-2}}+c \cdot t^{2}\right) \\
& =B^{i} \cdot M_{*}\left(t / B^{i}\right)+c \cdot t^{2} \cdot \sum_{k=1}^{i} \frac{1}{B^{k-2}} .
\end{aligned}
$$

We need one more lemma before we can prove the main result.
Lemma 10. If $t \geqslant 4$ then $((\log t) / \log \log t)^{(\log t) / \log \log t} \leqslant t$.
Proof. Choose $t \geqslant 4$. Using the identity that $x^{x}=2^{x \cdot \log x}$, with $x=$ $(\log t) / \log \log t$, we get

$$
\begin{aligned}
((\log t) / \log \log t)^{(\log t) / \log \log t} & =2^{((\log t) / \log \log t) \cdot \log ((\log t) / \log \log t)} \\
& \leqslant 2^{((\log t) / \log \log t) \cdot \log \log t} \\
& =2^{\log t} \\
& =t
\end{aligned}
$$

Proof of Theorem 4. For any $P$ and $t, \mathrm{BA}(P, t)$ is defined to be $\mathrm{BA}_{i}(P, t)$, where $i=f(t)$. Correctness of BA follows from the correctness of the constituent transformations.

We now calculate the four complexity measures for BA. Recall that $B, l$, and $d$ are fixed constants.

The processor complexity $P(t)$ is $3 t+1$. If $f(t)=0$ this follows from Lemma 6; otherwise, it follows from Theorem 3.

The round, message bit, and message size complexities are calculated for all $t \geqslant t_{0}$, where $t_{0}$ is a constant such that $t_{0} \geqslant \max \left\{4,(l+1) \cdot B^{f\left(t_{0}\right)}\right\}$. (Such a constant exists since $t_{0} \geqslant \max \left\{4,(l+1) \cdot B^{f\left(t_{0}\right)}\right\}$ implies $\log t_{0} \geqslant$ $(l+1) \cdot d \cdot \log \log t_{0}$ and $\log$ grows faster than $\log \log$.) Obviously for all $t \geqslant t_{0}, t \geqslant \max \left\{4,(l+1) \cdot B^{f(t)}\right\}$.

For the rest of this proof we assume $t \geqslant t_{0}$. Thus Lemmas 7, 8, and 9 hold. For clarity of presentation, let $i=f(t)$. We have the following facts.

- Fact 1. $\quad i=\left\lceil\log _{B}(d \cdot t \cdot(\log \log t) / \log t)\right\rceil$, since $t \geqslant 4$.
- Fact 2. $\log _{B}(d \cdot t \cdot(\log \log t) / \log t) \leqslant i<\log _{B}(d \cdot t \cdot(\log \log t) / \log t)$ +1 , by Fact 1 and the definition of ceiling.
- Fact 3. $d \cdot t \cdot(\log \log t) / \log t \leqslant B^{i}<B \cdot d \cdot t \cdot(\log \log t) / \log t$, by Fact 2.
- Fact 4. $1 \leqslant t / B^{i} \leqslant \log t /(d \cdot \log \log t)$, since $t \geqslant(l+1) \cdot B^{i}$ and by Fact 3.

The round complexity is calculated as

$$
\begin{aligned}
R(t) & =R_{i}(t) & & \\
& =t+1+3 \cdot \sum_{j=1}^{i} B^{j} & & \text { by Lemma } 7 \\
& <t+1+3 B^{i+1} & & \\
& <t+1+3 B \cdot B \cdot d \cdot t \cdot(\log \log t) / \log t & & \text { by Fact } 3 \\
& =t+o(t) & & \text { since } B \text { and } d \text { are fixed. }
\end{aligned}
$$

The message bit complexity is calculated as

$$
\begin{aligned}
M(t) & =M_{i}(t) \\
& \leqslant B^{i} \cdot M_{*}\left(t / B^{i}\right)+c \cdot t^{2} \cdot \sum_{k=1}^{i} \frac{1}{B^{k-2}} \quad \text { by Lemma } 9 .
\end{aligned}
$$

We now show that each of the two terms is $O\left(t^{2}\right)$. The second term obviously is. The first term is $B^{i} \cdot M_{*}\left(t / B^{i}\right)$. By Fact $3, B^{i}<B \cdot d \cdot t$. $(\log \log t) / \log t$. By the definition of $M_{*}$,

$$
\begin{aligned}
M_{*}\left(t / B^{i}\right) & =\left(3 t / B^{i}+1\right)^{t / B^{i}+3} & & \\
& \leqslant\left(4 t / B^{i}\right)^{4 t / B^{i}} & & \text { since } \left.t / B^{i} \geqslant 1 \text { (Fact } 4\right) \\
& \leqslant\left(\frac{4 \cdot \log t}{d \cdot \log \log t}\right)^{4 \cdot \log t /(d \cdot \log \log t)} & & \text { by Fact } 4 \\
& \leqslant\left(\frac{\log t}{\log \log t}\right)^{\log t / \log \log t} & & \text { since } d \geqslant 4 . \\
& \leqslant t & & \text { by Lemma } 10 .
\end{aligned}
$$

Thus the first term is $O\left(t^{2} \log \log t / \log t\right)=o\left(t^{2}\right)$.

The message size complexity is calculated as

$$
\begin{array}{rlrl}
S(t) & =S_{i}(t) & & \\
& \leqslant S_{*}\left(t / B^{i}\right) & & \text { by Lemma } 8 \\
& =\left(3 t / B^{i}\right)^{t / B^{i}} & & \text { since } t / B^{i} \geqslant 1 \text { (Fact 4) } \\
& \leqslant\left(\frac{3 \cdot \log t}{d \cdot \log \log t}\right)^{\log t /(d \cdot \log \log t)} & & \text { by Fact } 4 \\
& \leqslant\left(\frac{\log t}{\log \log t}\right)^{(\log t / \log \log t) \cdot(1 / d)} & & \\
& \leqslant \sqrt[d]{t} & & \text { since } d \geqslant 4 \\
& \leqslant t^{\varepsilon} & & \text { by Lemma } 10 \\
& & \text { because } d \geqslant 1 / \varepsilon
\end{array}
$$

This is $O\left(t^{\varepsilon}\right)$ as desired.

## 6. Bounding the Round Complexity

In Theorem 4 of Section 5 we gave a bound of $t+o(t)$ on the asymptotic round complexity of our new protocol. In this section we show that the appropriate choice of the parameters $B$ and $l$ to Transformation 3 yields a protocol with a round complexity that is better than $2 \cdot(t+1)$ for all $t \geqslant 0$. In other words, the $o(t)$ term is not hiding a large constant. This result follows from Theorem 5 below, when $B=4$ and $l=3$.

Theorem 5. For all $i \geqslant 0, t \geqslant 0, l \geqslant 0$, and $B \geqslant 2$,

$$
R_{i}(t)<\left(\frac{3 B}{(l+1)(B-1)}+1\right) \cdot(t+1)
$$

Proof. Fix $B, l, i, P$, and $t$. Let $k=\max \{0\} \cup\{j: 1 \leqslant j \leqslant i$ and $t \geqslant$ $\left.(l+1) \cdot B^{j}-1\right\}$. By inspection of the recursive definition of $\mathrm{BA}_{i}(P, t)$ in Section 5 we see that $\mathrm{BA}_{i}(P, t)=\mathrm{BA}_{k}(P, t)$. Thus $R_{i}(t)=R_{k}(t)$ and it is sufficient to bound $R_{k}(t)$. There are two cases.

Case 1. Suppose $k=0$. In this case $R_{k}(t)=t+1$ by Lemma 6. The claimed bound follows immediatcly since $l \geqslant 0$ and $B \geqslant 2$.

Case 2. Suppose $k \geqslant 1$. From the definition of $k$ it follows that $t \geqslant(l+1) \cdot B^{k}-1$. Thus Lemma 7 implies that

$$
R_{k}(t)=t+1+3 \cdot \sum_{j=1}^{k} B^{j}
$$

Because $B \geqslant 2$ the sum of the geometric series is strictly less than $B^{k+1} /(B-1)$. After substituting and rearranging we have that

$$
R_{k}(t)<t+1+B^{k} \cdot \frac{3 B}{B-1} .
$$

Since $t \geqslant(l+1) \cdot B^{k}-1$ if follows that $(t+1) /(l+1) \geqslant B^{k}$. Substituting this bound for $B^{k}$ in the above expression for $R_{k}(t)$ and rearranging produces the desired result.

## 7. Other Applications of the Transformation

In this section we use our general transformation to develop a few additional (small) results. Specifically, we consider the effect of recursively applying our transformation to other base protocol families besides LSP.

Suppose we choose a Byzantine agreement protocol family for $\mathrm{B} \Lambda_{0}$ such that $P_{0}(t)=a t+1$ for some $a \geqslant 3, R_{0}(t)=t+1, M_{0}(t) \leqslant M_{*}(t)$ for some nondecreasing function $M_{*}$ with nonnegative real arguments, and $S_{0}(t) \leqslant S_{*}(t)$ for somc nondecreasing function $S_{*}$ with nonnegative real arguments.

Then our analysis can be trivially modified to show that for all $i \geqslant 0$, and for all sufficiently large $t, P_{i}(t)=a t+1, R_{i}(t) \leqslant t+1+3 B^{i+1}, M_{i}(t) \leqslant$ $B^{i} \cdot M_{*}\left(t / B^{i}\right)+O\left(t^{2}\right)$, and $S_{i}(t) \leqslant S_{*}\left(t / B^{i}\right)$.

Once one is given a particular choice of the base protocol family, it remains to choose $f(t)$, the function that gives the depth of the recursion when tolerating $t$ faults. The complexities of the resulting protocol family, call them $P, R, M$, and $S$, can be obtained by substituting $f(t)$ for $i$ in the above formulas.

Observation 1. If we let $\left.f(t)=\max \left\{0, \log _{B}(t /(3 B))\right\rfloor\right\}$, then, regardless of the base protocol, we get $P(t)=a t+1, R(t) \leqslant 2 t+1, M(t)=O\left(t^{2}\right)$, and $S(t)=O(1)$. This choice of $f(t)$ causes the number of faults to be tolerated in each base protocol instance, which is $t / B^{f(t)}$, to be of constant size, $3 B$. Since each base protocol instance is of constant size, its contribution to the complexities is constant. Thus if we use a base protocol family with $a=3$, the resulting protocol family has simultaneously optimal processors, message bits, and message size, at the cost of doubling the number of rounds.

Observation 2. Now suppose that $\mathrm{BA}_{0}$ is a protocol such as that of Moses and Waarts (1992) or Berman, Garay, and Perry (1989a), so that $a \geqslant 4, M_{*}(t)=t^{x}$ for some constant $x$, and $S_{*}(t)=t^{y}$ for some constant $y<x$. Then if we let $f(t)=\left\lfloor\log _{B}\left(t^{(x-2) /(x-1)}\right)\right\rfloor$, we get $P(t)=a t+1, R(t) \leqslant$
$t+O\left(t^{(x-2) /(x-1)}\right), M(t)=O\left(t^{2}\right)$, and $S(t) \leqslant t^{y /(x-1)}$. Compared to using LSP as the base protocol, we have somewhat better round complexity (although still $t+o(t)$ ) and worse proccssor complexity and mcssage size complexity. The message bit complexity remains optimal.

Observation 3. Now suppose that $\mathrm{BA}_{0}$ is the protocol of Berman and Garay (1990) with $a=3, R_{0}=t+1, M_{*}=O\left(t^{3} \cdot 1.5^{t}\right)$, and $S_{*}=O\left(1.5^{t}\right)$. We let $f(t)=0$ if $t \leqslant 1$, otherwise we let $f(t)=\left\lceil\log _{B}(d \cdot t / \log t)\right\rceil$, where $d$ is defined as in Section 5 to be $\max \{4,\lceil 1 / \varepsilon\rceil\}$. The result is that $P(t)=3 t+1$, $R(t)=t+O(t / \log t), M(t)=O\left(t^{2}\right)$, and $S(t)=O\left(t^{\varepsilon}\right)$ for any $\varepsilon$. Compared to using LSP as the base, we obtain improved round complexity (the $o(t)$ term is $O(t / \log t)$ instead of $O(t \cdot \log \log t / \log t)$ ), while the other complexities are unchanged.

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[^0]:    * This paper combines results that were presented in preliminary form at the 8th ACM Symposium on Principles of Distributed Computing (Coan and Welch, 1989a) and the 27th Allerton Conference on Communication, Control, and Computing (Coan and Welch, 1989b).

