Supervision Localization of Timed Discrete-Event Systems

Renyuan Zhang^a, Kai Cai^b, Yongmei Gan^a, Zhaoan Wang^a, W.M. Wonham^b

^aSchool of Electrical Engineering, Xi'an Jiaotong University, Xi'an, Shaanxi 710049, China

^bDepartment of Electrical and Computer Engineering, University of Toronto, Toronto, ON M5S 3G4, Canada

Abstract

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We study supervisor localization for real-time discrete-event systems (DES) in the Brandin-Wonham framework of timed supervisory control. We view a real-time DES as comprised of asynchronous agents which are coupled through imposed logical and temporal specifications; the essence of supervisor localization is the decomposition of monolithic (global) control action into local control strategies for these individual agents. This study extends our previous work on supervisor localization for untimed DES, in that monolithic timed control action typically includes not only disabling action as in the untimed case, but also "clock preempting" action which enforces prescribed temporal behavior. The latter action is executed by a class of special events, called "forcible" events; accordingly, we localize monolithic preemptive action with respect to these events. We demonstrate the new features of timed supervisor localization with a manufacturing cell case study, and discuss a distributed control implementation.

Key words: Supervisory control; Supervisor localization; Timed discrete-event systems.

1 Introduction

Recently we developed a top-down approach, called *su*pervisor localization [3, 4] to the distributed control of untimed discrete-event systems (DES) in the Ramadge-Wonham (RW) supervisory control framework [10, 14]. We view the plant to be controlled as comprised of independent asynchronous agents which are coupled implicitly through logical control specifications. To make the agents smart and semi-autonomous, our localization algorithm allocates *external* supervisory control action to individual agents as their *internal* control strategies, while preserving the optimality (maximal permissiveness) and nonblocking properties of the overall monolithic (global) controlled behavior. Under the localization scheme, each agent controls only its own events, although it may very well need to observe events originating in other (typically neighboring) agents.

In this paper we extend the supervisor localization theory to a class of *real-time* DES, and address distributed control problems therein. Many time-critical applications can be modeled as real-time DES, such as communication channels, sensor networks, scheduling and resource management [8]. Typical timing features include communication delays and operational hard deadlines. The correctness and optimality of real-time DES depend not only on the system's logical behavior, but also on the times at which various actions are executed. Moreover, rapid advances in embedded, mobile computation and communication technologies [8, Part III] have enabled distributed implementation of control algorithms. These developments jointly motivate this study of supervisor localization for real-time DES.

A variety of real-time DES models and approaches are available. Notable works include Brave and Heymann's "clock automata" [2], Ostroff's "timed transition models" [9], Brandin and Wonham's timed DES (TDES) [1], Cassandras's "timed state automata" [5], Wong-Toi and Hoffman's model based on "timed automata" [12], and Cofer and Garg's model based on "timed Petri nets" [6]. We adopt Brandin and Wonham's TDES (or BW model) as the framework for developing a timed supervisor localization theory for two reasons. First, the BW model is a direct extension from the RW framework (where our untimed localization theory is based), retaining the central concepts of controllability, and maximally permis-

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Email addresses: r.yuan.zhang@gmail.com (Renyuan Zhang), kai.cai@scg.utoronto.ca (Kai Cai), ymgan@mail.xjtu.edu.cn (Yongmei Gan), zawang@mail.xjtu.edu.cn (Zhaoan Wang), wonham@control.utoronto.ca (W.M. Wonham).

sive nonblocking supervision. This feature facilitates developing a timed counterpart of supervisor localization. Second, the BW model captures a variety of timing issues in a useful range of real-time discrete-event control problems [1], [14, Chapter 9]. While it may be possible to develop supervisor localization in an alternative framework, as a preliminary step into real-time supervisor localization we choose the BW model for its close relation with previous work.

The principal contribution of this paper is the development of a timed supervisor localization theory in the BW TDES framework, which extends the untimed counterpart in [3, 4]. In this timed localization, a novel feature is "event forcing" as means of control, in addition to the usual "event disabling". Specifically, "forcible" events are present in the BW model as events that can be relied on, when subject to some temporal specification, to "preempt the tick of the clock", as explained further in Section 2. Correspondingly, in localizing the monolithic supervisor's control action, we localize not only its disabling action as in the untimed case, but also its preemptive action with respect to individual forcible events. Central to the latter are several new ideas: "local preemptor", "preemption consistency relation", and "preemption cover". We will prove that localized disabling and preemptive behaviors collectively achieve the same global optimal and nonblocking controlled behavior as the monolithic supervisor does. The proof relies on the new preemption concepts and also controllability for TDES. Moreover, the derived local controllers typically have much smaller state size than the monolithic supervisor, and hence their disabling and preemptive logics are often more transparent. We demonstrate this empirical result by a case study of a manufacturing cell taken from [1].

The paper is organized as follows. Section 2 provides a review of the BW TDES framework. Section 3 formulates the timed supervisor localization problem, and Section 4 presents a constructive solution procedure. Section 5 studies a manufacturing cell example; and finally, Section 6 draws conclusions.

2 Preliminaries on Timed Discrete-Event Systems

This section reviews the TDES model proposed by Brandin and Wonham [1], [14, Chapter 9]. First consider the untimed DES model

$$\mathbf{G}_{act} = (A, \Sigma_{act}, \delta_{act}, a_0, A_m). \tag{1}$$

Here A is the finite set of activities, Σ_{act} is the finite set of events, $\delta_{act} : A \times \Sigma_{act} \to A$ is the (partial) activity transition function, $a_0 \in A$ is the initial activity, and $A_m \subseteq A$ is the set of marker activities. Let \mathbb{N} denote the set of natural numbers $\{0, 1, 2, ...\}$. We introduce time into \mathbf{G}_{act}

by assigning to each event $\sigma \in \Sigma_{act}$ a lower time bound $l_{\sigma} \in \mathbb{N}$ and an upper time bound $u_{\sigma} \in \mathbb{N} \cup \{\infty\}$, such that $l_{\sigma} \leq u_{\sigma}$; typically, l_{σ} represents a delay in communication or in control enforcement, while u_{σ} is often a hard deadline imposed by legal specification or physical necessity. With these assigned time bounds, the event set Σ_{act} is partitioned into two subsets: $\Sigma_{act} = \Sigma_{spe} \cup \Sigma_{rem}$ (\bigcup denotes disjoint union) with $\Sigma_{spe} := \{\sigma \in \Sigma_{act} | u_{\sigma} \in \mathbb{N}\}$ and $\Sigma_{rem} := \{\sigma \in \Sigma_{act} | u_{\sigma} = \infty\}$; here "spe" denotes "prospective", i.e. σ will occur within some prospective time (with a finite upper bound), while "rem" denotes "remote", i.e. σ will occur at some indefinite time (with no upper bound), or possibly will never occur at all.

A distinguished event, written tick, is introduced which represents "tick of the global clock". Attach to each event $\sigma \in \Sigma_{act}$ a (countdown) timer $t_{\sigma} \in \mathbb{N}$, whose default value $t_{\sigma 0}$ is set to be

$$t_{\sigma 0} := \begin{cases} u_{\sigma} \text{ if } \sigma \in \Sigma_{spe}, \\ l_{\sigma} \text{ if } \sigma \in \Sigma_{rem}. \end{cases}$$
(2)

When timer $t_{\sigma} > 0$, it decreases by 1 (counting down) if event *tick* occurs; and when $t_{\sigma} = 0$, event σ must occur (resp. may occur) if $\sigma \in \Sigma_{spe}$ (resp. if $\sigma \in \Sigma_{rem}$). Note that while *tick* is a global event, each timer t_{σ} is local (with respect to the event σ). Also define the *timer interval* T_{σ} by

$$T_{\sigma} := \begin{cases} [0, u_{\sigma}] \text{ if } \sigma \in \Sigma_{spe}, \\ [0, l_{\sigma}] \text{ if } \sigma \in \Sigma_{rem}. \end{cases}$$
(3)

Thus $t_{\sigma} \in T_{\sigma}$.

Based on (1)-(3), the TDES model **G** is given by

$$\mathbf{G} := (Q, \Sigma, \delta, q_0, Q_m), \tag{4}$$

where $Q := A \times \prod \{T_{\sigma} | \sigma \in \Sigma_{act}\}$ (\prod denotes *Cartesian* product) is the finite set of states ¹, a state $q \in Q$ being of the form $q = (a, \{t_{\sigma} | \sigma \in \Sigma_{act}\})$ (i.e. a $(1 + |\Sigma_{act}|)$ tuple); $\Sigma := \Sigma_{act} \cup \{tick\}$ is the finite set of events; $\delta :$ $Q \times \Sigma \to Q$ is the (partial) state transition function; $q_0 =$ $(a_0, \{t_{\sigma 0} | \sigma \in \Sigma_{act}\})$ ($t_{\sigma 0}$ as in (2)) is the initial state; and $Q_m \subseteq A_m \times \prod \{T_{\sigma} | \sigma \in \Sigma_{act}\}$ is the set of marker states. Starting from q_0 , TDES **G** executes state transitions in accordance with its transition function δ . Let q = $(a, \{t_{\alpha} | \alpha \in \Sigma_{act}\}) \in Q$ and $\sigma \in \Sigma_{act}$; δ is defined at (q, σ) , written $\delta(q, \sigma)!$, if δ_{act} of **G**_{act} is defined at (a, σ) (i.e. $\delta_{act}(a, \sigma)!$) and timer t_{σ} satisfies (i) $0 \le t_{\sigma} \le u_{\sigma} - l_{\sigma}$ when $\sigma \in \Sigma_{spe}$, and (ii) $t_{\sigma} = 0$ when $\sigma \in \Sigma_{rem}$. The new state $q' = \delta(q, \sigma)$ is given by $q' = (\delta_{act}(a, \sigma), \{t'_{\alpha} | \alpha \in \Sigma_{act}\}) = 0$

¹ An upper bound for the state size |Q| is $|A| * \prod_{\sigma \in \Sigma_{act}} t_{\sigma 0}$ (here \prod denotes *scalar multiplication*), which in practice can be much larger than its untimed counterpart |A|.

 Σ_{act} }), where t'_{σ} is set to be its default value $t_{\sigma 0}$ as in (2); for other timers t_{α} , $\alpha \neq \sigma$, the reader is referred to detailed updating rules given in [1,14]. On the other hand, $\delta(q, tick)$! if no timer of a prospective event is zero, and $q' = \delta(q, tick) = (a, \{t'_{\alpha} | \alpha \in \Sigma_{act}\})$, i.e. there is no change in the activity component a of q, while the rules for updating timers are again referred to [1, 14].

Let Σ^* be the set of all finite strings of elements in $\Sigma = \Sigma_{act} \dot{\cup} \{tick\}$, including the empty string ϵ . For $\Sigma' \subseteq \Sigma$, the *natural projection* $P : \Sigma^* \to \Sigma'^*$ is defined according to

$$P(\epsilon) = \epsilon, \quad \epsilon \text{ is the empty string;}$$

$$P(\sigma) = \begin{cases} \epsilon, \text{ if } \sigma \notin \Sigma', \\ \sigma, \text{ if } \sigma \in \Sigma'; \end{cases}$$

$$P(s\sigma) = P(s)P(\sigma), \quad s \in \Sigma^*, \sigma \in \Sigma.$$
(5)

In the usual way, P is extended to $P : Pwr(\Sigma^*) \rightarrow Pwr(\Sigma'^*)$, where $Pwr(\cdot)$ denotes powerset. Write $P^{-1} : Pwr(\Sigma'^*) \rightarrow Pwr(\Sigma^*)$ for the *inverse-image function* of P.

We introduce the languages generated by TDES **G** in (4). The transition function δ is extended to $\delta : Q \times \Sigma^* \to Q$ in the usual way. The *closed behavior* of **G** is the language

$$L(\mathbf{G}) := \{ s \in \Sigma^* | \delta(q_0, s)! \}$$
(6)

and the marked behavior is

$$L_m(\mathbf{G}) := \{ s \in L(\mathbf{G}) | \delta(q_0, s) \in Q_m \} \subseteq L(\mathbf{G}).$$
(7)

We say that **G** is nonblocking if the prefix closure ([14]) $\overline{L}_m(\mathbf{G}) = L(\mathbf{G}).$

To use TDES \mathbf{G} in (4) for supervisory control, it is necessary to specify certain transitions that can be controlled by an external supervisor. First, as in the untimed theory [14], we need a subset of events that may be *disabled*. Since disabling an event usually requires preventing that event indefinitely from occurring, only remote events belong to this category. Thus let a new subset $\Sigma_{hib} \subseteq \Sigma_{rem}$ denote the *prohibitible* events; the supervisor is allowed to disable any prohibitible event. Next, and specific to TDES, we bring in another category of events which can preempt event tick. Note that tick may not be disabled, inasmuch as no control technology can stop the global clock indefinitely. On this basis let a new subset $\Sigma_{for} \subseteq \Sigma_{act}$ denote the *forcible* events; a forcible event is one that preempts event *tick*: if, at a state q of \mathbf{G} , *tick* is defined and so are one or more forcible events, then tick can be effectively erased from the current list of defined events (contrast with indefinite erasure) 2 . There is no particular relation postulated a priori between Σ_{for} and any of Σ_{hib} , Σ_{rem} or Σ_{spe} ; in particular, a remote event may be both forcible and prohibitible. It is now convenient to define the *controllable* event set

$$\Sigma_c := \Sigma_{hib} \dot{\cup} \{tick\}.$$
 (8)

Here designating both Σ_{hib} and *tick* controllable is to simplify terminology. We emphasize that events in Σ_{hib} can be disabled indefinitely, while *tick* may be preempted only by events in Σ_{for} . The *uncontrollable* event set Σ_u is

$$\Sigma_u := \Sigma - \Sigma_c = \Sigma_{spe} \dot{\cup} (\Sigma_{rem} - \Sigma_{hib}). \tag{9}$$

We introduce the notion of controllability as follows. For a string $s \in L(\mathbf{G})$, define

$$Elig_{\mathbf{G}}(s) := \{ \sigma \in \Sigma | s\sigma \in L(\mathbf{G}) \}$$
(10)

to be the subset of events 'eligible' to occur (i.e. defined) at the state $q = \delta(q_0, s)$. Consider an arbitrary language $F \subseteq L(\mathbf{G})$ and a string $s \in \overline{F}$; similarly define the eligible event subset

$$Elig_F(s) := \{ \sigma \in \Sigma | s\sigma \in \overline{F} \}, \tag{11}$$

We say F is *controllable* with respect to **G** in (4) if, for all $s \in \overline{F}$,

$$Elig_{\mathbf{F}}(s) \supseteq \begin{cases} Elig_{\mathbf{G}}(s) \cap (\Sigma_{u} \dot{\cup} \{tick\}) \\ \text{if } Elig_{F}(s) \cap \Sigma_{for} = \emptyset, \\ Elig_{\mathbf{G}}(s) \cap \Sigma_{u} \\ \text{if } Elig_{F}(s) \cap \Sigma_{for} \neq \emptyset. \end{cases}$$
(12)

Thus F controllable means that an event σ is eligible to occur in F if (i) σ is currently eligible in \mathbf{G} , and (ii) either σ is uncontrollable or $\sigma = tick$ when there is no forcible event currently eligible in F. Recall that in the untimed supervisory control theory [10,14], F controllable means that the occurrence of an uncontrollable event in \mathbf{G} will not cause a string $s \in \overline{F}$ to exit from \overline{F} ; the difference in TDES is that, the special event *tick* (formally controllable) can be preempted only by a forcible event when the forcible event is eligible to occur.

Whether or not F is controllable, we denote by $\mathcal{C}(F)$ the set of all controllable sublanguages of F. Then $\mathcal{C}(F)$ is nonempty, closed under arbitrary set unions, and thus contains a unique supremal element denoted by $sup\mathcal{C}(F)$ [1, 14]. Now consider a specification language $E \subseteq \Sigma^*$ imposed on the timed behavior of \mathbf{G} ; E may represent

² One may also think of forcible events as being able to occur so fast that they can occur between *ticks*. For a more general use of forcible events, see [7].

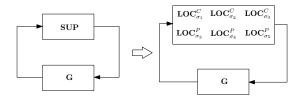


Fig. 1. Supervisor localization example for illustration: let $\Sigma_{hib} = \{\sigma_1, \sigma_2, \sigma_3\}, \Sigma_{for} = \{\sigma_3, \sigma_4, \sigma_5\}$; note $\sigma_3 \in \Sigma_{hib} \cap \Sigma_{for}$. Localization of **SUP**'s control action includes two parts: (i) localizing its disabling action into three *local controllers* $\mathbf{LOC}_{\sigma_i}^{P}$, i = 1, 2, 3, and (ii) localizing its preemptive action into three *local preemptors* $\mathbf{LOC}_{\sigma_j}^{P}$, j = 3, 4, 5.

a logical and/or temporal requirement. Let³

$$\mathbf{SUP} = (X, \Sigma, \xi, x_0, X_m) \tag{13}$$

be the corresponding monolithic supervisor that is optimal (i.e., maximally permissive) and nonblocking in the following sense: **SUP**'s marked language $L_m($ **SUP**) is

$$L_m(\mathbf{SUP}) = sup\mathcal{C}(E \cap L_m(\mathbf{G})) \subseteq L_m(\mathbf{G})$$
(14)

and moreover its closed language $L(\mathbf{SUP})$ is $L(\mathbf{SUP}) = \overline{L}_m(\mathbf{SUP})$. We note that in order to achieve optimal and nonblocking supervision, \mathbf{SUP} should correctly disable prohibitible events as well as preempt *tick* via forcible events.

3 Formulation of Localization Problem

Let TDES **G** in (4) be the plant to be controlled, and E be a specification language. Synthesize as in (14) the monolithic optimal and nonblocking supervisor SUP; throughout the paper we assume that $\overline{L}_m(\mathbf{SUP}) \neq \emptyset$. Supervisor **SUP**'s control action includes (i) disabling prohibitible events in Σ_{hib} and (ii) preempting *tick* via forcible events in Σ_{for} . This section formulates the localization of SUP's control action with respect to each prohibitible event as well as to each forcible event; an illustration of localization is provided in Fig. 1. Compared to [3], the present supervisor localization is an extension from untimed DES to TDES. As will be seen below, the treatment of prohibitible events is the timed counterpart of the treatment of controllable events in [3]; on the other hand, localization of forcible events' preemptive action is specific to TDES, and we introduce below the new concept "local preemptor". Further, we will discuss applying supervisor localization to the distributed control of multi-agent TDES.

First, let $\alpha \in \Sigma_{for}$ be an arbitrary forcible event. We say that $\mathbf{LOC}_{\alpha}^{P} = (Y_{\alpha}, \Sigma_{\alpha}, \zeta_{\alpha}, y_{0,\alpha}, Y_{m,\alpha})^{4}, \Sigma_{\alpha} \subseteq \Sigma$, is a *local preemptor* (for α) if α is defined at every state of $\mathbf{LOC}_{\alpha}^{P}$ where event *tick* is preempted. Let $P_{\alpha} : \Sigma^{*} \to \Sigma_{\alpha}^{*}$ be the natural projection as in (5). Then in terms of language, the above condition means that for every $s \in \Sigma^{*}$ there holds

s.tick
$$\in L(\mathbf{G})$$
 & $s \in P_{\alpha}^{-1}L(\mathbf{LOC}_{\alpha}^{P})$ &
s.tick $\notin P_{\alpha}^{-1}L(\mathbf{LOC}_{\alpha}^{P}) \Rightarrow s\alpha \in L(\mathbf{G}) \cap P_{\alpha}^{-1}L(\mathbf{LOC}_{\alpha}^{P}).$

Notation *s.tick* means that event *tick* occurs after string s, and will be used henceforth. The left side of the above implication means that event tick is preempted in $\mathbf{LOC}_{\alpha}^{P}$ after string *s* (after *s* event *tick* is defined in $L(\mathbf{G})$ but not in $\mathbf{LOC}_{\alpha}^{P}$), and the right side says that forcible event α is defined in $\mathbf{LOC}^{P}_{\alpha}$ (and in $L(\mathbf{G})$) after s. That is, forcible event α acts to preempt *tick.* The event set Σ_{α} of $\mathbf{LOC}_{\alpha}^{P}$ in general satisfies $\{\alpha, tick\} \subseteq \Sigma_{\alpha} \subseteq \Sigma$; in typical cases, however, both subset containments are strict, as will be illustrated in Section 5. Also, for simplicity we assume the lower and upper time bounds of events in Σ_{α} coincide with the bounds on the corresponding events in Σ (this is, in fact, guaranteed by the localization procedure presented below in Section 4). It is worth emphasizing that Σ_{α} (precisely defined below) is not fixed a priori, but will be systematically determined, as part of our localization result, to ensure correct preemptive action.

Next, let $\beta \in \Sigma_{hib}$ be an arbitrary prohibitible event. We say that $\mathbf{LOC}_{\beta}^{C} = (Y_{\beta}, \Sigma_{\beta}, \zeta_{\beta}, y_{0,\beta}, Y_{m,\beta}), \Sigma_{\beta} \subseteq \Sigma$, is a *local controller* (for β) if \mathbf{LOC}_{β}^{C} can disable only event β . Let $P_{\beta} : \Sigma^* \to \Sigma_{\beta}^*$ be the natural projection as in (5). Then in terms of language, the above condition means that for all $s \in \Sigma^*$ and $\sigma \in \Sigma$, there holds (cf. [3])

$$s\sigma \in L(\mathbf{G}) \& s \in P_{\alpha}^{-1}L(\mathbf{LOC}_{\beta}^{C}) \&$$
$$s\sigma \notin P_{\alpha}^{-1}L(\mathbf{LOC}_{\beta}^{C}) \Rightarrow \sigma = \beta.$$

The event set Σ_{β} of \mathbf{LOC}_{β}^{C} in general satisfies $\{\beta\} \subseteq \Sigma_{\beta} \subseteq \Sigma$.⁵ Like Σ_{α} above, Σ_{β} will be generated as part of our localization result to guarantee correct disabling action; again, the events in Σ_{β} are assumed to have the same lower and upper time bounds as the corresponding events in Σ .

Now we formulate the Supervisor Localization Problem of TDES: Construct a set of local preemptors $\{\mathbf{LOC}_{\alpha}^{P} | \alpha \in$

³ **SUP** need not be a (strict) TDES as defined in (4). It can be any automaton whose event set contains *tick*; we refer to such automata as *generalized* TDES.

⁴ $\mathbf{LOC}_{\alpha}^{P}$ is a generalized TDES; we further explain this below in Section 4.

⁵ Event set Σ_{β} need not contain event *tick*, since \mathbf{LOC}_{β}^{C} 's disabling action may be purely logical and irrelevant to time.

 Σ_{for} and a set of local controllers $\{ \mathbf{LOC}_{\beta}^{C} | \beta \in \Sigma_{hib} \}$, with

$$L(\mathbf{LOC}) := \left(\bigcap_{\alpha \in \Sigma_{for}} P_{\alpha}^{-1} L(\mathbf{LOC}_{\alpha}^{P})\right)$$
$$\cap \left(\bigcap_{\beta \in \Sigma_{hib}} P_{\beta}^{-1} L(\mathbf{LOC}_{\beta}^{C})\right) \qquad (15)$$
$$L_{m}(\mathbf{LOC}) := \left(\bigcap_{\alpha \in \Sigma_{for}} P_{\alpha}^{-1} L_{m}(\mathbf{LOC}_{\alpha}^{P})\right)$$
$$\cap \left(\bigcap_{\beta \in \Sigma_{hib}} P_{\beta}^{-1} L_{m}(\mathbf{LOC}_{\beta}^{C})\right) \qquad (16)$$

such that **LOC** is *control equivalent* to **SUP** (with respect to G) in the following sense:

$$L(\mathbf{G}) \cap L(\mathbf{LOC}) = L(\mathbf{SUP}),$$

$$L_m(\mathbf{G}) \cap L_m(\mathbf{LOC}) = L_m(\mathbf{SUP}).$$

For the sake of easy implementation and comprehensibility, it would be desired in practice that the state sizes of local preemptors/controllers be very much less than that of their parent monolithic supervisor. Inasmuch as this property is neither precise to state nor always achievable, it is omitted from the above formal problem statement; in applications, nevertheless, it should be kept in mind.

Using a set of local preemptors and local controllers that is control equivalent to **SUP**, we can build an optimal and nonblocking distributed control architecture for a multi-agent TDES plant. Let the plant **G** with event set Σ be composed ⁶ of *n* component TDES (or agents) \mathbf{G}_k with Σ_k ($k \in [1, n]$).⁷ According to (4), $\Sigma_k =$ $\Sigma_{act,k} \cup \{tick\}$ (event tick is shared by all agents); thus $\Sigma = \bigcup_{k=1}^n \Sigma_{act,k} \cup \{tick\}$. In addition to tick, we also allow the $\Sigma_{act,k}$ to share events. Now let $\Sigma_{for,k}, \Sigma_{hib,k} \subseteq$ Σ_k be the forcible event set and prohibitible event set, respectively, of agent \mathbf{G}_k ; then $\Sigma_{for} = \bigcup_{k=1}^n \Sigma_{for,k}$ and $\Sigma_{hib} = \bigcup_{k=1}^n \Sigma_{hib,k}$. For each forcible event $\alpha \in \Sigma_{for}$ there is a local preemptor \mathbf{LOC}_{α}^P ; and for each prohibitible event $\beta \in \Sigma_{hib}$ there is a local controller \mathbf{LOC}_{β}^C . These local preemptors/controllers need to be allocated among individual agents, for each agent may have multiple forcible/prohibitible events. A convenient allocation is to let each local controller/preemptor be owned by exactly one agent; an example is displayed in Fig 2.

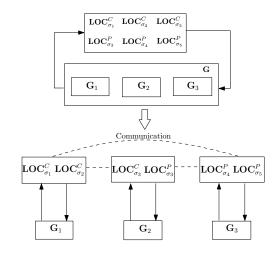


Fig. 2. Example of distributed control by allocating local preemptors/controllers. Continuing the example in Fig. 1, let plant **G** be composed of three agents \mathbf{G}_k with event sets Σ_k , $k \in [1,3]$. Suppose $\sigma_1, \sigma_2 \in \Sigma_1, \sigma_2, \sigma_3 \in \Sigma_2$, and $\sigma_3, \sigma_4, \sigma_5 \in \Sigma_3$; thus \mathbf{G}_1 and \mathbf{G}_2 share event σ_2 , and \mathbf{G}_2 and \mathbf{G}_3 share event σ_3 . Then a convenient allocation is displayed, where each local controller/preemptor is owned by exactly one agent. The allocation creates a distributed control architecture for the multi-agent plant, in which each agent acts semi-autonomously while interacting with other agents through communication of shared events.

Choosing this or (obvious) alternative ways of allocation would be case-dependent.

4 Procedure of Supervisor Localization

We solve the Supervisor Localization Problem of TDES by developing a localization procedure for the supervisor's preemptive and disabling action, respectively. The procedure extends the untimed counterpart in [3]. In particular, localizing the supervisor's preemption of event *tick* with respect to each individual forcible event is novel in the current TDES setup, for which we introduce below two new ideas "preemption consistency relation" and "preemption cover".

Given a TDES plant $\mathbf{G} = (Q, \Sigma, \delta, q_0, Q_m)$ (as in (4)) and a corresponding monolithic supervisor $\mathbf{SUP} = (X, \Sigma, \xi, x_0, X_m)$ (as in (13)) with respect to an imposed specification, we present the localization of \mathbf{SUP} 's preemptive and disabling action in the sequel.

4.1 Localization of Preemptive Action

Fix an arbitrary forcible event $\alpha \in \Sigma_{for}$. First define $E_{tick}: X \to \{1, 0\}$ according to

$$E_{tick}(x) = 1 \text{ iff } \xi(x, tick)!. \tag{17}$$

Thus $E_{tick}(x) = 1$ means that tick is defined at state x in **SUP**. Next define $F_{\alpha} : X \to \{1, 0\}$ according to

⁶ Composition of multiple TDES involves first taking *synchronous product* of the untimed DES, and then unifying the time bounds of shared events [1, 14].

⁷ Note that each \mathbf{G}_k may contain multiple forcible and/or prohibitible events.

Notation: \rightarrow initial state \bigcirc marker state

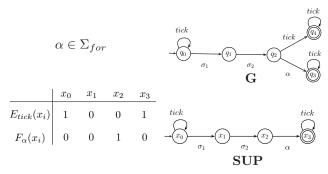


Fig. 3. Preemption consistency relation is not transitive: $(x_0, x_1) \in \mathcal{R}^P_{\alpha}, (x_1, x_2) \in \mathcal{R}^P_{\alpha}, \text{ but } (x_0, x_2) \notin \mathcal{R}^P_{\alpha}.$

 $F_{\alpha}(x) = 1$ iff

$$\xi(x,\alpha)! \& \neg \xi(x,tick)! \& (\exists s \in \Sigma^*) \\ \left(\xi(x_0,s) = x \& \delta(q_0,s.tick)!\right)$$
(18)

So $F_{\alpha}(x) = 1$ means that forcible event α is defined at state x (i.e. $\xi(x, \alpha)$!), which effectively preempts the occurrence of event *tick* (i.e. *tick* is not defined at xin **SUP** but is defined at some state in the plant **G** corresponding to x via string s). It should be noted that at state x, α need not be the only forcible event that preempts *tick*, for there can be other forcible events, say α' , defined at x. In that case, by (18) $F_{\alpha'}(x) = 1$ as well.

Based on the preemption information captured by E_{tick} and F_{α} above, we define the following binary relation \mathcal{R}^{P}_{α} (for α) on X, called 'preemption consistency'. This relation determines if two states of **SUP** have consistent preemptive action with respect to the forcible event α .

Definition 1 Let $\mathcal{R}^{P}_{\alpha} \subseteq X \times X$. We say that \mathcal{R}^{P}_{α} is a preemption consistency relation with respect to $\alpha \in \Sigma_{for}$ if for every $x, x' \in X$, $(x, x') \in \mathcal{R}^{P}_{\alpha}$ iff

$$E_{tick}(x) \cdot F_{\alpha}(x') = 0 = E_{tick}(x') \cdot F_{\alpha}(x).$$
(19)

Thus a pair of states (x, x') in **SUP** is *not* preemption consistent with respect to α only when *tick* is defined at x but is preempted by α at x', or vice versa. Otherwise, x and x' are preemption consistent, i.e. $(x, x') \in \mathcal{R}^P_{\alpha}$. It is easily verified that \mathcal{R}^P_{α} is reflexive and symmetric, but not transitive; an illustration is provided in Fig. 3. Hence \mathcal{R}^P_{α} is not an equivalence relation. This fact leads to the following definition of a *preemption cover*. Recall that a *cover* on a set X is a family of nonempty subsets (or *cells*) of X whose union is X.

Definition 2 Let I be some index set, and $C^P_{\alpha} = \{X_i \subseteq X | i \in I\}$ a cover on X. We say that C^P_{α} is a preemption

cover with respect to α if

(i)
$$(\forall i \in I, \forall x, x' \in X_i)(x, x') \in \mathcal{R}^P_{\alpha},$$

(ii) $(\forall i \in I, \forall \sigma \in \Sigma) \Big[(\exists x \in X_i) \xi(x, \sigma)! \Rightarrow$ (20)
 $((\exists j \in I)(\forall x' \in X_i) \xi(x', \sigma)! \Rightarrow \xi(x', \sigma) \in X_j) \Big].$

A preemption cover C_{α}^{P} lumps states of **SUP** into (possibly overlapping) cells X_i , $i \in I$. According to (i) all states that reside in a cell X_i must be pairwise preemption consistent; and (ii) for every event $\sigma \in \Sigma$, all states that can be reached from any states in X_i by a one-step transition σ must be covered by the same cell X_j . Inductively, two states x, x' belong to a common cell of C_{α}^{P} if and only if x and x' are preemption consistent, and two future states, say y and y', that can be reached respectively from x and x' by a given string are again preemption consistent. We say that a preemption cover C_{α}^{P} is a preemption congruence if C_{α}^{P} happens to be a partition on X, namely its cells are pairwise disjoint.

Having defined a preemption cover C^{P}_{α} on X, we construct, below, a local preemptor $\mathbf{LOC}^{P}_{\alpha} = (Y_{\alpha}, \Sigma_{\alpha}, \zeta_{\alpha}, y_{0,\alpha}, Y_{m,\alpha})$ for the forcible event α to preempt *tick*.

(Step 1) The state set is $Y_{\alpha} := I$, with each state $y \in Y_{\alpha}$ being a cell X_i of the cover \mathcal{C}^P_{α} . In particular, the initial state $y_{0,\alpha}$ is a cell X_{i0} where x_0 belongs, i.e. $x_0 \in X_{i0}$, and the marker state set $Y_{m,\alpha} := \{i \in I | X_i \cap X_m \neq \emptyset\}$.

(Step 2) For the event set Σ_{α} , define the transition function $\zeta'_{\alpha}: I \times \Sigma \to I$ over the entire event set Σ by $\zeta'_{\alpha}(i, \sigma) = j$ if

$$(\exists x \in X_i)\xi(x,\sigma) \in X_j \text{ and} (\forall x' \in X_i)[\xi(x',\sigma)! \Rightarrow \xi(x',\sigma) \in X_j]. (21)$$

Choose Σ_{α} to be the union of $\{\alpha, tick\}$ with other events which are *not* selfloop transitions of ζ'_{α} , i.e.

$$\Sigma_{\alpha} := \{\alpha, tick\} \dot{\cup} \{\sigma \in \Sigma - \{\alpha, tick\} \mid (\exists i, j \in I) \\ i \neq j \& \zeta_{\alpha}'(i, \sigma) = j\}.$$
(22)

Intuitively, only those non-selfloop transitions may affect decisions on *tick* preemption, and thus the events that are only selfloops may be removed. Note that $\{\alpha, tick\} \subseteq \Sigma_{\alpha} \subseteq \Sigma$.

(Step 3) Define the transition function ζ_{α} to be the restriction of ζ'_{α} to Σ_{α} ; namely $\zeta_{\alpha} := \zeta'_{\alpha}|_{\Sigma_{\alpha}} : I \times \Sigma_{\alpha} \to I$ according to $\zeta_{\alpha}(i,\sigma) = \zeta'_{\alpha}(i,\sigma)$ for every $i \in I$ and $\sigma \in \Sigma_{\alpha}$.

We note that $\mathbf{LOC}_{\alpha}^{P}$ thus constructed is not a TDES as defined in (4), for its states do not contain timer information. $\mathbf{LOC}_{\alpha}^{P}$ is indeed a generalized TDES because

its event set Σ_{α} contains *tick*. We will be concerned only with its behavior, namely its closed and marked languages. Also note that, owing to possible overlapping of cells in the cover C_{α}^{P} , the choices of $y_{0,\alpha}$ and ζ_{α} may not be unique, and consequently $\mathbf{LOC}_{\alpha}^{P}$ may not be unique. In that case we pick an arbitrary instance of $\mathbf{LOC}_{\alpha}^{P}$. If C_{α}^{P} happens to be a preemption congruence, however, then $\mathbf{LOC}_{\alpha}^{P}$ is unique.

By the same procedure, we generate a set of local preemptors $\mathbf{LOC}_{\alpha}^{P}$, one for each forcible event $\alpha \in \Sigma_{for}$. We will verify below that these generated preemptors collectively achieve the same preemptive action of event *tick* as the monolithic supervisor **SUP** does.

4.2 Localization of Disabling Action

Next, we turn to the localization of **SUP**'s disabling action, which is analogous to the treatment in [3]. Fix an arbitrary prohibitible event $\beta \in \Sigma_{hib}$. First define $E_{\beta}: X \to \{1, 0\}$ according to

$$E_{\beta}(x) = 1 \text{ iff } \xi(x,\beta)! \tag{23}$$

So $E_{\beta}(x) = 1$ means that β is defined at state x in **SUP**. Next define $D_{\beta}: X \to \{1, 0\}$ according to $D_{\beta}(x) = 1$ iff

$$\neg \xi(x,\beta)! \& (\exists s \in \Sigma^*) (\xi(x_0,s) = x \& \delta(q_0,s\beta)!)$$
(24)

Thus $D_{\beta}(x) = 1$ means that β must be disabled at x(i.e. β is disabled at x in **SUP** but is defined at some state in the plant **G** corresponding to x via string s). In addition, define $M: X \to \{1, 0\}$ according to

$$M(x) = 1 \text{ iff } x \in X_m. \tag{25}$$

Thus M(x) = 1 means that state x is marked in **SUP**. Finally define $T: X \to \{1, 0\}$ according to

$$T(x) = 1 \text{ iff } (\exists s \in \Sigma^*) \xi(x_0, s) = x \& \delta(q_0, s) \in Q_m$$
(26)

So T(x) = 1 means that some state, corresponding to x via s, is marked in **G**. Note that for each $x \in X$, it follows from $L_m(\mathbf{SUP}) \subseteq L_m(\mathbf{G})$ that $T(x) = 0 \Rightarrow M(x) = 0$ and $M(x) = 1 \Rightarrow T(x) = 1$ [3].

Based on (23)-(26), we define the following binary relation $\mathcal{R}_{\beta}^{C} \subseteq X \times X$, called *control consistency* with respect to prohibitible event β (cf. [3]), according to $(x, x') \in \mathcal{R}_{\beta}^{C}$ iff

(i)
$$E_{\beta}(x) \cdot D_{\beta}(x') = 0 = E_{\beta}(x') \cdot D_{\beta}(x),$$

(ii) $T(x) = T(x') \Rightarrow M(x) = M(x').$ (27)

Thus a pair of states (x, x') in **SUP** satisfies $(x, x') \in \mathcal{R}_{\beta}^{C}$ if (i) event β is defined at one state, but not disabled at the other; and (ii) x and x' are both marked or both unmarked in **SUP**, provided both are marked or unmarked in **G**. It is easily verified that \mathcal{R}_{β}^{C} is generally not transitive [3], thus not an equivalence relation. Now let I be some index set, and $\mathcal{C}_{\beta}^{C} = \{X_i \subseteq X | i \in I\}$ a cover on X. Similar to Definition 2, we define \mathcal{C}_{β}^{C} to be a *control cover* with respect to β if

(i)
$$(\forall i \in I, \forall x, x' \in X_i)(x, x') \in \mathcal{R}^C_\beta,$$

(ii) $(\forall i \in I, \forall \sigma \in \Sigma) \Big[(\exists x \in X_i)\xi(x, \sigma)! \Rightarrow$ (28)
 $((\exists j \in I)(\forall x' \in X_i)\xi(x', \sigma)! \Rightarrow \xi(x', \sigma) \in X_j) \Big].$

Note that the only difference between control cover and preemption cover in Definition 2 is the binary relation (control consistency \mathcal{R}^{C}_{β} or preemption consistency \mathcal{R}^{P}_{α}) used in condition (i).

With the control cover C_{β}^{C} on X, we construct by the same steps (**Step1**) - (**Step 3**), above, a local controller $\mathbf{LOC}_{\beta}^{C} = (Y_{\beta}, \Sigma_{\beta}, \zeta_{\beta}, y_{0,\beta}, Y_{m,\beta})$ for prohibitible event β . Here, the choice of event set Σ_{β} is (cf. (22))

$$\Sigma_{\beta} := \{\beta\} \dot{\cup} \{\sigma \in \Sigma - \{\beta\} \mid (\exists i, j \in I) \\ i \neq j \& \zeta_{\beta}'(i, \sigma) = j\}.$$
(29)

 Σ_{β} need not contain event *tick*, as noted in Footnote 5. As before, owing to possible overlapping of cells in the control cover C_{β}^{C} , a local controller \mathbf{LOC}_{β}^{C} need not be unique. If, however, C_{β}^{C} happens to be a control congruence (i.e. C_{β}^{C} is a partition on X), then \mathbf{LOC}_{β}^{C} is unique. In the same way, we generate a set of local controllers \mathbf{LOC}_{β}^{C} , one for each prohibitible event $\beta \in \Sigma_{hib}$. We will verify that the collective disabling action of these local controllers is identical to that of the monolithic supervisor **SUP**.

Finally, notice that an event β may be both prohibitible and forcible. In that case, β will be equipped with both a local controller which exercises disabling action specific to β , and a local preemptor which implements preemption of event *tick* via β . It appears that here a conflict could arise: β 's local preemptor intends to use β to preempt *tick*, but β is disabled by its local controller. However, since β 's local preemptor and controller are both derived from **SUP** which is proved to contain no such conflict [1, 14], the conflict indeed cannot arise between β 's local preemptor and controller. Our main result below confirms this fact.

4.3 Main Result

Here is the main result of this section, which states that the local preemptors and controllers generated by the proposed localization procedure collectively achieve the monolithic optimal and nonblocking supervision.

Theorem 3 The set of local preemptors $\{\mathbf{LOC}_{\alpha}^{P} | \alpha \in \Sigma_{for}\}$ and the set of local controllers $\{\mathbf{LOC}_{\beta}^{C} | \beta \in \Sigma_{hib}\}$ constructed above solve the Supervisor Localization Problem; that is,

$$L(\mathbf{G}) \cap L(\mathbf{LOC}) = L(\mathbf{SUP}), \tag{30}$$

$$L_m(\mathbf{G}) \cap L_m(\mathbf{LOC}) = L_m(\mathbf{SUP}).$$
 (31)

where $L(\mathbf{LOC})$ and $L_m(\mathbf{LOC})$ are as defined in (15) and (16), respectively.

Theorem 3 extends the untimed supervisor localization result in [3] to the TDES setup, where not only the disabling action but also the *tick*-preemptive action of the monolithic supervisor needs to be localized. Thus supervisor localization in TDES generates a set of local controllers, one for each individual prohibitible event, as well as a set of local preemptors, one for each individual forcible event. The proof of Theorem 3, below, relies on the concepts of TDES controllability, control cover, as well as preemption cover.

Since for every preemption cover (resp. control cover), the presented procedure constructs a local preemptor (resp. preemption cover), Theorem 3 asserts that every set of preemption and control covers together generates a solution to the Supervisor Localization Problem. In particular, a set of *state-minimal* local preemptors (resp. local controllers), possibly non-unique, can in principle be defined from a set of suitable preemption covers (resp. control covers). The minimal state problem, however, is known to be NP-hard [11]. In [3] we proposed, nevertheless, a polynomial-time localization algorithm which computes congruences instead of covers; and empirical evidence was given that significant state size reduction can often be achieved. That localization algorithm (see [3, Section III-B]) for untimed DES can easily be adapted in the current TDES case, the only modification being to use the new definitions of preemption and control consistency given in Sections 4.1 and 4.2.

So far we have focused on localization of the monolithic supervisor. In fact, the developed localization procedure may be applied to decompose a modular (decentralized or hierarchical) supervisor just as well. Thus when a TDES is large-scale and the monolithic supervisor not feasibly computable, we may in principle combine localization with an effective modular supervisory synthesis: first compute a set of modular supervisors which achieves the same behavior as the monolithic supervisor, and then apply localization to decompose each modular supervisor in the set. This is done in [3,4] for large-scale untimed DES; and we aim to work out the timed counterpart in future research.

We now provide the proof of Theorem 3. Equation (31) and the (\supseteq) direction of (30) may be verified analogously as in [3]. For completeness we present the verification in the Appendix. Here we prove (\subseteq) in (30), which involves the TDES's controllability definition, preemption consistency, and control consistency.

Proof of Theorem 3. (\subseteq , 30) We show this by induction. First, the empty string ϵ belongs to $L(\mathbf{G})$, $L(\mathbf{LOC})$, and $L(\mathbf{SUP})$, because these languages are all nonempty. Next, suppose $s \in L(\mathbf{G}) \cap L(\mathbf{LOC})$, $s \in L(\mathbf{SUP})$, and $s\sigma \in L(\mathbf{G}) \cap L(\mathbf{LOC})$ for an arbitrary event $\sigma \in \Sigma$. It will be proved that $s\sigma \in L(\mathbf{SUP})$. Since $\Sigma = \Sigma_u \dot{\cup} \Sigma_c = \Sigma_u \dot{\cup} \{tick\} \dot{\cup} \Sigma_{hib}$ (as in (9)), we consider the following three cases.

(i) Let $\sigma \in \Sigma_u$. Since $L_m(\mathbf{SUP})$ is controllable (see (12)), and $s\sigma \in L(\mathbf{G})$ (i.e. $\sigma \in Elig_{\mathbf{G}}(s)$ by (10)), we have $\sigma \in Elig_{L_m(\mathbf{SUP})}(s)$. That is, by (11) $s\sigma \in \overline{L}_m(\mathbf{SUP}) = L(\mathbf{SUP})$.

(ii) Let $\sigma = tick$. We will show $tick \in Elig_{L_m(\mathbf{SUP})}(s)$ to conclude that $s.tick \in \overline{L}_m(\mathbf{SUP}) = L(\mathbf{SUP})$. By the hypothesis that $s, s.tick \in L(\mathbf{LOC})$ and equation (15), for every forcible event $\alpha \in \Sigma_{for}$ there holds $s, s.tick \in P_{\alpha}^{-1}L(\mathbf{LOC}_{\alpha}^{P})$, i.e. $P_{\alpha}(s), P_{\alpha}(s)$ $tick \in$ $L(\mathbf{LOC}_{\alpha}^{P})$. Recall $\mathbf{LOC}_{\alpha}^{P} = (Y_{\alpha}, \Sigma_{\alpha}, \zeta_{\alpha}, y_{0,\alpha}, Y_{m,\alpha})$, and let $i := \zeta_{\alpha}(y_{0,\alpha}, P_{\alpha}(s))$ and $j := \zeta_{\alpha}(i, tick)$. By definition of ζ'_{α} in (21), any $\sigma \notin \Sigma_{\alpha}$ (defined in (22)) is only a selfloop transition of ζ'_{α} ; hence $\zeta'_{\alpha}(y_{0,\alpha}, s) = i$. By (21) again, there exist $x, x' \in X_i$ and $x'' \in X_j$ such that $\xi(x_0, s) = x$ and $\xi(x', tick) = x''$ in **SUP**. These state-transition correspondences between $\mathbf{LOC}_{\alpha}^{P}$ and **SUP** are displayed in Fig. 4.

Now that x, x' belong to the same cell X_i , by the preemption cover definition (Definition 2) x and x' must be preemption consistent, i.e. $(x, x') \in \mathcal{R}^P_{\alpha}$. Since $\xi(x', tick)!$, by (17) we have $E_{tick}(x') = 1$. Thus the requirement $E_{tick}(x') \cdot F_{\alpha}(x) = 0$ (Definition 1) yields that $F_{\alpha}(x) = 0$. The latter, by (18), gives rise to the following three cases: (Case 1) $\neg \xi(x, \alpha)!$, (Case 2) $\xi(x, tick)!$, or (Case 3) $(\neg \exists s \in \Sigma^*)(\xi(x_0, s) = x \& \delta(q_0, s.tick)!)$. First, Case 3 is impossible, because by the hypothesis that $s \in L(\mathbf{SUP})$ and $s.tick \in L(\mathbf{G})$ we have $\xi(x_0, s)!$ and $\delta(q_0, s.tick)!$. Next, Case 2 means directly $tick \in Elig_{L_m}(\mathbf{SUP})(s)$. Finally, Case 1 implies $\alpha \notin Elig_{L_m}(\mathbf{SUP})(s) \cap \Sigma_{for} = \emptyset$. Then by the fact that \mathbf{SUP} is controllable, we derive from (12) that $tick \in Elig_{L_m}(\mathbf{SUP})(s)$.

(iii) Let $\sigma \in \Sigma_{hib}$. By the hypothesis $s, s\sigma \in L(\mathbf{LOC})$ and equation (15), we have $s, s\sigma \in P_{\sigma}^{-1}L(\mathbf{LOC}_{\sigma}^{C})$,

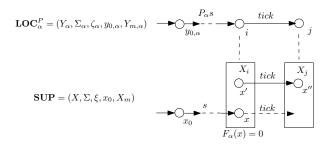
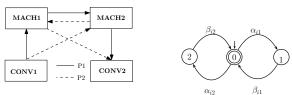
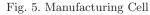


Fig. 4. State-transition correspondences between $\mathbf{LOC}_{\alpha}^{P}$ and **SUP**. It is proved in the text that *tick* is also defined at state x.



P1: $\text{CONV1} \rightarrow \text{MACH1} \rightarrow \text{MACH2} \rightarrow \text{CONV2}$ Untimed DES models of P2: $\text{CONV1} \rightarrow \text{MACH2} \rightarrow \text{MACH1} \rightarrow \text{CONV2}$ MACHi, i = 1, 2



i.e. $P_{\sigma}(s), P_{\sigma}(s)\sigma \in L(\mathbf{LOC}_{\sigma}^{C})$. As in (ii), let $i := \zeta_{\sigma}(y_{0,\sigma}, P_{\sigma}(s)) = \zeta'_{\sigma}(y_{0,\sigma}, s)$ and $j := \zeta_{\sigma}(i, \sigma)$. By the definition of ζ'_{σ} in (21), there exist $x, x' \in X_i$, $x'' \in X_j$ such that $\xi(x_0, s) = x$ and $\xi(x', \sigma) = x''$. Since x, x' belong to the same cell X_i , by the control cover definition x and x' must be control consistent, i.e. $(x, x') \in \mathcal{R}_{\sigma}^{C}$. That $\xi(x', \sigma)!$ implies by (23) that $E_{\sigma}(x') = 1$. Thus the requirement $E_{\sigma}(x') \cdot D_{\sigma}(x) = 0$ yields that $D_{\sigma}(x) = 0$. The latter, by (24), gives rise to the following two cases: (Case 1) $\xi(x, \sigma)!$, or (Case 2) $(\neg \exists s \in \Sigma^*)\xi(x_0, s) = x \& \delta(q_0, s\sigma)!$. Case 2 is impossible, because by the hypothesis that $s \in L(\mathbf{SUP})$ and $s.tick \in L(\mathbf{G})$ we have $\xi(x_0, s)!$ and $\delta(q_0, s.tick)!$. But in Case 1, $\xi(x, \sigma)!$ i.e. $s\sigma \in L(\mathbf{SUP})$.

5 Case Study: Manufacturing Cell

We illustrate supervisor localization in TDES by studying a manufacturing cell example, taken from [1], [14, Section 9.11]. As displayed in Fig. 5, the cell consists of two machines, **MACH1** and **MACH2**, an input conveyor **CONV1** as an infinite source of workpieces, and output conveyor **CONV2** as an infinite sink. Each machine processes two types of parts, P1 and P2. Each type of part is routed as shown in Fig. 5. The untimed DES models of the machines are also displayed in Fig. 5; here α_{ij} $(i, j \in [1, 2])$ is the event "**MACHi** starts to work on a Pj-part", while β_{ij} $(i, j \in [1, 2])$ is "**MACHi** finishes working on a Pj-part". Assign lower and upper time bounds to each event, with the notation (event, lower

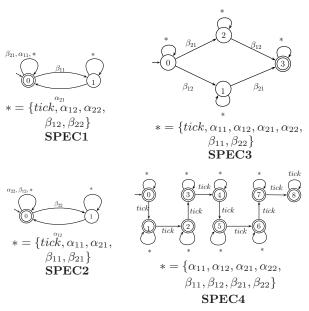


Fig. 6. Control specifications: logical and temporal. The marked state 3 of **SPEC3** corresponds to the completion of a production cycle: one P1-part and one P2-part are processed by both machines.

bound, upper bound), as follows:

MACH1's timed events :

$$(\alpha_{11}, 1, \infty)$$
 ($\beta_{11}, 3, 3$) ($\alpha_{12}, 1, \infty$) ($\beta_{12}, 2, 2$)
MACH2's timed events :
 $(\alpha_{21}, 1, \infty)$ ($\beta_{21}, 1, 1$) ($\alpha_{22}, 1, \infty$) ($\beta_{22}, 4, 4$)

So α_{ij} are remote events (upper bound ∞), and β_{ij} prospective events (finite upper bounds). Now the TDES models of the two machines can be generated [14, p.425]. Their joint behavior is the synchronous product of the two TDES, which in this example is the plant to be controlled.

To impose behavioral constraints on the two machines' joint behavior, we take the events α_{ij} to be both prohibitible and forcible, i.e. $\Sigma_{hib} = \Sigma_{for} =$ $\{\alpha_{ij}|i, j = 1, 2\}$, and the β_{ij} to be uncontrollable, i.e. $\Sigma_u = \{\beta_{ij}|i, j = 1, 2\}$. We impose the following *logical* control specifications as well as a *temporal* specification:

(S1) A P1-part must be processed first by **MACH1** and then by **MACH2**.

(S2) A P2-part must be processed first by **MACH2** and then by **MACH1**.

(S3) One P1-part and one P2-part must be processed in a production cycle.

(S4) A production cycle must be completed in at most 8 time units. 8

⁸ Here we choose "8 time units" because it is, according to [1,

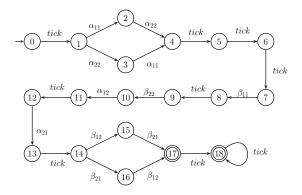


Fig. 7. Monolithic optimal and nonblocking supervisor SUP.

These four specifications are formalized as automata **SPEC1**, **SPEC2**, **SPEC3**, and **SPEC4**, respectively, as displayed in Fig. 6. The temporal specification **SPEC4** is simply an 8-*tick* sequence, with all states marked; **SPEC4** forces any TDES with which it is synchronized to halt after at most 8 *ticks*, i.e. after 8 *ticks* to execute no further event whatever except event *tick*. Thus it extracts the marked strings (if any) which satisfy this constraint, namely the 'tasks' of TDES that can be accomplished in at most 8 *ticks* (which turns out to be exactly one production cycle according to [1, 14]).

Now the plant to be controlled is the synchronous product of TDES MACH1 and MACH2 [14, p.425], and the overall control specification is the synchronous product of automata **SPEC1-SPEC4** in Fig. 6. We compute as in (14) the corresponding monolithic optimal and nonblocking supervisor **SUP**; the computation is done by the *supcon* command in XPTTCT [13]. **SUP** has 19 states and 21 transitions, as displayed in Fig. 7. We see that **SUP** represents the behavior that the manufacturing cell accomplishes exactly one working cycle, within 8 ticks, producing one P1-part and one P2-part. Indeed, each event is executed exactly once, and each forcible event preempts *tick* immediately after it becomes eligible to occur.

We now apply supervisor localization to decompose the monolithic supervisor **SUP** into local preemptors and local controllers, respectively for each forcible event and each prohibitible event. Specifically, since $\Sigma_{hib} = \Sigma_{for} = \{\alpha_{ij} | i, j = 1, 2\}$, we will compute a local preemptor and a local controller for each α_{ij} , responsible for α_{ij} 's tick-preemptive action and its disabling action, respectively. This computation can be done by an algorithm adapted from [3] (as discussed in Section 4.3); here, however, owing to the simple (chain-like) structure of **SUP** (Fig. 7), local preemptors/controllers can be derived by inspection. We demonstrate such a derivation below, which results in a local preemptor $\mathbf{LOC}_{\alpha_{11}}^{P}$ for the forcible (and

prohibitible) event α_{11} . Other derivations of local preemptors/controllers are similar.

To derive a local preemptor $\mathbf{LOC}_{\alpha_{11}}^{P}$ for event α_{11} , we find a preemption cover $\mathcal{C}_{\alpha_{11}}^{P}$ for α_{11} on **SUP**'s state set as follows. Initialize $\mathcal{C}_{\alpha_{11}}^{P}$ to be $\mathcal{C}_{\alpha_{11}}^{P} = \{[0], [1], [2], ..., [18]\}$, i.e. each cell contains exactly one state of **SUP**. Subsequently, we merge as many cells together as possible according to Definitions 1 and 2, while maintaining $\mathcal{C}_{\alpha_{11}}^{P}$ to be a preemption cover.

(i) Cells [0] and [1] cannot be merged. Since $E_{tick}(0) = 1$ (event *tick* is defined at state 0) and $F_{\alpha_{11}}(1) = 1$ (*tick* is preempted by α_{11} at state 1), the pair of states (0, 1) is not preemption consistent, i.e. $(0, 1) \notin \mathcal{R}^{P}_{\alpha_{11}}$. Consequently, merging cells [0] and [1] violates requirement (i) of preemption cover (Definition 2).

(ii) Cells [1], [3] and cells [2], [4] can be merged. For cells [2] and [4], we have $F_{\alpha_{11}}(2) = 0$, $E_{tick}(2) = 0$ (tick is preempted at state 2, but by α_{22} not by α_{11}) and $E_{tick}(4) = 1$, $F_{\alpha_{11}}(4) = 0$ (event tick is defined at state 4). Thus $(2, 4) \in \mathcal{R}_{\alpha_{11}}^{P}$, which satisfies requirement (i) of preemption cover. Moreover since no common event is defined on states 2 and 4, requirement (ii) of preemption cover is trivially satisfied. Therefore cells [2], [4] can be merged.

For cells [1] and [3], we have $F_{\alpha_{11}}(1) = F_{\alpha_{11}}(3) = 1$ (*tick* is preempted by α_{11} at both states 1 and 3) and $E_{tick}(1) = E_{tick}(3) = 0$. Thus $(1,3) \in \mathcal{R}^P_{\alpha_{11}}$, which satisfies requirement (i) of preemption cover. Now event α_{11} is defined at both states 1 and 3, but it leads to states 2 and 4 respectively, which have been verified to be preemption consistent. Hence, requirement (ii) of preemption cover is also satisfied, and cells [1], [3] can be merged.

By merging the above two pairs of cells, we derive $C^{P}_{\alpha_{11}} = \{[0], [1, 3], [2, 4], [5], ..., [18]\}.$

(iii) Cells $[2, 4], [5], \ldots, [18]$ can all be merged together. Note, indeed, that $F_{\alpha_{11}}(\cdot) = 0$ for all these states (no *tick* preemption by α_{11}). On checking the preemption consistency and preemption cover definitions as above, we conclude that the final preemption cover is $C_{\alpha_{11}}^P = \{[0], [1, 3], [2, 4, 5, ..., 18]\}$. It is in fact a preemption congruence.

Having found the preemption cover $C_{\alpha_{11}}^P$, we apply (Step 1) - (Step 3) in Section 4.1 to construct a local preemptor $\mathbf{LOC}_{\alpha_{11}}^P$, with transition structure displayed in Fig. 8. Note that the event set of $\mathbf{LOC}_{\alpha_{11}}^P$ is exactly $\{\alpha_{11}, tick\}$, which means that $\mathbf{LOC}_{\alpha_{11}}^P$ does not need to observe any external events in order to execute its preemptive action. Similarly, we derive other local preemptors and local controllers, all displayed in Figs. 8 and 9. Here, for example, the event set of $\mathbf{LOC}_{\alpha_{12}}^P$ is

^{14],} the minimal time to complete one production cycle. Thus this temporal specification represents a time-minimization requirement.

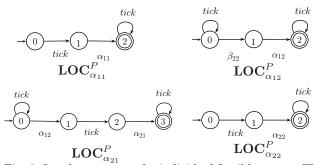


Fig. 8. Local preemptors for individual forcible events. The alphabet of each local preemptor is the set of events displayed in each automaton.

 $\{\alpha_{12}, tick, \beta_{22}\}$; so event β_{22} originating in **MACH2** has to be observed by $\mathbf{LOC}_{\alpha_{12}}^{P}$. We have then verified that their joint behavior (via synchronous product) is identical to the monolithic optimal and nonblocking behavior of **SUP**, i.e. (30) and (31) hold.

We see that each local preemptor/controller has fewer states, with a simpler structure, than the monolithic **SUP**; this renders each one's preemptive/disabling action more transparent. For example, the local preemptor $\mathbf{LOC}_{\alpha_{11}}^{P}$ (resp. $\mathbf{LOC}_{\alpha_{22}}^{P}$) in Fig. 8 means that after one tick, forcible event $\tilde{\alpha}_{11}$ preempts event *tick* and **MACH1** starts to work on a P1-part (resp. α_{22} preempts tick and MACH2 works on a P2-part). This is possible because α_{11} (resp. α_{22}) has lower time bound 1 and becomes eligible to occur after one tick. For another example, the local preemptor $\mathbf{LOC}_{\alpha_{21}}^{P}$ in Fig. 8 specifies that after occurrence of α_{12} followed by a *tick*, forcible event α_{21} preempts *tick* and **MACH2** starts to work on a P1-part. This preemption is due to the fact that α_{21} has lower time bound 1 and becomes eligible to occur after occurrence of β_{22} plus one tick (according to Fig. 7 event α_{22} first occurs in **MACH2**, which implies from the untimed model in Fig. 5 the event order $\alpha_{22},\beta_{22},\alpha_{21}$). But occurrence of α_{12} implies that β_{22} has just occurred (see Fig. 7).

For control logic, the local controller $\mathbf{LOC}_{\alpha_{12}}^C$ in Fig. 9 means that prohibitible event α_{12} is enabled only after occurrence of event β_{22} , i.e. **MACH1** starts to work on a P2-part only after **MACH2** finishes that P2-part. On the other hand, the logic of $\mathbf{LOC}_{\alpha_{21}}^C$ is a bit subtle; it specifies that prohibitible event α_{21} is enabled after occurrence of event α_{22} . At first glance, the logic seems to violate the specification **SPEC1** in Fig. 6, which says that α_{21} should not be enabled before occurrence of β_{11} . Observe, nevertheless, that α_{21} cannot become eligible to occur before occurrence of β_{22} which has lower (and upper) time bound 4, and event β_{11} in fact has already occurred when β_{22} occurs (see Fig. 7). Hence it is legal to enable α_{21} after α_{22} .

Finally, with the derived set of local preemptors and controllers, we build a distributed control architec-

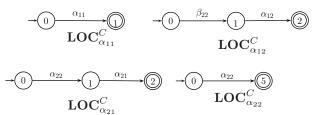


Fig. 9. Local controllers for individual prohibitible events. The alphabet of each local controller is the set of events displayed in each automaton.

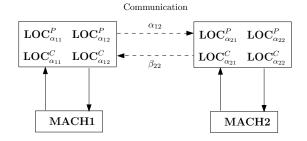


Fig. 10. Distributed control architecture for manufacturing cell.

ture for this manufacturing cell of two machines; see Fig. 10. Each machine acquires those local preemptors/controllers with respect to its own distinct forcible/prohibitible events, thereby being capable of executing local preemptive/disabling actions. For these local actions to jointly achieve the same controlled behavior as the monolithic supervisor does, communicating certain 'critical' events (in this case α_{12} and β_{22}) between the two machines is essential. The critical events are obtained by intersecting the alphabet of one machine and the alphabets of local preemptors/controllers of the other machine.

6 Conclusions

We have established supervisor localization in the Brandin-Wonham timed DES framework. Under this localization scheme, each individual agent disables its own prohibitible events and preempts event *tick* via its own forcible events; overall, these local control actions collectively achieve monolithic optimal and nonblocking supervision. We have demonstrated the timed supervisor localization on a manufacturing cell case study. In future research, we aim to combine the developed localization approach with an effective modular supervisor synthesis to address distributed control of large-scale real-time DES.

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A Appendix

We complete the proof of Theorem 3, namely equation (31) and (\supseteq) in (30).

 $(\supseteq, 31)$ Since $L_m(\mathbf{SUP}) \subseteq L_m(\mathbf{G})$, it suffices to show that $L_m(\mathbf{SUP}) \subseteq L_m(\mathbf{LOC})$. That is, by (16),

$$(\forall \alpha \in \Sigma_{for}) \ L_m(\mathbf{SUP}) \subseteq P_{\alpha}^{-1} L_m(\mathbf{LOC}_{\alpha}^P), \quad (A.1) (\forall \beta \in \Sigma_{hib}) L_m(\mathbf{SUP}) \subseteq P_{\beta}^{-1} L_m(\mathbf{LOC}_{\beta}^C). \quad (A.2)$$

We prove (A.1), and (A.2) follows similarly. Let $s = \sigma_0 \sigma_1 \cdots \sigma_h \in L_m(\mathbf{SUP})$. Then $x_1 := \xi(x_0, \sigma_0), ..., x_{h+1} := \xi(x_0, s) \in X_m$. By the construction of \mathbf{LOC}_{α}^P ($\alpha \in \Sigma_{for}$ arbitrary), in particular the transition function ζ'_{α} over Σ in (21), there exist $i_0, i_1, ..., i_{h+1}$ with

 $(i_0 = y_{0,\alpha})$ such that

$$\begin{aligned}
x_{0} \in X_{i_{0}} \& \zeta_{\alpha}'(i_{0}, \sigma_{0}) &= i_{1}, \\
x_{1} \in X_{i_{1}} \& \zeta_{\alpha}'(i_{1}, \sigma_{1}) &= i_{2}, \\
\vdots \\
x_{h+1} \in X_{i_{h+1}} \& \zeta_{\alpha}'(i_{h}, \sigma_{h}) &= i_{h+1}.
\end{aligned}$$
(A.3)

So $\zeta'_{\alpha}(i_0, \sigma_0 \sigma_1 \cdots \sigma_h) = \zeta'_{\alpha}(i_0, s)!$, and belongs to $Y_{m,\alpha}$ because $X_{i_{h+1}} \cap X_m \neq \emptyset$ (x_{h+1} belongs to X_m). Moreover since any $\sigma \notin \Sigma_{\alpha}$ (defined in (22)) is only a selfloop transition of ζ'_{α} , we derive $\zeta_{\alpha}(i_0, P_{\alpha}(s)) \in Y_{m,\alpha}$. Hence, $P_{\alpha}(s) \in L_m(\mathbf{LOC}^P_{\alpha})$, i.e. $s \in P_{\alpha}^{-1}L_m(\mathbf{LOC}^P_{\alpha})$.

 $(\supseteq, 30)$ This is an easy consequence of $(\supseteq, 31)$:

$$L(\mathbf{SUP}) = \overline{L_m(\mathbf{SUP})}$$
$$\subseteq \overline{L_m(\mathbf{G}) \cap L_m(\mathbf{LOC})}$$
$$\subseteq L(\mathbf{G}) \cap L(\mathbf{LOC}).$$

 $(\subseteq, 31)$ Let $s \in L_m(\mathbf{G}) \cap L_m(\mathbf{LOC})$; by (16), for every $\beta \in \Sigma_{hib}$, $s \in P_\beta^{-1}L_m(\mathbf{LOC}_\beta^C)$, i.e. $P_\beta(s) \in L_m(\mathbf{LOC}_\beta^C)$. Write $i := \zeta_\beta(y_{0,\beta}, P_\beta(s))$. Then there exists $x \in X_i \cap X_m$; thus M(x) = 1 (defined in (25)), which also implies T(x) = 1 (defined in (26)). On the other hand, since $L_m(\mathbf{G}) \cap L_m(\mathbf{LOC}) \subseteq L(\mathbf{G}) \cap L(\mathbf{LOC}) =$ $L(\mathbf{SUP})$ (the last equality has already been shown), we have $s \in L(\mathbf{SUP})$. That is, $\xi(x_0, s)$!; as in (A.3) above we derive $\xi(x_0, s) \in X_i$, and by the control cover definition (28) it holds that $(x, \xi(x_0, s)) \in \mathcal{R}_\beta^C$. Since $s \in L_m(\mathbf{G})$, i.e. $\delta(q_0, s) \in Q_m$, we have $T(\xi(x_0, s)) = 1$. Therefore by requirement (ii) of the control consistency definition (27), $M(\xi(x_0, s)) = 1$, i.e. $s \in L_m(\mathbf{SUP})$.