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A PTIME-complete matching problem for SLP-compressed words

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Abstract

SLP-compressed words are words given by simple deterministic grammars called "straight-line programs". We prove that the problem of deciding whether an SLP-compressed word is recognized by a FSA is complete for polynomial-time.

Key words: Algorithms, pattern-matching, compressed strings, complexity.

1 Introduction

Data compression is a powerful and versatile (hence popular) technique for reducing storage space. The issue of finding efficient algorithms working directly on compressed data received a lot of attention recently. In this field, a paradigmatic family of problems are (searching and) matching problems on compressed strings [10]. This is because strings are the most basic data structure, while searching and matching are ubiquitous problems.

For strings, many compression schemes exist [11]. At a conceptual level, the main compression schemes are SLP (for "Straight-Line Programs"), LZ (for "Lempel-Ziv"), RLZ (for "Restricted Lempel-Ziv") and LZW (Lempel-Ziv-Welch). These four schemes allow *exponential compression*: compressed strings may denote full-length texts of exponential length. They are more or less equivalent since it is possible to translate in polynomial-time compressed strings

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from one scheme to another. Hence these compression schemes are successful on the same instances, and polynomial-time algorithms for one scheme can be transferred to the others.

Many matching problems on compressed strings can be solved in polynomial-time [9]. But there are situations where polynomial-time is not good enough: since compressed texts can be quite large, it is interesting to have efficient parallel algorithms (i.e., algorithms in NC) or polynomial-time algorithms that only use polylog-space (i.e., in SC)¹.

It has recently been observed that some matching problems on compressed strings admit NC algorithms [5,4,6]. However NC or SC algorithms are not always possible ², and some matching problems on compressed strings are known to be complete for polynomial-time [3,4].

When it comes to algorithms in NC or SC, the three main compression schemes (SLP, LZ and LZW) are not equivalent. Regarding complexity below PTIME, the available results are scarce. Some problems are PTIME-hard for LZ [5,9,4] but not for SLP. The evidence seems to indicate that SLP-compressed words are easier (that is, more amenable to efficient algorithms) than (R)LZ(W)-compressed words.

In this note, we prove that deciding whether an SLP-compressed word is accepted by a (fixed) finite-state automaton (a FSA) is PTIME-complete. This is the first example of a PTIME-hard problem for SLP-compressed words.

We also consider matching problems simpler than FSA-acceptance, namely telling whether a given string occurs in the SLP-compressed word. For these problems, we provide simple proofs showing they admit LOGCFL algorithms, hence are low inside NC.

2 Preliminaries

We follow [9]. A straight-line program, or SLP, is a context-free grammar where the non-terminals N_1, \ldots, N_m are ordered $(N_m$ being the axiom), and where every non-terminal has a single production of the form $N_i \to a$ for a terminal a, or $N_i \to N_j N_k$ for some j, k < i. For an SLP P, we write $w(N_i)$ for the unique word described by N_i . Then w(P) stands for $w(N_m)$.

¹ We refer to [8] or [7] for more details on classes below PTIME.

² Here and in the following we implicitly make the standard assumption that PTIME does not collapse to NC or SC.

Proposition 1 [9] Saying whether w(P) is recognized by \mathcal{A} (for P an SLP, and \mathcal{A} a finite-state automaton) can be done in time $O(|P| \times |\mathcal{A}|^3)$.

PROOF. [9] describes a simple dynamical programming solution. For two states r, s of \mathcal{A} and non-terminal N_i of P, set $T[r, s, i] = \mathsf{true}$ iff $w(N_i)$ labels a path going from r to s in \mathcal{A} . Obviously, if $N_i \to N_j N_k$ is a rule in P, then $T[r, s, i] = \bigvee_u T[r, u, j] \wedge T[u, s, k]$. Hence the table $T[\ldots]$ is easy to fill. Then we can use $T[\ldots]$ to see whether w(P), i.e. $w(N_m)$, labels an accepting path.

3 The main result

Theorem 2 Saying whether w(P) is accepted by a FSA A, is PTIME-complete. Furthermore, PTIME-hardness already occurs for a fixed FSA.

In view of Proposition 1, only the second part of Theorem 2 has to be proved.

For this, we start by describing A_5 , the fixed FSA, and some of its properties. We were inspired by [1] for this construction. A_5 has 5 states numbered from 0 to 4 and is depicted in Fig. 1. The initial state is 0 and the only final state is 1.

For two states $s, t \in \{0, 1, 2, 3, 4\}$, we write $s \xrightarrow{a} t$ $(s \xrightarrow{b} t, s \xrightarrow{c} t)$ when there is an a-labeled (resp., b-labeled, c-labeled) arrow from s to t. We write $s \xrightarrow{\text{inc}} t$ when $t = s + 1 \mod 5$: hence $\xrightarrow{\text{inc}}$ rotates one step clockwise. Observe that the relations \xrightarrow{a} , \xrightarrow{b} , \xrightarrow{c} , and $\xrightarrow{\text{inc}}$ are in fact bijections. Further observe that $\xrightarrow{\text{inc}} = \xrightarrow{b} \xrightarrow{a}$ (we compose functions from left to right: $f \cdot f'$ denotes $f' \circ f$). Finally, we let id denote the identity between states.

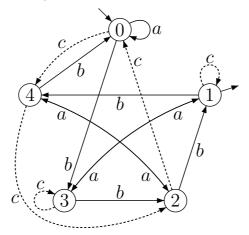


Fig. 1. A_5 , a fixed FSA.

As the reader will easily check, the construction of A_5 ensures that the following equalities hold:

$$\xrightarrow{a} \cdot \xrightarrow{b} \cdot \xrightarrow{c} \cdot \xrightarrow{b} \cdot \xrightarrow{b} = \xrightarrow{\text{inc}}$$
 (1a)

$$\xrightarrow{a} \cdot \xrightarrow{b} \xrightarrow{\text{inc}} \xrightarrow{c} \cdot \xrightarrow{b} \xrightarrow{\text{inc}} \xrightarrow{b} = \xrightarrow{\text{inc}} \tag{1b}$$

$$\stackrel{a}{\rightarrow} \stackrel{\text{inc}}{\rightarrow} \stackrel{b}{\rightarrow} \cdot \stackrel{c}{\rightarrow} \stackrel{\text{inc}}{\rightarrow} \stackrel{b}{\rightarrow} \cdot \stackrel{b}{\rightarrow} = \stackrel{\text{inc}}{\rightarrow}$$
 (1c)

$$\xrightarrow{a} \xrightarrow{\text{inc}} \xrightarrow{b} \xrightarrow{\text{inc}} \xrightarrow{c} \xrightarrow{\text{inc}} \xrightarrow{b} \xrightarrow{\text{inc}} \xrightarrow{b} = id$$
 (1d)

We now prove Theorem 2 by reduction from NAND-CIRCUIT-VALUE. Let $\mathcal C$ be a Boolean circuit made of nand gates with fan-in 2, some constant inputs, and with one designated output gate g. Fig. 2 displays an example.

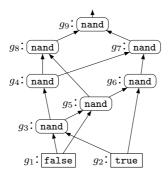


Fig. 2. C, a Boolean circuit of nand gates.

Every gate g_i evaluates to some $v(g_i) \in \{\text{true}, \text{false}\}\$ in the obvious way, and we let $v(\mathcal{C})$ denote the value of the output gate. It is well-known that telling whether $v(\mathcal{C}) = \text{true}$ is a PTIME-complete problem [7, problem A.1.5].

With a circuit like C, we associate a grammar P_C that has one non-terminal N_i for every gate g_i . The rule for N_i depends on whether g_i is a nand gate inputing from g_i and g_k (note that j = k is possible) or it is an input gate carrying true or false:

if
$$g_i := \operatorname{nand}(g_i, g_k)$$
 then $N_i \to a N_i b N_k c N_i b N_k b$ (2)

$$\begin{array}{ll} \text{if } g_i := \operatorname{nand}(g_j,g_k) & \quad \text{then } N_i \to a \; N_j \; b \; N_k \; c \; N_j \; b \; N_k \; b \\ \text{if } g_i := \text{true} & \quad \text{then } N_i \to b \; a \\ \text{if } g_i := \text{false} & \quad \text{then } N_i \to \varepsilon \end{array} \tag{3}$$

if
$$g_i := \mathtt{false}$$
 then $N_i \to \varepsilon$ (4)

Finally, if g_m is the output gate in C, then the axiom of P_C is N_m .

For a word $w = a_1 \dots a_m$, we write \xrightarrow{w} for the composition $\xrightarrow{a_1} \dots \xrightarrow{a_m}$ (with $\xrightarrow{\varepsilon}$ being id).

³ Strictly speaking $P_{\mathcal{C}}$ is not a SLP, but it is easy to provide an equivalent linear-sized SLP. In particular, Eq. (4) needs not be used in view of false = nand(true, true).

Lemma 3 For every gate g_i in C:

- (i) if $v(g_i) = \text{true } then \xrightarrow{w(N_i)} = \xrightarrow{\text{inc}}$,
- (ii) if $v(g_i) =$ false then $\stackrel{w(N_i)}{\longrightarrow} = id$.

PROOF. By induction over the height of the gate in the circuit. For the base cases Eqs. (3) and (4) directly ensure (i) and (ii) (recall that $\stackrel{ba}{\rightarrow} = \stackrel{\text{inc}}{\rightarrow}$).

For the inductive step, g_i is some $\operatorname{nand}(g_j, g_k)$. There are four cases. Assume for example that $v(g_j) = \operatorname{true}$ and $v(g_k) = \operatorname{false}$, so that $v(g_i) = \operatorname{nand}(\operatorname{true}, \operatorname{false}) = \operatorname{true}$ and we have to prove $\stackrel{w(N_i)}{\longrightarrow} = \stackrel{\operatorname{inc}}{\longrightarrow}$. By ind. hyp. $\stackrel{w(N_j)}{\longrightarrow} = \stackrel{\operatorname{inc}}{\longrightarrow}$ and $\stackrel{w(N_k)}{\longrightarrow} = id$. Now equation (1c) exactly states that $\stackrel{w(N_i)}{\longrightarrow} = \stackrel{\operatorname{inc}}{\longrightarrow}$, given that N_i is defined by (2). The three other cases use the other equalities in (1).

Corollary 4
$$v(\mathcal{C}) = \text{true } iff \xrightarrow{w(P_{\mathcal{C}})} = \text{ inc } iff \ w(P_{\mathcal{C}}) \ is \ accepted \ by \ \mathcal{A}_5.$$

Thus we have provided a logspace reduction from NAND-CIRCUIT-VALUE to acceptance of SLP compressed words by the fixed FSA \mathcal{A}_5 , proving the second part of Theorem 2. \mathcal{A}_5 uses three letters for clarity but a two-letter alphabet would have been sufficient in view of $\stackrel{c}{\hookrightarrow} = \stackrel{bbababab}{\longrightarrow}$.

4 Efficient pattern-matching for SLP-compressed words

In this section we look at pattern-matching problems that are special cases of FSA acceptance, trying to strengthen our PTIME-hardness result by extending it to a problem simpler than FSA acceptance.

It turns out that the problems we consider are in LOGDCFL and LOGCFL respectively, hence they are in AC¹ and admit fast parallel algorithms. Note that these same problems are PTIME-complete for LZ-compressed words [4]. We see this as evidence that SLP-compressed words are "more manageable" than LZ-compressed words even though they are not significantly longer.

We start with the simplest matching problem: does w(P) exactly match a given string p?

Theorem 5 Deciding whether w(P) equals a string p is in LOGDCFL.

PROOF. [Sketch] We show that the problem can be solved by a deterministic

Turing Machine having access to an auxiliary unbounded pushdown storage and working in logarithmic space and polynomial time. Then we conclude relying on the characterization LOGDCFL = $\mathbf{AuxPD\text{-}DSPACE}(\log n, \operatorname{pol} n)$ from [2,12].

The algorithm works as follows: Given an SLP P, one puts the axiom N_m on the initially empty stack, and stores a pointer to the beginning of p. The stack will be used to store the sequence of non-terminals that remain to be developed. As long as the stack is not empty, we pop the first non-terminal, N_i (say), from the top of the stack: either P has a rule $N_i \to N_j N_k$, and we add N_k and N_j , in that order, onto the stack; or the rule is $N_i \to a$, and we check that a is the current letter in p, in which case we advance the pointer inside p.

The algorithm runs in time $O(|p| \cdot |P|)$, and needs space $O(\log |p| + \log |P|)$ to memorize the pointers inside p and inside P.

Next we consider *combined patterns* of the form $p_0 \star p_1 \star \cdots \star p_m$ where the p_i are words and where \star means "any substring". Such combined patterns can express problems like "does p occur inside the text?" (by picking m=2, $p_0=p_m=\varepsilon$ and $p_1=p$), and "is p a subword of the text?" (for p of the form $a_1 \ldots a_n$, one picks m=n+1, $p_0=p_m=\varepsilon$ and $p_i=a_i$ for $1 \leq i \leq n$).

Theorem 6 Deciding whether w(P) matches a combined pattern $p_0 \star \cdots \star p_m$ is in LOGCFL.

PROOF. [Sketch] We proceed as in the previous Theorem, as if we were checking that w(P) equals $p_0 \star \cdots \star p_m$. The difference is that, when we are matching a \star inside the pattern, we just discard letters from w(P). This is achieved by nondeterministically guessing the (occurrences of) non-terminals we won't have to develop when we pop them from the stack. Note that this runs in polynomial-time since we can discard a useless (occurrence of a) non-terminal as soon as possible, i.e. before expanding it. (Expanding it and discarding its expansion would require exponential-time.) One concludes with the characterization LOGCFL = $\mathbf{AuxPD\text{-}NSPACE}(\log n, \operatorname{pol} n)$ from [2,12].

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