P4-NetFPGA-based network slicing solution for 5G MEC architectures

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Abstract—Network Slicing is one of the fundamental capabilities of the new Fifth-generation (5G) networks. It is defined as several logical networks that are created to fulfil specific Quality of Service (QoS) and Quality of Experience (QoE) requirements and are available over the same physical infrastructure. This paper proposes a novel extension to P4-NetFPGA framework to achieve network slicing between different 5G users in the edge-to-core network segment. This solution provides hardware-isolation of the performance in terms of bandwidth, latency and packet loss of 5G network traffic. The work proposed has been validated in a real 5G infrastructure.

Index Terms—5G mobile networks, P4, NetFPGA, FPGA, Network Slicing

I. INTRODUCTION

This new generation of mobile network communications requires to shift the current "best-effort" approach to an approach that allows different vertical businesses to address ambitious use cases in terms of ultra-reliable low-latency communications (uRLLC), enhanced mobile broadband (eMBB), massive internet of things (mIoT) and massive machine-type communications (mMTC). In this context, network slicing is required to allow the creation of different logical networks that guarantee the Service Level Agreements (SLAs) of the ambitious KPIs required by the vertical businesses.

This paper demonstrates a novel P4-based framework which allows the definition and control of the slicing in a 5G Mobile Edge Computing (MEC) architecture, guaranteeing the complete isolation of 5G network traffic while fulfilling the ambitious 5G end-to-end latency KPI, defined as <1ms for critical traffic and <10ms for normal traffic [1]. The definition of network slicing has been based on an extended usage of the P4 language. The proposed system is based on a NetFPGA [2] network card and the NetFPGA-SUME project [3] and provides an extension to support network slicing. A functional prototype has been deployed in a real 5G infrastructure,

carrying out an exhaustive empirical evaluation to demonstrate the scalability and reliability of the system.
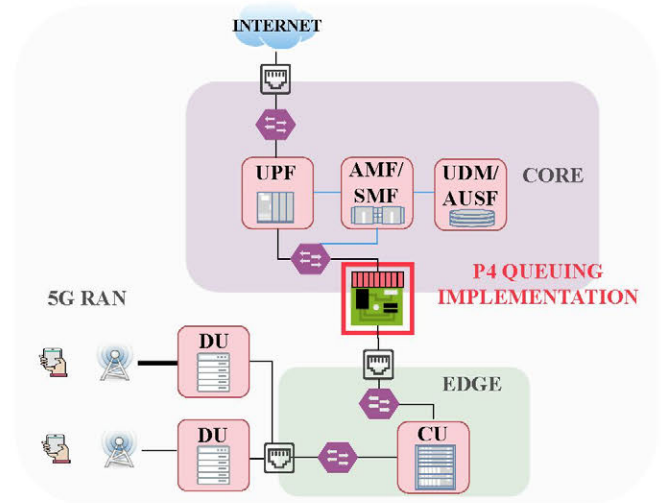

Fig. 1. 5G Edge to Core Architecture based on a P4 queuing implementation.

II. QoS-AWARE 5G ARCHITECTURE OVERVIEW

The architecture shown in Figure 1 is clearly divided in three parts; i) 5G RAN with antennas and 5G users connected to such antennas; ii) Edge with CUs to allow users mobility; and, iii) Core with the data centre hosting the core of the network. Our solution allows us to define QoS network slicing over the 5G network traffic exchanged between Edge and Core. In this edge-to-core network segment, there is a General Packet Radio Service (GPRS) Tunneling Protocol (GTP) encapsulation to establish a tunnel between User Equipments (UEs) and Core Network. This tunnel is required to achieve user mobility across the different Distributed Units (DUs) of the architecture. Traditional network cards do not provide support for GTP and this research is an step forward to overcome such limitation,

while also providing the possibility to define network slices over such traffic.
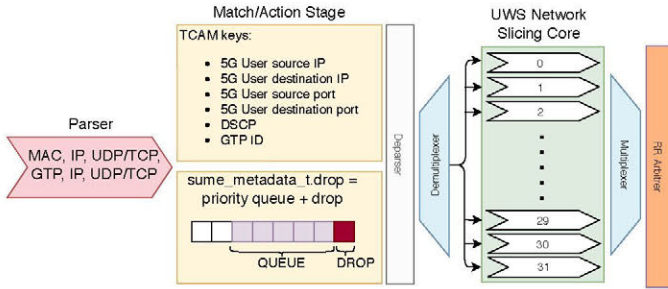
III. UWS NETFPGA-BASED CORE

Fig. 2. NetFPGA pipeline extension with UWS Network Slicing Core implemented.

A 5G flow is composed by a traditional IP flow which is encapsulated over another IP flow using a GTP header (see Parser in Fig 2).

The parser extracts the 5G flow data that will be used as part of the definition of a network slice. A network slice is defined by the following 6-tuple: 5G user source and destination IPs, 5G user source and destination ports, Differentiated Services Code Point (DSCP) and GTP Tunnel ID. The foundations of this parser stage are based on our previous publication, just in case the reader is interested [4].

The match/action stage contains a TCAM table where the previous 6-tuple have been used to define a network slice and determine the actions to be taken over such slice. This stage has been extended to allow P4 to control the new UWS Network Slicing Core. The connection between our new core and the match/action stage has been achieved by modifying the *sume metadata* structure provided by the NetFPGA-SUME project. Fig. 2 shows the field used in *sume metadata* structure to drop a network packet. This is also used to choose the priority queue where the different 5G flows are sent. This mechanism allows us to control the latency of a given network slice.

At deparser stage, packets are rebuild and relayed to the UWS network slicing core created. This core guarantees the QoS and allows the isolation of the 5G traffic.

The UWS network slicing core provides a queuing discipline composed by 32 different queues which allows the definition of 32 types of QoS. It guarantees the complete isolation of the traffic in each queue. These queues have differentiated priorities, with queue 31 receiving the highest priority traffic and queue 0 the lowest. The queuing discipline is based on a priority-based algorithm where a queue with less priority will not be processed until the queues with higher priority are empty, enforcing an strict priority-based discipline.

The novel P4 pipeline is fully controlled by a software API which allows the insertion and deletion of rules in the TCAM table. The match aspect of the rule corresponds to the 6-tuple previously defined. The action aspect of the rule determines the queue ID. That ID is received in the pipeline when a flow matches with a rule of the table. Thus, the API provides the administrator of the network with full control of the queuing discipline implemented and also, the possibility to define up to 32 types of QoS for the 5G traffic that is being processed.

IV. EXPERIMENTAL VALIDATION

The solution proposed has been validated in a 5G edge-to-core infrastructure fully deployed in our data centre. This validation has been performed by sending traffic from 32 separate user equipments (UE) simultaneously and defining 32 different types of QoS by using 32 rules. The traffic sent congested the network. The framework under such circumstances achieved full isolation between the traffic transmitted by the different UEs in terms of latency, packet losses and bandwidth. The results obtained in terms of end-to-end latency have been under 0.5ms for the highest priority queues and under 3ms for the lowest priority queues in an end-to-end communication. These results achieved the ambitious 5G KPIs in terms of end-to-end latency, defined as ¡1ms and ¡10ms for critical and non-critical traffic [1].

V. CONCLUSIONS

This paper has proposed a novel QoS-aware network slicing solution based on P4-NetFPGA to meet the SLA requirements for network slicing. This solution guarantees total isolation between the different 5G users of the network based on the SLA established by the network operator and it fulfils the KPIs required to make our framework suitable for 5G networks.

Future work will analyse the different queuing algorithms in an attempt to find the most efficient and fair system. The work can be further extended to allow multi-tenancy scenarios which are a key feature to reduce capital and operational costs for telecommunication industries.

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