# **Oblivious program execution and path-sensitive non-interference**

Jérémy Planul Stanford University John C Mitchell Stanford University

Abstract-Various cryptographic constructions allow an untrusted cloud server to compute over encrypted data, without decrypting the data. However, this prevents the cloud server from branching according to encrypted values. We study the constraints imposed by this important scenario by formulating and solving an equivalent information-flow problem, based on assuming an adversary could observe the control path. We develop a type system that prevents control-path information leaks, prove soundness, and compare with traditional implicit information-flow. Because simply preventing programs that leak information severely restricts the language, we define alternate (and easily implemented) semantics that execute multiple paths and combine the results using data operations. This produces a termination problem which we address with a more refined type system that characterizes a useful class of obliviously executable programs. We prove fundamental results about this language, semantics, and type system and conclude by comparing with traditional timing-based information-flow.

# I. INTRODUCTION

Cloud computing poses new security challenges [IDC, Chow et al., 2009] because programs may manipulate sensitive data on an untrusted or partially trusted platform. Modern cryptography such as homomorphic encryption [Gentry, 2009, Gentry, 2010, van Dijk et al., 2010] and secure multiparty computation [Ben-Or et al., 1988, Gennaro et al., 1998, Cramer et al., 2000, Naor and Nissim, 2001] may be used to compute on encrypted data without access to the decryption key (or associated process) that protects the data. However, if the program branches on an encrypted value, the cloud platform cannot determine which branch to execute because that depends on a hidden plaintext value. This language restriction can be formulated as an informationflow requirement: control-flow is public information and therefore cannot depend on private data.

In this paper, we formulate and study the informationflow problem associated with public branching on private data, which is distinct from traditional explicit and implicit information flow [Sabelfeld and Myers, 2003]. We develop a type system that prevents control-path information leaks, prove soundness, and compare with traditional implicit information-flow. Because simply preventing programs that leak information severely restricts the language, we define alternate (and appropriately implemented) semantics that execute multiple paths and combine the results using data operations. This produces a termination problem which we address with a more refined type system that characterizes a useful class of *obliviously executable* programs. We prove fundamental results about this language, semantics, and type system and conclude by comparing with traditional timingbased information-flow. This study produces an implemented language for computing over encrypted data that improves on previous solutions (*cf.* [Bain et al., 2011, Mitchell et al., 2012]).

As an illustrative setting providing both functional and imperative constructs, we work with the standard semantics of lambda calculus with references, augmented with path annotations representing the information available to an adversary that can tell which execution path is taken. Note that this problem of control-path information leaks has been studied before, with similar ideas, but with an imperative language [Molnar et al., 2006, Agat, 2000]. We consider standard information-flow typing concepts [Sabelfeld and Myers, 2003, Pottier and Simonet, 2003, Volpano et al., 1996] with security labels indicating high or low confidentiality. In a secure program, high-confidentiality values must not influence the control path or any observable result. This condition is formalized as a form of noninterference [Goguen and Meseguer, 1982].

Our initial candidate solution begins with a conventional information-flow type system (similar to the one of flowcaml [Pottier and Simonet, 2003]), for which we prove soundness for IO non-interference. We then extend the type system to prevent path information-flow leaks and prove soundness for path non-interference. Just as implicit information-flow can be reduced to explicit flow by a program transformation Sabelfeld and Myers [2003], we also define a program transformation that makes path leakage explicit, reducing path information-flow to IO informationflow.

Since simply preventing path information leakage reduces programming language expressiveness severely, we present an alternate semantics that effectively executes multiple paths and combines the results using data operations. This semantics is implementable with the correct informationflow properties (see, e.g., [Mitchell et al., 2012]) using homomorphic encryption or secure multiparty computation because such data operations hide the results of each path from the execution platform. We prove that this semantics has a weak confluence property we call *convergence*, implying that the semantics of a program is well-defined and equivalent in output to the standard semantics (but with fewer path-flow leaks).

While the alternate semantics reduces path information leaks, it introduces another problem, namely termination of loops. If loop termination depends on a high confidentiality secret, then the loop *necessarily* fails to terminate because the semantics requires infinitely many paths to be executed. We solve this problem with a type system that allows arbitrary dependence on low confidentiality values, but only primitive recursion on high confidentiality values. This extends and improves on Mitchell et al. [2012], which proposes a similar but more restrictive language, explores its implementation with secure multiparty computation or homomorphic encryptions, but does not consider the more general language properties and analysis presented here. We prove that well-typed programs under this discipline terminate in the new semantics whenever they would terminate under the standard semantics and prove path-flow non-interference.

As a final contribution, we investigate the connection between path-flow information leakage and timing channels. We formalize timing leakage for our language and prove that our system preventing path-flow leakage also prevents timing leakage.

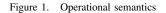
In Section II, we define our syntax and semantics. In Section III, we define our information-flow settings, and give some weak information-flow results (this section is standard and can be skipped by informed readers). In Section IV, we examine path information-flow problem, and propose our two solutions. In Section V, we acknowledge and solve our termination problem. In Section VI, we remark that we incidentally gave a solution for other implicit flows problems (e.g. timing channels). In Section VII, we show how our solution can be used for secure computation.

# II. PROGRAMS AND THEIR SEMANTICS

In this section, we define our language and its semantics. The semantics has annotations that reveal the control-flow of the program (i.e., which path of the computation has been taken). We use those annotations in the sections below to formalize the leak of information.

A  $\lambda$ -calculus with references: The language is a typed call-by-value  $\lambda$ -calculus with booleans, an error value, conditionals, and references. We use the conditional as a way to express observable control-flow branching in a program, and we use the error value as a default value for uninitialized memory references. For general background on the  $\lambda$ -calculus, see Mitchell [1996], Pierce [2002].

where k range over 0, 1, and a range over a set Ref of references values. Values consist of variables, references, booleans, an error value, or lambda abstractions.



We write V for the set of values. Expressions consist of values, conjunctions, disjunctions, negations, functions applications, conditionals, references allocations, dereferences, or assignments. We adopt standard notions of free and bound variables, of closed expressions, and of the captureavoiding substitution e[e'/x] of an expression e' for all free occurrences of a variable x in an expression e. We write  $e_1; e_2$  as syntactic sugar for  $(\lambda x.e_2) e_1$  where x is not free in  $e_2$ . We write If e then  $e_1$  else  $e_0$  as syntactic sugar for  $(\lambda x.$  If x then  $e_1$  else  $e_0)$  e where x is not free in  $e_0$  and  $e_1$ . Intuitively, the error value  $\perp$  can have any type, and direct computations with an error results in an error. However, only branches taken by the expression can result in errors. For instance If 1 then e else  $\perp$  does not result in an error. The reduction of |f v| then e else e is annotated with the value v, as a visible path-flow. In the following, we only consider values and expressions that are simply typed (we omit the standard  $\lambda$ -calculus typing rules).

Semantic: A memory is a mapping  $\mu$ : Ref  $\rightarrow \mathbb{V}$  that sends references to values of our language. A configuration is a pair  $\langle e, \mu \rangle$  with  $\mu$  a memory and e an expression. The small step semantics is a labeled transition relation  $\xrightarrow{\alpha}$ (where  $\alpha$  is 0, 1, or nothing). Basic rules for the semantics are defined in Figure 1 (with dom( $\mu$ ) =  $\{a | \mu(a) \neq \bot\}$ ) and context rules are defined in Figure 2. (We omit the rules of computation with  $\bot$ .) Most of these rules are standard except the rules for the conditional, which annotate the transition with a value corresponding to the path taken. Hence, the control-flow of the program execution is visible on the trace. Those trace annotations are a model for when the control-flow is visible by the host.

*Example 1:* Let a be a reference, we consider the expression If !a then 0 else 0, and we consider the two initial memories  $\mu_0 = [a \mapsto 0]$  and  $\mu_1 = [a \mapsto 1]$ . We have

$$\langle \text{If } | a \text{ then } 0 \text{ else } 0, \mu_0 \rangle \xrightarrow{0} \langle 0, \mu_0 \rangle$$
  
 $\langle \text{If } | a \text{ then } 0 \text{ else } 0, \mu_1 \rangle \xrightarrow{1} \langle 0, \mu_1 \rangle$ 

The two final results are equal, but the two traces are different.

$$\frac{\langle e_1, \mu \rangle \xrightarrow{\alpha} \langle e'_1, \mu' \rangle}{\langle e_1 e_2, \mu \rangle \xrightarrow{\alpha} \langle e'_1 e_2, \mu' \rangle} \qquad \frac{\langle e_2, \mu \rangle \xrightarrow{\alpha} \langle e'_2, \mu' \rangle}{\langle v_1 e_2, \mu \rangle \xrightarrow{\alpha} \langle v_1 e'_2, \mu' \rangle}$$

$$\frac{\langle e, \mu \rangle \xrightarrow{\alpha} \langle e', \mu' \rangle}{\langle \text{ref } e, \mu \rangle \xrightarrow{\alpha} \langle \text{ref } e', \mu' \rangle} \qquad \frac{\langle e, \mu \rangle \xrightarrow{\alpha} \langle e', \mu' \rangle}{\langle !e, \mu \rangle \xrightarrow{\alpha} \langle !e', \mu' \rangle}$$

$$\frac{\langle e_1, \mu \rangle \xrightarrow{\alpha} \langle e'_1, \mu' \rangle}{\langle e_1 := e_2, \mu \rangle \xrightarrow{\alpha} \langle e'_1 := e_2, \mu' \rangle}$$

$$\frac{\langle e_2, \mu \rangle \xrightarrow{\alpha} \langle e'_2, \mu' \rangle}{\langle v_1 := e_2, \mu \rangle \xrightarrow{\alpha} \langle v_1 := e'_2, \mu' \rangle}$$

Figure 2. Context rules

We write  $\langle e, \mu \rangle \xrightarrow{\gamma}^* \langle e', \mu' \rangle$  with  $\gamma = \alpha_0 \alpha_1 \dots \alpha_n$  for the corresponding big-step semantics, i.e., when

$$\langle e, \mu \rangle \xrightarrow{\alpha_0} \langle e_1, \mu_1 \rangle \xrightarrow{\alpha_1} \dots \xrightarrow{\alpha_n} \langle e', \mu' \rangle$$

for some  $\alpha_0, \alpha_1, \ldots, \alpha_n$  and  $e, e_1, \ldots, e'$ . When there exists no  $\alpha$ , e', and  $\mu'$  such that  $\langle e, \mu \rangle \xrightarrow{\alpha} \langle e', \mu' \rangle$ ; we say that  $\langle e, \mu \rangle$  is irreducible (in this semantics, this appends when eis a value).

## **III. IO INFORMATION-FLOW**

We now discuss our security policies. We annotate our types with security labels, and consider the security of our programs given an environment that specifies the types of the free references. This specifies the security intent; for instance, free references with a private type are supposed not to influence public references (or in the case of path non-interference, the control-flow). We then define the corresponding security statement in terms of different types of non-interference (IO and path non-interference) and see how this control-flow leak is detrimental to security. If we exclude the definition of path non-interference, this section is mostly standard.

Security types: We first discuss our security types:

$$\begin{array}{lll} \ell & ::= & L \mid H \\ t & ::= & bool \mid \tau \text{ ref} \mid (\tau \xrightarrow{\ell} \tau) \\ \tau & ::= & t^{\ell} \end{array}$$

We consider public (L) and private (H) security labels, and a reflexive relation  $\leq$  such that  $L \leq H$  but  $H \not\leq L$  ( $\sqcup$  and  $\sqcap$  are the associated least upper bound and greatest lower bound on security labels). *Types* (t) consist of booleans, references, and abstractions. References recursively contain the security type of the value they refer to. Abstractions specify the security type of the value they take as input, the security type of the resulting application, and an additional label to specify their pc, i.e. a lower bound on the label of the references they modify. For instance, a function of type  $\tau \xrightarrow{H} \tau'$  is guaranteed to never write on public references. Security types  $(\tau)$  are labeled types, for instance a value of type  $(bool^H \text{ ref})^L$  is public reference to a confidential boolean. We extend  $\leq$  on security types by saying that  $\ell \leq t^{\ell'}$  when  $\ell \leq \ell'$ . We let  $t^{\ell} \sqcup \ell'$  stands for  $t^{\ell \sqcup \ell'}$ .

*Environment:* We now define security policies, which give security types to the free references of the expression. More generally, we define *environments*  $\Gamma$  as functions from variables and references to security types ( $\tau$ ). These environments serve for initial security policy and are also used in our type system where they specify types for bound variables and references.

*Memory typing:* We want to consider expressions containing free reference names and running on initialized memories, hence we need a way to specify when a particular memory corresponds to the environment. When the environment states that a memory reference contains a boolean, it suffices to check that it is indeed the case. However, the environment may also state functional information-flow types or reference types. When this is the case, we check it with an information-flow typing judgment  $\Gamma \vdash v : \tau$ . Our judgment for memory typing is parametric in  $\vdash$  (since typing judgments have not yet been defined). It is of the form  $\Gamma \models \mu$ , and is defined inductively as follows:

$$\frac{\Gamma' \models \mu \qquad \Gamma \vdash v : \tau \qquad v \neq \bot}{\Gamma'; a : \tau \models \mu[a \mapsto v]} \qquad \emptyset \models \forall a.a \mapsto \bot$$

For a given  $\Gamma$ , the first statement says that, if a memory  $\mu$  is correctly typed for some partial environment  $\Gamma'$  (typically,  $\Gamma'$  is a subset of  $\Gamma$ ), and an additional value v types (with judgment  $\vdash$ ) in the initial environment  $\Gamma$  at level  $\tau$ , then the memory enriched with this value at a reference a types in the environment  $\Gamma'$  enriched with reference a at type  $\tau$ . The second statement ensures that memory references that don't appear in the environment contain the default error value. When  $\Gamma \models \mu$  for  $\Gamma$  and  $\vdash$ , we say that  $\mu$  is a typed memory for  $\vdash$  and  $\Gamma$ . When it is clear from the context, we may omit  $\vdash$  or  $\Gamma$ .

*Low-equalities:* Once a security policy is defined, it is possible to partially compare two memories relatively to this policy. For example, if we determine that two memory are equal for public references, it is not possible distinguish them by accessing only the public references. Formally, let  $\Gamma$  be an environment; let  $\mu_0$  and  $\mu_1$  be two memories. We say that  $\mu_0 =_L \mu_1$  when, for every reference *a* such that  $\Gamma(a) = t^L$ , we have  $\mu_0(a) = \mu_1(a)$ . We also define a weaker equality that only examines boolean constants. We say that  $\mu_0 =_{bool-L} \mu_1$  when, for every reference *a* such that  $\Gamma(a) = bool^L$ , we have  $\mu_0(a) = \mu_1(a)$ . This is useful to avoid some complications of non-interference in a functional setting (for instance when two functions are semantically equivalent, but syntactically not equal), which are not the focus of this article. *Non-interference:* For IO non-interference (standard non-interference), an expression is considered secure when the initial values of the privates references do not influence the final values of the public references. In other words, an expression is secure when its execution on two initial memories with the sames values in public references leads to two final memories with the same values in public reference, in which the control-flow channel is considered public. In this case, an expression is considered secure when the initial values in privates references do not influence the final values in public references or the semantics path annotations.

Definition 1: Let  $\Gamma$  be an environment,  $\ell$  a label, and e an expression. We say that e is IO non-interferent for  $\vdash$  when, for every typed memories  $\mu_0$  and  $\mu_1$  such that  $\mu_0 =_L \mu_1$ , and for every expressions  $e_0$  and  $e_1$ , memories  $\mu'_0$  and  $\mu'_1$ , and traces  $\gamma_0$  and  $\gamma_1$  such that

and both  $\langle e_0, \mu'_0 \rangle$  and  $\langle e_1, \mu'_1 \rangle$  are irreducible, we have  $\mu'_0 =_{bool-L} \mu'_1$ . If, additionally,  $\gamma_0 = \gamma_1$ , we say that *e* is *path* non-interferent.

We may omit  $\vdash$  when it is obvious from the context.

*Type system for non-interference:* As a starting point, we equip our language with a type system that enforces IO non-interference. Typing judgments for values are of the form  $\Gamma \vdash v : \tau$ . Typing judgments for expressions are of the form  $\ell, \Gamma \vdash e : \tau$ . The typing rules for commands appear in Figure 3. This type system is similar to those typically used for non-interference [see e.g. Sabelfeld and Myers, 2003].

The judgment  $\ell, \Gamma \vdash e : \tau$  guarantees that e is of type  $\tau$ , that it only writes references with a level higher than  $\ell$  (sometimes called its 'program counter' level), and that it is IO non-interferent. In the application rule, we match the input type of the abstraction with the type of the input, we also need to make sure that the level of the abstraction flows to the the pc of its arrow type. In the conditional rule, we ensure that the guard is not more confidential than the pc of the branches. In the assignment rule, we ensure that the types matches, and that the assigned reference is taken account of in the pc of the expression.

The judgment  $\Gamma \vdash v : \tau$  guarantees that v is of type  $\tau$ . Boolean constants can be seen as of type  $bool^L$ . Reference names and variables get their types from the environment. For abstractions, if we can type the abstracted expression in an environment with the abstraction variable at some type, we get an arrow type linking both types; the arrow is annotated with the pc used to type the expression, thus specifying a lower bound on the level of variables accessed when applying the abstraction.

# Relations between types

$$\frac{\ell \leq \ell' \quad t \leq t'}{t^{\ell} \leq t'^{\ell'}} \qquad \qquad \frac{\tau'_0 \leq \tau_0 \quad \tau_1 \leq \tau'_1 \quad \ell' \leq \ell}{\tau_0 \stackrel{\ell}{\to} \tau_1 \leq \tau'_0 \stackrel{\ell'}{\to} \tau'_1}$$

# Type judgments for values

$$\Gamma \vdash k: bool^{L} \qquad \Gamma \vdash \bot: \tau \qquad \frac{\Gamma \vdash v: \tau \quad \tau \leq \tau'}{\Gamma \vdash v: \tau'}$$

$$\begin{split} \frac{\Gamma(a) = \tau}{\Gamma \vdash a : \tau \ \mathrm{ref}^L} & \Gamma \vdash x : \Gamma(x) & \frac{\ell, \Gamma; x : \tau \vdash e : \tau'}{\Gamma \vdash \lambda x. e : (\tau \xrightarrow{\ell} \tau')^L} \\ \frac{\ell, \Gamma; x : \tau; f : (\tau \xrightarrow{\ell} \tau')^{\ell'} \vdash e : \tau'}{\Gamma \vdash \mathrm{fix} \ f. \lambda x. e : (\tau \xrightarrow{\ell} \tau')^{\ell'}} \end{split}$$

# Type judgments for expressions

$$\begin{split} \frac{\ell, \Gamma \vdash e: \tau}{\ell', \Gamma \vdash e: \tau'} & \frac{\Gamma \vdash v: \tau}{H, \Gamma \vdash v: \tau} \\ \frac{\Gamma \vdash v: bool^{\ell}}{H, \Gamma \vdash v \wedge v': bool^{\ell}} \\ \frac{\Gamma \vdash v: bool^{\ell}}{H, \Gamma \vdash v \wedge v': bool^{\ell}} & \frac{\Gamma \vdash v: bool^{\ell}}{H, \Gamma \vdash v \vee v': bool^{\ell}} \\ \frac{\Gamma \vdash v: bool^{\ell}}{H, \Gamma \vdash v \vee v': bool^{\ell}} & \frac{\Gamma \vdash v: bool^{\ell}}{H, \Gamma \vdash v : bool^{\ell}} \\ \frac{\ell, \Gamma \vdash e: (\tau' \xrightarrow{\ell'} \tau)^{\ell''}}{\ell \sqcap \ell', \Gamma \vdash e e': \tau \sqcup \ell''} & \frac{\ell, \Gamma \vdash e: \tau'}{\ell \sqcap \ell', \Gamma \vdash e e': \tau \sqcup \ell''} \\ \frac{\Gamma \vdash v: bool^{\ell}}{\ell, \Gamma \vdash ref e: t^{\ell}} & \frac{\ell, \Gamma \vdash e: t}{\ell, \Gamma \vdash e: t^{\ell'} \sqcup t''} \\ \frac{\ell, \Gamma \vdash e: t^{\ell}}{\ell, \Gamma \vdash ref e: t^{\ell'} ref^{\ell'}} & \frac{\ell, \Gamma \vdash e: t^{\ell'} ref^{\ell''}}{\ell, \Gamma \vdash e: t^{\ell' \sqcup \ell''}} \\ \frac{\ell, \Gamma \vdash e: t^{\ell'} ref^{\ell'}}{\ell \sqcap \ell', \Gamma \vdash e: t^{\ell'} \vdash t''} \end{split}$$

Figure 3. Type system for IO non-interference

$\llbracket f v \text{ then } e_1 \text{ else } e_0  rbracket_b$	=	If $v$ then $\llbracket e_1 \rrbracket_b; b := 1$
		else $[\![e_0]\!]_b; b := 0$
$\llbracket e \ e'  rbracket_b$	=	$[\![e]\!]_b \ [\![e']\!]_b$
$[\![\lambda x.e]\!]_b$	=	$\lambda x. \llbracket e \rrbracket_b$
$\llbracket fix f.\lambda x.e \rrbracket_b$	=	fix $f.\lambda x.\llbracket e \rrbracket_b$
$\llbracket ref \ v \rrbracket_b$	=	ref $v$
$\llbracket !v \rrbracket_b$	=	!v
$\llbracket v := v' \rrbracket_b$	=	v := v'
$\llbracket x \rrbracket_b$	=	x

Figure 4. Transformation for explicit leak

Theorem 1: Let  $\Gamma$  be an environment, let e be an expression, let  $\tau$  be a security type, and  $\ell$  a label such that  $\ell, \Gamma \vdash e : \tau$ . Then e is IO non-interferent.

#### **IV. PATH INFORMATION-FLOW**

The above type system does guarantee IO noninterference but not path non-interference. In the example below, we show a typed expression (hence IO noninterferent) that branches on a private reference, and thus is not path non-interferent.

*Example 2:* Let h be a reference. We consider the expression If !h then 0 else 0 and the environment  $\Gamma = h : bool^H$ . We have  $H, \Gamma \vdash$  If !h then 0 else 0 : H. However, if we consider the two low-equal memories  $\mu_0 = [h \mapsto 0]$  and  $\mu_1 = [h \mapsto 1]$ , we have

The two final memories are low-equal, but the two traces are different; the expression is not path non-interferent.

Conditional leak the value of their guard in the trace. Hence, we can secure the type system by replacing the conditional rule with a rule that disallows private guards (as previously observed by Smith and Volpano [1998]):

$$\frac{\Gamma \vdash v: \mathsf{bool}^L \quad L, \Gamma \vdash e_1: \tau \quad L, \Gamma \vdash e_0: \tau}{L, \Gamma \vdash \mathsf{lf} \ v \ \mathsf{then} \ e_1 \ \mathsf{else} \ e_0: \tau}$$

The resulting type system guarantees path non-interference, but fewer expressions can be typed.

Theorem 2: Let  $\Gamma$  be an environment, let e be an expression, let  $\tau$  be a security type, and  $\ell$  a label such that  $\ell, \Gamma \vdash e : \tau$  in the type system with the more restrictive conditional rule. Then e is path non-interferent.

*Transformation:* As an alternative, it is also possible to detect expressions that leak information through their control-flow by making the leak explicit. In Figure 4, we describe such a transformation, in which we add a new public reference that explicitly records the control-flow leak. In the transformed expression, the control-flow now affects our public reference; hence, if the transformed expression is

non-interferent, then no private information influences the control-flow. Thus, the original expression must be path non-interferent. In the theorem below, we show that, if a transformed expression types in the standard information-flow type system (i.e., is IO non-interferent), then the original expression is path non-interferent.

Theorem 3: Let e be an expression, let b be a fresh reference name, let  $\Gamma$  be an environment such that  $\Gamma(b) = bool^L$ , let  $\tau$  be a security type, and  $\ell$  a label such that  $\ell, \Gamma \vdash [\![e]\!]_b : \tau$  in the original type system. Then e is path non-interferent.

Still, this approach has the same effect of disallowing private guards, and accepts exactly the same reduced set of expressions than the type system with the restrictive conditional rule.

Bypassing control-flow leaking: Formally, it is sometimes possible to rewrite programs to avoid the leak itself. For instance, if the two branches of a conditional are boolean constants, it is possible to implement it using basic operations, thus denying the leak. For example, let v,  $k_1$ and  $k_0$  be booleans, lf v then  $k_1$  else  $k_0$  gives the same result than  $(v \wedge k_1) \lor (\neg v \wedge k_0)$  but avoids the control-flow leak. Instead of transforming the original expression, we can achieve a similar result in the semantics by adding a rule

(If v then 
$$k_1$$
 else  $k_0, \mu$ )  $\rightarrow (v \wedge k_1) \lor (\neg v \wedge k_0)$ 

In practice, we can see the conditional (and its path leak) in our model as a way for the programmer to specify that requests on any of the two values is observable by the host (for instance, if they are stored on a remote machine). Hence, a request on one of the two values reveal the branch taken. In this case, the rewritten program requests both values, and thus avoid any path leak (similarly with the additional semantic rule). We can also draw parallels with homomorphic operations on encryptions. Homomorphic operations are computations done on encrypted plaintexts without reading the plaintexts themselves, and thus without revealing any information on those plaintexts. In such settings, it is not possible to do a generic conditional homomorphicaly since it would leak the control-flow, but it is possible to implement it on constants as above using basic operations.

This extended semantics works when the branches of a conditional are booleans, but not when the branches are expressions, references, or functions. In pure lambda calculus, the order of evaluation is irrelevant; with such a property, we could reduce the branches, and, in some cases, transform the conditional using the semantic rule above. However, this is not the case in lambda calculus with references since operations on references are only executed on the valid side of the conditional. Still, if we allow some expressions in memory (not only values), it is possible to have correct nested evaluations. For instance, when some reference a is updated on one side of a conditional, we replace the expression for a in the memory by a conditional expression on the updated value and the previous expression for a.

Even with nested evaluation, we still cannot safely (avoiding control-flow leaks) reduce conditionals of non-boolean types to values. However, we can adapt our semantics to reduce expressions containing such conditionals. For instance, if a dereference is applied on a conditional of references, we can safely distribute the dereference on the conditional. We can similarly safely distribute assignments and reference allocations. Similarly, when the branches values are lambda abstractions, we can safely factorize the abstraction over the conditional. Conditional trees of references are still irreducible (if we want to avoid control-flow leaks), hence, if we want to safely reduce all expressions that are not reference-typed to values, we need to allow the application of functions to those conditional trees of references (and not only values). Here is how we define those trees of references:

a is a tree of references (for all reference a)

$$e_1, e_0$$
 are trees of references  
If v then  $e_1$  else  $e_0$  is a tree of references

We now formally extend stores so that they can contain pure expressions, and not only values. We say that an expression e is pure when it contains no reference allocation, dereference, assignment, or application:

v is pure 
$$\frac{e_1, e_0 \text{ are pure}}{|f v \text{ then } e_1 \text{ else } e_0 \text{ is pure}}$$

The main rule for store typing becomes

$$\frac{\Gamma' \models \mu \quad \Gamma \vdash e : \tau \quad e \text{ is pure}}{\Gamma; l : \tau \models \mu[l \mapsto e]}$$

Then, in Figure 5, we describe our extension to the operational semantics. In Rules (Cond-L) and (Cond-R), we describe how to safely reduce expressions inside a conditional. To this end, we check the result of one step of reduction in one of the expression, then we update the memory with conditionals referring the two possible states (depending on the conditional branch). Rule (Mem) is for reductions in memory. Rule (App) is for application on trees of references. Rule (Fact- $\lambda$ ) is for factoring lambda abstractions on conditionals. Rule (Fact-f) is for factoring fixpoints on conditionals. Rule (Distr-d) is for distributing dereferences on conditionals. Rule (Distr-al) is for distributing assignments on the right of conditionals. Rule (Distr-ar) is for distributing assignments on the left of conditionals. Rule (Distr-r) is for distributing references declaration on conditionals. Rule (No-flow) is for reducing conditionals in arithmetic operations.

Convergence: This new semantics is not deterministic anymore, but it is convergent on pure memories for expressions that reduce to booleans (expressions that reduce to functions might get different, although equivalents, results).

Theorem 4: Let  $\mu$  be a pure memory. Let e be an expression. If  $\langle e, \mu \rangle \xrightarrow{\gamma_0}^* \langle e_0, \mu_0 \rangle$ , then,  $\mu_0$  is pure. Also, if

 $\langle e, \mu \rangle \xrightarrow{\gamma_1}^* \langle e_1, \mu_1 \rangle \xrightarrow{\gamma'_1}^* \langle k, \mu'_1 \rangle$ , then, there exists  $\mu'_0$ , and  $\gamma'_0$  such that  $\langle e_0, \mu_0 \rangle \xrightarrow{\gamma'_0} \langle k, \mu'_0 \rangle$ .

Our additional rules do not change the result of expressions, but they do induce different semantic annotations (they change the strategy of evaluation). In this example, we show how we can use those additional rules to avoid control-flow leaks.

*Example 3:* Let h be a reference name. We consider the following execution

- $\langle \text{If } 0 \text{ then } h := 1; 1 \text{ else } h := 0; 0, \{h \mapsto 1\} \rangle$ 
  - $\rightarrow \langle \text{If } 0 \text{ then } 1 \text{ else } h := 0; 0, \{h \mapsto \text{If } 0 \text{ then } 1 \text{ else } 1\} \rangle$  $\rightarrow$  (If 0 then 1 else 0,

 $\{h \mapsto \text{If } 0 \text{ then If } 0 \text{ then } 1 \text{ else } 1 \text{ else } 0\}$ 

 $\xrightarrow{} ((0 \land 1) \lor (\neg 0 \land 0), \\ \{h \mapsto (0 \land ((0 \land 1) \lor (\neg 0 \land 0))) \lor (\neg 0 \land 0)\}$  $\rightarrow^* \langle 0, \{h \mapsto 0\} \rangle$ 

This execution is longer than an execution in the original semantics (since it executes both paths), but it gives the same result and has no annotations.

$$\begin{array}{ll} \langle \text{If } 0 \text{ then } h := 1; 1 \text{ else } h := 0; 0, \{h \mapsto 1\} \rangle \\ & \stackrel{0}{\rightarrow} & \langle h := 0; 0, \{h \mapsto 1\} \rangle \\ & \rightarrow & \langle 0, \{h \mapsto 0\} \rangle \end{array}$$

We avoid control-flow leaks of secret values when we use those extended rules on conditionals with a private guard, but we don't need to use them when the guard is public. In order to guide the usage of the extended rules (so that they are not used when the conditional is public), we add annotations on the conditionals. We consider the annotated conditionals  $If_L$  and  $If_H$ , named respectively H- and Lconditionals. From now on, we use exclusively the additional rules for H-conditionals, and exclusively the original rules for L-conditionals (except when specified otherwise). Note that this makes it so that some irreducible expressions (Htrees of references) are not values.

Theorem 5: Let  $\mu$  be a pure memory. Let e be an expression. If  $\langle e, \mu \rangle$  is irreducible, then e is a value, or e is a tree of references.

To satisfy standard notion of progress, we could consider H-trees of references as values.

# V. TERMINATION

We showed that our additional semantic rules are consistent; hence we can safely execute the program while avoiding the leaking conditional rule when the guard is private. However, since we now run additional branches, we have no guarantee that an expression that terminates with the initial rules still terminates with the extended rules, as shown in the example below.

Example 4: We consider the following expression

# If 0 then $\Omega$ else 1

Figure 5. Extension to the operational semantics

where  $\Omega$  is a non-terminating expression. This expression terminates, but only with the original semantic rules.

This problem is inherent to the idea of not leaking control flow. Intuitively, if the secret values cannot influence which branches are executed, the execution must be at least as long as the longest possible execution; if the execution is not bounded by public values, the program cannot terminate. In order to avoid this problem, we require termination of the branches of H-conditionals, and, more generally, of all expressions typed with a secret pc. In typed lambda calculus with references, non-termination can appear with two mechanisms: fixpoints (which are easy to disallow) and an encoding of fixpoint using references, as in the following expression

$$a := (\lambda x.(!a \ 1)); !a \ 1$$

It is possible to disallow this kind of encoding by including the type of the references read in the type of functions. Then, in the example above, the type of a needs to verify the following equation

$$\Gamma(a) = bool \xrightarrow{\Gamma(a)} t$$

for some t. This is not possible, hence this expression cannot be typed.

We describe a type system that ensures the termination of expressions typed with a secret pc by disallowing fixpoints and their encoding through references. This type system also enforces the annotations (L/H) on conditional branches depending on the level of the guard so that we know if we need to use the alternate semantic rules or if we can safely use the original ones. Here are our updated security types:

$$\begin{array}{lll} \Upsilon & ::= & \widetilde{\tau} \mid L \\ t & ::= & bool \mid \tau \ \mathrm{ref} \mid \tau \xrightarrow{\Upsilon} \tau \\ \tau & ::= & t^{\ell} \end{array}$$

We introduce a new type of pc  $(\Upsilon)$  that is either L or a set of security types. We let  $\Upsilon \leq \Upsilon'$  if  $\Upsilon = L$  or  $\Upsilon' \neq L$  and  $\Upsilon' \subseteq \Upsilon$  (and we define the associated least upper bound and greatest lower bound accordingly). In the updated security type for abstractions, the label is either L if the pc of the function is low (we don't need to restrict functions with a low pc since they cannot appear in H-conditionals) or the set of the types of the references it accesses. In the following, we consider environments and memory typing with these new types.

We now equip our language with a type system that enforces (together with the extended semantics rules) path non-interference and termination for expressions with a secret pc. Typing judgments for values are similar to those of the original type system. Typing judgments for expressions are of the form  $\Upsilon, \Gamma \vdash e : \tau$ , with our new pc ( $\Upsilon$ ). The typing rules appear in Figure 6. Most typing rules for values and expressions are similar to those of the type system for IO non-interference. In the typing rule for recursive functions, we now require all recursive calls to be run at a low pc (since those can cause non-termination). In the typing rule for dereference, we add the reference accessed to the pc, thus the pc of an expression is either L or the set of the references it access.

We first show that this type system does enforce termination for expressions that type with a pc that is not low.

Theorem 6: Let  $\Gamma$  be an environment, let  $\mu$  be a typed memory, let e be an expression, let  $\tau$  be a security type,

#### Relations between types

$$\frac{\ell \leq \ell' \quad t \leq t'}{t^{\ell} \leq t'^{\ell'}} \qquad \frac{\tau'_0 \leq \tau_0 \quad \tau_1 \leq \tau'_1 \quad \Upsilon' \leq \Upsilon}{(\tau_0 \xrightarrow{\Upsilon} \tau_1) \leq (\tau'_0 \xrightarrow{\Upsilon'} \tau'_1)}$$

#### Type judgments for values

$$\begin{split} \Gamma \vdash k : bool^{L} & \Gamma \vdash \bot : \tau & \frac{\Gamma \vdash v : \tau \quad \tau \leq \tau'}{\Gamma \vdash v : \tau'} \\ \frac{\Gamma(a) = \tau}{\Gamma \vdash a : \tau \ \mathrm{ref}^{L}} & \Gamma \vdash x : \Gamma(x) & \frac{\Upsilon, \Gamma; x : \tau \vdash e : \tau'}{\Gamma \vdash \lambda x. e : (\tau \xrightarrow{\Upsilon} \tau')^{L}} \\ \frac{\Upsilon, \Gamma; x : \tau; f : (\tau \xrightarrow{L} \tau')^{L} \vdash e : \tau'}{\Gamma \vdash \mathrm{fix} \ f. \lambda x. e : (\tau \xrightarrow{\Upsilon} \tau')^{L}} \end{split}$$

#### Type judgments for expressions

$$\begin{split} \frac{\Upsilon, \Gamma \vdash e: t^{\ell}}{\Upsilon', \Gamma \vdash e: \tau'} & \frac{\Upsilon \leq \Upsilon}{\chi', \Gamma \vdash e: \tau'} & \frac{\Gamma \vdash v: bool^{\ell}}{\emptyset, \Gamma \vdash v: \tau} & \frac{\Gamma \vdash v: bool^{\ell}}{\emptyset, \Gamma \vdash v \wedge v': bool^{\ell}} \\ \frac{\Gamma \vdash v: bool^{\ell}}{\emptyset, \Gamma \vdash v: \tau} & \frac{\Gamma \vdash v: bool^{\ell}}{\emptyset, \Gamma \vdash v \wedge v': bool^{\ell}} & \frac{\Gamma \vdash v: bool^{\ell}}{\emptyset, \Gamma \vdash \neg v: bool^{\ell}} \\ \frac{\Gamma \vdash v: bool^{\ell}}{\chi, \Gamma \vdash v \vee v': bool^{\ell}} & \frac{\Gamma \vdash v: bool^{\ell}}{\emptyset, \Gamma \vdash \neg v: bool^{\ell}} \\ \frac{\Gamma \vdash v: bool^{\ell}}{\Upsilon \cap \Upsilon', \Gamma \vdash e e': \tau'} & \Upsilon' = L \Rightarrow \ell = L \\ & \frac{\Gamma \vdash v: bool^{\ell}}{\Upsilon, \Gamma \vdash e e then e_{1} else e_{0}: \tau} \\ & \frac{\Upsilon, \Gamma \vdash e: T^{\ell}}{\Upsilon, \Gamma \vdash f_{\ell} e then e_{1} else e_{0}: \tau \sqcup \ell} \\ & \frac{\Upsilon, \Gamma \vdash e: T^{\ell}}{\Upsilon \cup \{T^{\ell'}\}, \Gamma \vdash e: T^{\ell} \square t^{\ell'}} \\ & \frac{\Upsilon, \Gamma \vdash v: T^{\ell} \operatorname{ref}^{\ell}}{\Upsilon, \Gamma \vdash v: t' : t'} & \Upsilon = L \Rightarrow \ell = L \\ & \frac{\Upsilon, \Gamma \vdash v: T^{\ell'} \operatorname{ref}^{\ell'}}{\Upsilon, \Gamma \vdash v: v' : t'} & \Upsilon = L \Rightarrow \ell = L \end{split}$$

Figure 6. Type system for termination and path non-interference

and let  $\Upsilon$  be a pc such that  $\Upsilon \neq L$  and  $\Upsilon, \Gamma \vdash e : \tau$ . There exists no infinite sequence of configurations  $\langle e_i, \mu_i \rangle$ and infinite sequence  $\alpha_i$  such that

$$\langle e, \mu \rangle \xrightarrow{\alpha_0} \langle e_1, \mu_1 \rangle \xrightarrow{\alpha_1} \dots$$

We now show that if an expression terminates with the standard rules, and can be typed in this type system, then it terminates when we use the additional rules for H-conditionals. This means, in particular that typed expressions terminate as often with the additional rules.

Theorem 7: Let  $\Gamma$  be an environment, let  $\mu$  be a typed memory, let e be an expression, let  $\tau$  be a security type, and let  $\Upsilon$  be a pc such that  $\Upsilon, \Gamma \vdash e : \tau$ . If there exists a sequence of configurations  $\langle e_i, \mu_i \rangle_{i \leq n}$ , and a sequence  $(\alpha_i)_{i \leq n}$  such that

$$\langle e, \mu \rangle \xrightarrow{\alpha_0} \langle e_1, \mu_1 \rangle \xrightarrow{\alpha_1} \dots \langle e_n, \mu_n \rangle$$

in the original rules, and where  $e_n$  is irreducible, then there exists no infinite sequence of configurations  $\langle e'_i, \mu'_i \rangle_i$  and infinite sequence  $(\alpha'_i)_i$  such that

$$\langle e, \mu \rangle \xrightarrow{\alpha'_0} \langle e'_1, \mu'_1 \rangle \xrightarrow{\alpha'_1} \dots$$

Last, and this is our main theorem, we show that typed expressions are path non-interferent with the additional rules.

Theorem 8: Let  $\Gamma$  be an environment, let e be an expression, let  $\tau$  be a security type, and let  $\Upsilon$  be a pc such that  $\Upsilon, \Gamma \vdash e : \tau$ . Then e is path non-interferent. when executed with the additional rules.

### VI. TIMING CHANNEL

In the section above, we offered a semantics solution for control-flow leaks. Incidentally, since the control-flow of programs does not depend on the private data anymore, the code executed does not depend on the private data either, hence our solution also closes timing channels.

In order to show that no part of the executed code depends on the private data, we now annotate every semantic rule with its own label (we omit similar labels for the extended rules):

$$\begin{array}{cccc} \langle k \wedge k', \mu \rangle & \stackrel{\wedge}{\longrightarrow} & \langle k'', \mu \rangle & \text{where } k'' = k \wedge k \\ \langle k \vee k', \mu \rangle & \stackrel{\vee}{\longrightarrow} & \langle k'', \mu \rangle & \text{where } k'' = k \vee k \\ \langle \neg k, \mu \rangle & \stackrel{\neg}{\longrightarrow} & \langle k', \mu \rangle & \text{where } k'' = k \vee k \\ \langle (\lambda x.e)v, \mu \rangle & \stackrel{\lambda}{\longrightarrow} & \langle e[v/x], \mu \rangle \\ (\text{fix } f.\lambda x.e)v, \mu \rangle & \stackrel{\text{fix}}{\longrightarrow} & \langle e[v/x][(\text{fix } f.\lambda x.e)/f], \mu \rangle \\ \text{If}_L v \text{ then } e_1 \text{ else } e_0, \mu \rangle & \stackrel{1}{\longrightarrow} \langle e_1, \mu \rangle & \text{when } v = 1 \\ \text{If}_L v \text{ then } e_1 \text{ else } e_0, \mu \rangle & \stackrel{0}{\longrightarrow} \langle e_0, \mu \rangle & \text{when } v = 0 \\ & \langle \text{ref } v, \mu \rangle & \stackrel{\nu a}{\longrightarrow} & \langle a, \mu\{a \mapsto v\} \rangle & \text{for } a \not\in \text{dom}(\mu) \\ & \langle v := v', \mu \rangle & \stackrel{!v}{\longrightarrow} & \langle 0, \mu\{v \mapsto v'\} \rangle & \text{when } v \in \text{dom}(\mu) \end{array}$$

These annotations on the behaviors of programs are very precise; still, we can show that typed programs stay noninterferent. *Time-sensitive non-interference:* Similarly to path noninterference, an expression is considered secure when the initial values of the privates variables do not influence the final values of the public variables or the semantics annotations. However, in this case, the semantic annotations carry more information. For instance, the length of the program execution, or even the commands executed or the references accessed are observable through the annotations.

Definition 2: Let  $\Gamma$  be an environment,  $\ell$  a label, and e an expression. We say that e is time non-interferent when, for every memories  $\mu_0$  and  $\mu_1$  that contain only booleans and such that  $\mu_0 =_L \mu_1$ , and for every memory  $\mu'_0$ , value  $v_0$ , and trace  $\gamma_0$  such that

$$\langle e, \mu_0 \rangle \xrightarrow{\gamma_0}^* \langle v_0, \mu'_0 \rangle$$

there exist a memory  $\mu_1',$  a value  $v_1,$  and a trace  $\gamma_1$  such that

$$\langle e, \mu_1 \rangle \xrightarrow{\gamma_1} \langle v_1, \mu_1' \rangle$$

 $\mu'_0 =_L \mu'_1$ , and  $\gamma_0 = \gamma_1$ .

We now restate our theorem:

Theorem 9: Let  $\Gamma$  be an environment, let e be an expression, let  $t^{\ell}$  be a security type, and let  $\Upsilon$  be a pc such that  $\Upsilon, \Gamma \vdash e : t^{\ell}$ . Then e is time non-interferent when executed with the extended rules.

#### VII. RELATED WORK

The work described in this paper is part of a larger project exploring computation on encrypted data. A previous paper from our group Mitchell et al. [2012] describes an implementation of oblivious computations on a simpler and more restricted language with related semantics, using either homomorphic encryption or secure multiparty computation for secrecy. The more recent compiler developed by our group takes source code corresponding to the present paper as input, typechecks it, then produces Haskell code that implements the semantics of this paper using optimized libraries (from another team collaborating with us) to execute the cryptographic operations. The implementation ensures strong correctness and security properties, subject to some assumptions on the cryptographic operations.

Our extended semantics is used in the implementation of oblivious computation because most operations can be executed without reading secret values. The only operations that need to read those values are boolean operations, since we never directly reduce a conditional with a confidential guard. It is possible to implement these operations using, for instance, homomorphic encryption or secure multiparty computation. With fully homomorphic encryption, we can execute our boolean operations on encrypted values without revealing them, so we can implement our semantics with encrypted blobs for secret values. In the secure multiparty computation case, the secrets are shared between a number of servers. The servers can compute on the original secret by computing on their share and exchanging some information. If enough servers do not collude, none of them learn anything about the secrets. Once again, we can implement our semantics using those shares for secret values.

A large part of the slowdown (presently potentially on the order of  $10^9$  for homomorphic encryption) currently comes from the cryptographic operations, but some of it comes from the principle discussed in this paper that we need oblivious executions. In related work, our team has worked on faster oblivious algorithms for specific computational problems of interest. While homomorphic encryption may be prohibitively slow for some applications, the same programs run with practical running time when executed using secret sharing for confidentiality.

Other systems for secure computations include SCET [Bogetoft et al., 2005], with focus on economic applications and secure double auctions; FairplayMP [Ben-David et al., 2008], a specification language SFDL that is converted to primitive operations on bits; Sharemind [Bogdanov et al., 2008], for multiparty computations on large datasets; VIFF [Damgård et al., 2009], a basic language embedded in Python and API to cryptographic primitives. These systems implement cryptographic protocols, without proving the more comprehensive correctness and security properties. Similarly, SMCL [Nielsen and Schwartzbach, 2007] is an imperative-style DSL implemented with secure multiparty computation, that has some correctness and security properties.

#### VIII. CONCLUSION

We have studied path-based information-flow using a lambda calculus with references whose semantics represents information leaked to an adversary that can see the execution path. Starting with a type system (similar to the one of flow-caml [Pottier and Simonet, 2003]), we establish baseline soundness for IO non-interference and consider the additional path information-flow problem. One alternative for preventing path-based leakage is our first extended information-flow type system (Theorem 2); equivalently we present a program transformation making path leakage explicit, reducing path information-flow to IO information-flow (Theorem 3).

It is relatively easy to see that simply preventing path information leakage is very restrictive – no program can branch according to any confidential value. We can provide greater expressiveness using an alternate semantics that effectively executes multiple paths and combines the results using data operations. This semantics is implementable using homomorphic encryption [Gentry, 2009, Gentry, 2010, van Dijk et al., 2010] or secure multiparty computation [Ben-Or et al., 1988, Gennaro et al., 1998, Cramer et al., 2000, Naor and Nissim, 2001] because such data operations hide the data values from the execution platform. We prove that this semantics has a weak confluence property we call *convergence*, implying that the semantics of a program is well-defined and equivalent in output to its standard semantics, with fewer path-flow leaks (Theorem 4). Because executing all paths can force nontermination when loop iteration depends on a secret value, we extend our security type system to prevent loops that necessarily do not terminate for this reason. We prove that well-typed programs under this discipline terminate in the new semantics whenever they would terminate under the standard semantics (Theorem 6) and prove path-flow non-interference (Theorem 7). Thus we have developed and proved sound a language and type system that we believe is suitable for programming in an environment where path information must be hidden from an adversary. This language is more cleanly defined, more flexible for programming (for instance, we allow functions in references), and has cleaner and more comprehensive formal analysis than previous efforts (see [Bain et al., 2011, Mitchell et al., 2012]).

In order to put our main results in perspective, we investigate the connection between path-flow information leakage and timing channels. We formalize timing leakage and prove that our system preventing path-flow leakage also prevents timing leakage (Theorem 8). Intuitively, this is to be expected when data operations do not leak data values, because timing difference arise from the differences between execution paths.

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# APPENDIX: PROOF SKETCHES

This appendix contains proof sketches for the most interresting theorem, that is Theorems 4,6-9.

# A. Proof of Theorem 4

Theorem 4 is about convergence of our original and extended rules together. In the extended rules, the evaluation of conditionals can be delayed. Hence we introduce an equivalence between expressions that contain additional conditionals.

*Definition 3:* We let  $\equiv$  be the symmetric relation on expressions and values defined by:

$$e \equiv e$$
  $\lambda x.e_k \equiv \lambda x.$  If k then  $e_1$  else  $e_0$ 

fix  $f.\lambda x.e_k \equiv$  fix  $f.\lambda x.$  If k then  $e_1$  else  $e_0$ 

$$\begin{array}{ccc} e_1 \equiv e_1' & e_0 \equiv e_0' \\ \hline \mbox{If } v \mbox{ then } e_1 \mbox{ else } e_0 \equiv \mbox{If } v \mbox{ then } e_1' \mbox{ else } e_0' \\ \hline \hline e_0 \equiv e_0' & e_1 \equiv e_1' \\ \hline e_0 \ e_1 \equiv e_0' \ e_1' & \hline \mbox{ ref } e \equiv \mbox{ ref } e' \\ \hline \hline e_0 \equiv e_0' & e_1 \equiv e_1' \\ \hline \hline e_0 \coloneqq e_1 \equiv e_0' \ \vdots = e_1' & \hline \hline \lambda x.e \equiv \lambda x.e' \\ \hline \hline \hline \hline \mbox{ fix } f.\lambda x.e \equiv \mbox{ fix } f.\lambda x.e' \\ \end{array}$$

(We omit the symmetric rules.)

This lemma is a more generic version of Theorem 4 using our equivalence.

Lemma 1: Let  $\mu_0$  and  $\mu_1$  be two pure memories such that, for every location l,  $\mu_0(l) \equiv \mu_1(l)$ . Let  $e_0$  and  $e_1$  be two expressions such that  $e_0 \equiv e_1$ . If  $\langle e_0, \mu_0 \rangle \xrightarrow{\alpha_0} \langle e'_0, \mu'_0 \rangle$ and  $\langle e_1, \mu_1 \rangle \xrightarrow{\alpha_1} \langle e'_1, \mu'_1 \rangle$  then, there exists  $e''_0, e''_1, \mu''_0, \mu''_1,$  $\gamma'_0$ , and  $\gamma'_1$  such that  $\langle e'_0, \mu'_0 \rangle \xrightarrow{\gamma'_0} \langle e''_0, \mu''_0 \rangle$ ,  $\langle e'_1, \mu'_1 \rangle \xrightarrow{\gamma'_1} \langle e''_1, \mu''_1 \rangle$ ,  $e''_0 \equiv e''_1$ , and, for every location l,  $\mu'_0(l) \equiv \mu'_1(l)$ .

**Proof of Lemma 1:** Let  $\mu_0$  and  $\mu_1$  be two pure memories such that, for every location l,  $\mu_0(l) \equiv \mu_1(l)$ . Let  $e_0$  and  $e_1$  be two expressions such that  $e_0 \equiv e_1$ . We first prove the property on pure expressions, then on all expressions. Thus, we can apply Lemma 1 on the pure expressions stored in memory when doing the proof. The proof is by induction on the size of the expressions. We omit the proof for pure expressions since its cases are a subset of the general proof. We now sketch the examination of all possible couple of reductions.

• If both reductions are memory reductions:

$$\frac{\langle e^{0}, \mu \rangle \xrightarrow{\alpha} \langle e^{0'}, \mu \rangle \quad \mu(l_{0}) = e^{0}}{\langle e_{0}, \mu \rangle \xrightarrow{\alpha} \langle e_{0}, \mu[l_{0} \mapsto e^{0'}] \rangle}$$
$$\frac{\langle e^{1}, \mu \rangle \xrightarrow{\alpha} \langle e^{1'}, \mu \rangle \quad \mu(l_{1}) = e^{1}}{\langle e_{1}, \mu \rangle \xrightarrow{\alpha} \langle e_{1}, \mu[l_{1} \mapsto e^{1'}] \rangle}$$

If  $l_0 = l_1$ , we conclude by Lemma 1 on pure expressions  $e^0$  and  $e^1$ , otherwise, we reduce  $l_1$  in  $\langle e_0, \mu[l_0 \mapsto e^{0'}] \rangle$  and conclude.

- If one of the reduction is a reduction of
  - $\begin{array}{l}
     v \wedge v', \\
     v \lor v', \\
     \neg v, \\
     \lambda x.e v, \\
     \text{ fix } f.\lambda x.e v, \\
     \text{ ref } v, \\
     v, \\
     \text{ or } v := v', \\
    \end{array}$

and the other reduction is a memory reduction, then we can carefully mirror the reductions and conclude (we may have to add or remove one reduction in !v, v := v',  $\lambda x.e v$ , and fix  $f.\lambda x.e v$  cases). If the other reduction is similar, the resulting expressions are already equivalent (except in cases  $\lambda x.e v$  and fix  $f.\lambda x.e v$  where we may need to add a step.

• There are seven possible type of reductions (numbered 1-7 in the following) from expressions of the form If k then  $e^1$  else  $e^0$ , (we assume without loss of

generality that k = 1):

 $\langle \text{If } 1 \text{ then } e^1 \text{ else } e^0, \mu \rangle \xrightarrow{1} \langle e^1, \mu \rangle$ 

 $\langle \text{If } 1 \text{ then } k_1 \text{ else } k_0, \mu \rangle \ \rightarrow \ \langle (1 \wedge k_1) \vee (\neg 1 \wedge k_0), \mu \rangle$ 

 $\begin{array}{c} \langle e^1, \mu \rangle \xrightarrow{\alpha} \langle e^{1\prime}, \mu' \rangle \\ \\ \mu''(x) = \mathrm{lf} \ 1 \ \mathrm{then} \ \mu'(x) \ \mathrm{else} \ \mu(x) \ \mathrm{for} \ x \in dom(\mu') \\ \hline \\ \overline{\langle \mathrm{lf} \ 1 \ \mathrm{then} \ e^1 \ \mathrm{else} \ e^0, \mu \rangle \xrightarrow{\alpha} \langle \mathrm{lf} \ 1 \ \mathrm{then} \ e^{1\prime} \ \mathrm{else} \ e^0, \mu'' \rangle } \end{array}$ 

$$\begin{split} & \langle e^0, \mu \rangle \xrightarrow{\alpha} \langle e^{0\prime}, \mu' \rangle \\ & \mu''(x) = \text{If } 1 \text{ then } \mu'(x) \text{ else } \mu(x) \text{ for } x \in dom(\mu') \\ \hline & \langle \text{If } 1 \text{ then } e^1 \text{ else } e^0, \mu \rangle \xrightarrow{\alpha} \langle \text{If } 1 \text{ then } e^1 \text{ else } e^{0\prime}, \mu'' \rangle \end{split}$$

 $\langle {\rm If} \ 1 \ {\rm then} \ \lambda x. e^{1\prime} \ {\rm else} \ \lambda x. e^{0\prime}, \mu \rangle \ \rightarrow \$ 

$$\langle \lambda x.$$
 If 1 then  $e^{1\prime}$  else  $e^{0\prime}, \mu \rangle$ 

$$\langle \text{If } 1 \text{ then fix } f.\lambda x.e^{1\prime} \text{ else fix } f.\lambda x.e^{0\prime}, \mu \rangle \rightarrow$$

$$\begin{array}{c} \langle \text{fix } f.\lambda x.\text{If } 1 \text{ then } e^{1\prime} \text{ else } e^{0\prime}, \mu \rangle \\ \hline & \langle e^{\prime}, \mu \rangle \xrightarrow{\alpha} \langle e^{\prime\prime}, \mu \rangle \qquad \mu(l) = e^{\prime} \\ \hline & \langle \text{If } 1 \text{ then } e^{1} \text{ else } e^{0}, \mu \rangle \xrightarrow{\alpha} \\ & \langle \text{If } 1 \text{ then } e^{1} \text{ else } e^{2}, \mu[l \mapsto e^{\prime\prime}] \rangle \end{array}$$

We can verify that

- Reduction 1 when followed by a reduction on  $e^1$ , gives the same result than reduction 3 followed by 1 and reductions on memory. If  $e_0 = e_1$ , we conclude for these reductions, otherwise, we proceed by induction on the first branches.
- Reduction 1 gives the same result than reduction 4 followed by 1 and reductions on memory.
- Reduction 1 gives the same result than reduction 2 followed by three reductions on  $\land$  and  $\lor$ .
- 1 and 5 (or 6) give equivalent expressions.
- 7 easily commutes with 1.
- If both reductions are λ applications (and similarly for fixpoints):
  - If both  $e_0$  and  $e_1$  are of the form  $(\lambda x.e) e'$ , we conclude.
  - If  $e_0$  is of the form  $(\lambda x.e) e'$  and  $e_1$  is of the form  $\lambda x.$  If k then  $e^1$  else  $e^0 e'$  (where e' is a value or a tree of references and  $e_k \equiv e$ ), we can mirror one step of  $e_0$  with two steps of  $e_1$ .
- If the two reductions are contextual, we conclude by induction.

# • If one of the reduction is of the form

 $\langle !\mathsf{If}\ v \ \mathsf{then}\ e_1 \ \mathsf{else}\ e_2, \mu\rangle \to \langle \mathsf{If}\ v \ \mathsf{then}\ !e_1 \ \mathsf{else}\ !e_2, \mu\rangle$ 

 $\langle (\text{If } v \text{ then } e_1 \text{ else } e_2) := e_3, \mu \rangle \rightarrow \langle \text{If } v \text{ then } e_1 :=$ 

$$e_3$$
 else  $e_2 := e_3, \mu$ 

 $\langle v:=({\rm If}\ v' \ {\rm then}\ e_0 \ {\rm else}\ e_1), \mu\rangle \to \langle {\rm If}\ v' \ {\rm then}\ v:=$ 

$$e_0 \text{ else } v := e_1, \mu \rangle \qquad \langle \text{ref (If } v' \text{ then } e_0 \text{ else } e_1), \mu \rangle \rightarrow$$

$$\langle \mathsf{If} \ v' \mathsf{ then ref } e_0 \mathsf{ else ref } e_1, \mu \rangle$$

and the other is contextual, we verify that we can converge by reducing the conditional.

Proof: The proof of Theorem 4 follows from Lemma 1.

# B. Proof of Theorem 6

This proof of termination is similar to the proof of termination for simply typed lambda calculus, but references add complexity. We first inductively define a notion of *reducible expressions* for a particular set of types. This notion implies termination. Then, we show some properties of this definition (conservation by reduction, and independence from the set of types). Finally, we prove that all typed expression with a high pc are reducible.

Definition 4: Let  $\Upsilon$  be a set of types. We define *reducible* expressions for this set inductively. Let e be an expression such that  $\Upsilon, \Gamma \vdash e : \tau$ . Let  $(l_i)_{i \leq n}$  be the set of locations in  $\Gamma$  such that  $\Gamma(l_i) \in \Upsilon, l \leq n$ , e is reducible for  $\Upsilon$  if:

- $\tau$  is atomic and for every series of pure expressions  $(e_1^i, \ldots, e_n^i)_i$  such that  $\emptyset, \Gamma \vdash e_j^i : \Gamma(l_j), j \leq n$  and  $e_j^i$  is reducible for  $\Upsilon \setminus \Gamma(l_j)$ , there exists no infinite series of states  $(\langle e_i, \mu_i \rangle)_i$  such that  $e_0 = e$ , and  $\langle e_i, l_j \mapsto e_j^i, j \leq n \rangle \rightarrow \langle e_{i+1}, \mu_{i+1} \rangle$ ; and for every such finite chain, the  $(\mu_i)_i$  contain expressions that are reducible for  $\Upsilon \setminus \Gamma(l)$  (and with  $\Upsilon = \emptyset$ ).
- $\tau$  is of the form  $(\tau' \xrightarrow{\Upsilon'} \tau'')^{\ell}$  with  $\Upsilon' \subseteq \Upsilon$  and for every expression e' reducible for  $\Upsilon$  and such that  $\Upsilon, \Gamma \vdash e' : \tau$ , we have e e' reducible for  $\Upsilon$ .

This induction is well founded since we will only make inductive calls on  $\tau', \Upsilon'$  such that  $\Upsilon' \subset \Upsilon$  or  $\Upsilon' \subseteq \Upsilon$  and  $\tau' \in \tau$ .

In this lemma, we prove that the notion of reducibility is conserved by reduction.

Lemma 2: If e is reducible for  $\Upsilon$ , and  $\langle e, \mu \rangle \rightarrow \langle e', \mu' \rangle$  for some  $\mu$  that verifies the conditions of reducibility, then e' is reducible for  $\Upsilon$ .

*Proof of Lemma 2:* The proof is by definition of reducibility.

In this lemma, we prove that a conditional is reducible if and only if its branches are reducible. Lemma 3: If  $_H v$  then  $e_0$  else  $e_1$  is reducible if and only if  $e_0$  and  $e_1$  are reducible.

*Proof of Lemma 3:* If one step is made by  $e_0$  or  $e_1$ , we conclude by induction (on the number of steps for them to terminate). The other cases are also by induction.

In this lemma, we prove that if an expression is reducible for a particular set of types, it is reducible for smaller sets of types.

*Lemma 4:* Let e be an expression reducible for  $\Upsilon$ ,  $\tau$  a type and  $\Upsilon' \subseteq \Upsilon$  such that  $\Upsilon', \Gamma \vdash e : \tau$  and, for every  $\Upsilon'' \in \tau, \Upsilon'' \subseteq \Upsilon'$ . Then e is reducible for  $\Upsilon'$ .

*Proof of Lemma 4:* The proof is by induction on the definition of reducible

- If τ is atomic, the series of pure expressions (e<sup>i</sup><sub>1</sub>,...,e<sup>i</sup><sub>n</sub>)<sub>i</sub> such that Ø, Γ ⊢ e<sup>i</sup><sub>j</sub> : Γ(l<sub>j</sub>), j ≤ n and e<sup>i</sup><sub>j</sub> is reducible for Υ\Γ(l<sub>j</sub>) are the same than for Υ\Γ(l<sub>j</sub>) by induction.
- Otherwise, we conclude similarly by induction.

In this lemma, we prove that if an expression is reducible for a particular set of types, it is reducible for bigger sets of types.

Lemma 5: If e is reducible for  $\Upsilon$ ,  $\Upsilon \subseteq \Upsilon'$ , then e is reducible for  $\Upsilon'$ .

*Proof of Lemma 5:* The proof is by induction on the definition of reducible

- If τ is atomic, the series of pure expressions (e<sup>i</sup><sub>1</sub>,...,e<sup>i</sup><sub>n</sub>)<sub>i</sub> such that Ø, Γ ⊢ e<sup>i</sup><sub>j</sub> : Γ(l<sub>j</sub>), j ≤ n and e<sup>i</sup><sub>j</sub> is reducible for Υ'\Γ(l<sub>j</sub>) are the same than for Υ\Γ(l<sub>j</sub>) by induction.

Lemma 6: Every typed expression e is reducible.

*Proof of Lemma 6:* We show by induction on the structure of e that if  $\Upsilon, \Gamma \vdash e : \tau$  for some  $\Upsilon, \Gamma, \tau$  and  $e_1, \ldots, e_n$  are reducible pure expressions with  $\emptyset, \Gamma \vdash e_i : \Gamma(x_i)$ , then the term  $e[e_1/x_1, \ldots, e_n/x_n]$  is reducible.

- If e is a variable,  $e[e_1/x_1, \ldots, e_n/x_n]$  is a variable or one of the  $e_i$ , we conclude.
- If e = e' e'', we conclude by induction.
- If  $e = \lambda x.e'$ , we have  $e[e_1/x_1, \ldots, e_n/x_n] = \lambda x.e'[e_1/x_1, \ldots, e_n/x_n]$ . We let  $e'' = e'[e_1/x_1, \ldots, e_n/x_n]$ . Let  $\tau_0 \xrightarrow{\Upsilon_0} \tau_1 \xrightarrow{\Upsilon_1} \ldots \xrightarrow{\Upsilon_{m-1}} \tau_m$  be the type of  $\lambda x.e''$ , and let  $e^0, e^1, \ldots, e^m$  be reducible terms of types  $\tau_0, \tau_1, \ldots, \tau_n$ . Let  $\mu$  be a memory satisfying the properties of the reducible definition. Lets consider chain of reductions of the state  $\langle \lambda x.e'' e^0 e^1 \ldots e^m, \mu \rangle$ . In this chain, either the first application is never reduced (and we conclude), or

it is reduced at step k. In this case, the term after step k is of the form:  $\langle e''[e^{0*}/x] e^1 \dots e^m, \mu' \rangle$ , where  $e^{0*}$  is a reduction of  $e^0$  and either a value or a tree of locations (hence reducible for  $\Upsilon$ ), we conclude by induction on e''.

- If e = !e', we proceed similarly. Either ! is never reduced, (and we conclude), or the term after the reduction step is of the form:
  - $\langle (\text{If}_H v \text{ then } | e^{0*0} \text{ else } | e^{0*1}) e^1 \dots e^m, \mu' \rangle$  if If v then  $e^{0*0}$  else  $e^{0*1}$  is a reduction of e, in which case we use Lemma 3 to separate the two branches and continue reductions; or
  - $\langle e'' \ e^1 \ \dots \ e^m, \mu' \rangle$ , where  $e'' = \mu'(l)$  and l is a reduction of  $e^0$ . e'' is reducible for  $\Upsilon \setminus \tau''$ , but it is also reducible by  $\Upsilon$  from Lemma 5.
- If e = |f e'| then e'' else e''' we use Lemma 3 and conclude by induction.
- If e = ref e', we conclude similarly by induction.
- If e = (e' := e''), we conclude similarly by induction.

Proof: The proof of Theorem 6 follows from Lemma 6.

# C. Proof of Theorem 7

This theorem is about termination in two different reduction system (with the original or extended rules). In order to relate executions, and since extended rules delay the executions of conditionals, we introduce a relation from expressions to expressions that contain additional conditionals.

Definition 5: We let  $\leq$  be a transitive relation on expressions and values defined by

$$e \leq e \qquad e_k \leq \operatorname{lf}_H k \text{ then } e_1 \text{ else } e_0$$
$$v_k \leq (k \wedge v_1) \lor (\neg k \wedge v_0) \qquad v_0 \leq 0 \lor (\neg 0 \wedge v_0)$$
$$v_0 \leq (\neg 0 \wedge v_0) \qquad v_0 \leq (1 \wedge v_0) \qquad \frac{e_0 \leq e'_0 \qquad e_1 \leq e'_1}{e_0 \ e_1 \leq e'_0 \ e'_1}$$

(We omit the similar rules for k = 1 and most inductive rules.)

In this lemma, we prove that if a value is in relation with an expression (by  $\leq$ ), then every intermediate state is in relation with the original expression.

Lemma 7: Let  $\Gamma$  be an environment, let  $v_0$  be a typed value, and  $e_1$  be a typed expression such that  $v_0 \leq e_1$ , let  $\mu_0$  be a value memory and  $\mu_1$  be a pure memory such that, for every location l,  $\mu_0(l) \leq \mu_1(l)$ . Let  $\langle e^i, \mu^i \rangle_{i \leq k}$  be a sequence of configuration terminating on an irreducible expression, and  $(\alpha^i)_{i \leq k}$  be a sequence of transition labels such that

$$\langle e_1, \mu_1 \rangle = \langle e^1, \mu^1 \rangle \xrightarrow{\alpha^1} \dots \langle e^k, \mu^k \rangle$$

For every  $i \leq k$ ,  $v_0 \leq e^i$ , and  $\mu_0(l) \leq \mu^i(l)$ .

*Proof of Lemma 7:* The proof is by induction on the length of the trace (k), with a first pass on pure expressions so that we can apply the lemma on memory reductions.

- If the next reduction is a memory reduction, we conclude by induction.
- If  $e_1$  is a value, we conclude.
- If  $e_1$  is of the form

$$(0 \wedge v_1) \lor (\neg 0 \wedge v_0) \qquad (0 \wedge v_1) \lor (\neg 0 \wedge v_0)$$
$$0 \lor (\neg 0 \wedge v_0) \qquad (\neg 0 \wedge v_0) \qquad (1 \wedge v_0)$$

we reduce and conclude.

- If e<sub>1</sub> is of the form lf<sub>H</sub> k then e<sup>1</sup><sub>1</sub> else e<sup>0</sup><sub>1</sub> and v<sub>0</sub> ≤ e<sup>k</sup><sub>1</sub>, there are three possible cases:
  - $e_1^1$  or  $e_1^0$  reduces in the conditional, in which case we conclude by induction.
  - e<sub>1</sub><sup>1</sup> and e<sub>1</sub><sup>0</sup> are values, and the conditional reduces with one of the possible rules for conditionals on values. We verify that the results follows our definition (using induction in one case) and conclude.
  - $e_1$  is a tree of location, in which case it does not reduces, and we conclude.
- In all other cases where  $v_0 \preceq e_1$ ,  $e_1$  is already a value.

In this lemma, we prove that if two expressions are in relation (by  $\leq$ ), and the second expression terminates, then a reduction of the first expression is eventually mirrored in the second expression.

Lemma 8: Let  $\Gamma$  be an environment, let  $e_0$  and  $e_1$  be two typed expressions such that  $e_0 \leq e_1$ , let  $\mu_0$  be a value memory, and  $\mu_1$  a pure memories such that, for every location l,  $\mu_0(l) \leq \mu_1(l)$ . Let  $\langle e^i, \mu^i \rangle_{i \leq n}$  be a sequence of configurations, terminating on an irreducible expression, and  $(\alpha^i)_{i \leq n}$  be a (possibly infinite) sequence of transition labels such that

$$\langle e_1, \mu_1 \rangle = \langle e^1, \mu^1 \rangle \xrightarrow{\alpha^1} \dots \langle e^n, \mu^n \rangle$$

If  $\langle e_0, \mu_0 \rangle \xrightarrow{\alpha} \langle e'_0, \mu'_0 \rangle$  (in the original semantics), then, there exists an index *i* such that

- $e'_0 \preceq e^i$ ,
- for every  $j < i, e_0 \preceq e^j$ ,
- for every j < i, for every location l,  $\mu_0(l) \preceq \mu^j(l)$ , and
- for every location l,  $\mu_0(l) \preceq \mu^n(l)$ .

*Proof of Lemma 8:* The proof is by induction on the length of the trace (k), with a first pass on pure expressions so that we can apply the lemma on memory reductions.

- If the next reduction is a memory reduction, we conclude by induction.
- If  $e_1$  is a value, then  $e_0$  is a value, and we conclude.
- If *e*<sub>1</sub> is of the form *v*∧*v*, *v*∨*v*, or ¬*v*, then *e*<sub>0</sub> is also of this form (or is a value); we check that the reductions behave correctly and conclude.

- If  $e_1$  is of the form  $e_1'' e_1'''$ , then  $e_0$  is of the form  $e_0'' e_0'''$ , there are three possibilities:
  - $e_0''$  is not a value. The only possible reduction is a reduction of  $e_0''$ . We conclude by induction on  $e_0''$  and  $e_1''$ .
  - $e_0''$  is a value and  $e_1''$  is not a value, the only possible reductions of  $e_1$  are reductions of  $e_1''$ . By Lemma 7 on  $e_0''$  and  $e_1''$  we reduce the problem to the next point.
  - $e_0''$  and  $e_1''$  are values.  $e_0'''$  is not a value; the only possible reduction of  $e_0$  is a reduction of  $e_0'''$ . We conclude by induction on  $e_0'''$  and  $e_1'''$ .
  - $e_0''$  and  $e_1''$  are values;  $e_0'''$  is a value, but  $e_1'''$  is not a value or a tree of locations. By Lemma 7 on  $e_0'''$  and  $e_1'''$ , we reduce to the next point.
  - $e_0''$  and  $e_1''$  are values;  $e_0'''$  is a value,  $e_1'''$  is a value or a tree of location. The only reduction possible in both cases is the application; we conclude.
- If  $e_1$  is of the form  $|f_L k$  then  $e_1^1 | e| e e_1^0$ , then  $e_0$  is of the form  $|f_L k$  then  $e_0^1 | e| e e_0^0$ , the only possible reduction of  $e_0$  and  $e_1$  is the standard conditional reduction; we conclude.
- If  $e_1$  is of the form  $If_H k$  then  $e_1^1$  else  $e_0^1$  and  $e_0$  is of the form  $If_H k$  then  $e_0^1$  else  $e_0^0$  with  $e_0^1 \leq e_1^1$  and  $e_0^0 \leq e_1^0$ ,  $e_0$  reduces (with the original rules) to  $e_0^k$ , with  $e_0^k \leq If_H k$  then  $e_1^1$  else  $e_0^1$  and we conclude.
- If  $e_1$  is of the form  $\text{If}_H k$  then  $e_1^1$  else  $e_1^0$  and  $e_0 \leq e_1^k$ , then by induction on  $e_0$  and  $e_k$ , there are tow possible cases:
  - $e_1^1$  or  $e_1^0$  reduces in the conditional, in which case we conclude by induction.
  - $e_1^1$  and  $e_1^0$  are values or trees of locations, in which cases  $e_0$  is a value (contradicts the hypothesis that  $e_0$  reduces).
- If  $e_1$  is of the form  $!!f_H k$  then  $e_1^1$  else  $e_1^0$  and  $e_0$  is of the form  $!!f_H k$  then  $e_0^1$  else  $e_0^0$  with  $e_0^1 \leq e_1^1$  and  $e_0^0 \leq e_1^0$ ,  $e_0$  reduces (with the original rules) to  $!e_0^k$ , with  $!e_0^k \leq !!f_H k$  then  $e_1^1$  else  $e_1^0$  and we conclude.
- If  $e_1$  is of the form  $!!f_H k$  then  $e_1^1$  else  $e_1^0$  and  $e_0$  is of the form  $!e_0^k$  with  $e_0^k \leq e_1^k$ ,  $e_1$  reduces to  $|f_H k$  then  $!e_1^1$  else  $!e_1^0$  and we conclude by induction.
- In any over case were  $e_1$  is of the form !e, we conclude by induction.
- The cases for ref e and e := e' are similar.

In this lemma, we generalize the results above for nonterminating expressions.

Lemma 9: Let  $\Gamma$  be an environment, let  $e_0$  and  $e_1$  be two typed expressions such that  $e_0 \leq e_1$ , let  $\mu_0$  and  $\mu_1$  be two pure memories such that, for every location l,  $\mu_0(l) \leq \mu_1(l)$ . Let  $\langle e^i, \mu^i \rangle_i$  be a sequence of configurations, either infinite or terminating on an irreducible expression, and  $(\alpha^i)_i$  be a (possibly infinite) sequence of transition labels such that

$$\langle e_1, \mu_1 \rangle = \langle e^1, \mu^1 \rangle \xrightarrow{\alpha^1} \langle e^2, \mu^2 \rangle \xrightarrow{\alpha^2} \dots$$

If  $e_0$  is a value, then the sequence is finite, the last expression  $e^i$  is such that  $e_0 \leq e^i$ , and  $\mu_0(l) \leq \mu^i(l)$ . If  $\langle e_0, \mu_0 \rangle \xrightarrow{\alpha} \langle e'_0, \mu'_0 \rangle$  (in the original semantics), then, there exists an index *i* such that  $e'_0 \leq e^i$ , and for every location  $l, \mu'_0(l) \leq \mu^i(l)$ .

*Proof of Lemma 9:* The proof is by induction on the structure of  $e_1$ . It is very similar to the proof of Lemma 8 (even if the induction is different), so we only highlight the case that differs:

 If e<sub>1</sub> is of the form If<sub>H</sub> k then e<sup>1</sup><sub>1</sub> else e<sup>0</sup><sub>1</sub> and e<sub>0</sub> ≤ e<sup>k</sup><sub>1</sub>, then e<sub>1</sub> type with a pc Υ ≠ L. Hence, by Theorem 6, ⟨e<sup>i</sup>, μ<sup>i</sup>⟩<sub>i</sub> is finite. We conclude with Lemma 8 and 7.

*Proof:* The proof of Theorem 7 is by induction on the number of steps for e to terminate, using Lemma 9.

# D. Proof of Theorem 8

From Theorem 4, if an expression terminates with two different paths, the memories must be the same. This implies IO non-interference by Theorem 1. We omit the proof for path non-interference since it is similar to the proof of Theorem 9 (with a special case for private functions that use public branches).

# E. Proof of Theorem 9

We want to prove equivalence of two executions on memories that differ only on private values. Hence, we first define an equivalence between expressions that share the same public values, but not the privates ones.

Definition 6: Let  $\Gamma$  be an environment, let  $\Upsilon$  be a pc, let  $\tau$  be a type, let e and e' be two expressions such that  $\Upsilon, \Gamma \vdash e : \tau$  and  $\Upsilon, \Gamma \vdash e' : \tau$ . We define the reflexive relation  $\sim_{\tau, \Gamma, \Upsilon}$  as:

$$k \sim_{bool^H,\Gamma,\Upsilon} k'$$

plus the natural inductive rules.

We now prove a more generic lemma using this equivalence.

*Lemma 10:* Let  $\Gamma$  be an environment, let  $\tau$  be a security type, and let  $\Upsilon$  be a pc, let  $e_0$  and  $e_1$  be two expressions such that  $e_0 \sim_{\tau,\Gamma,\Upsilon} e_1$ , let  $\mu_0$  and  $\mu_1$  be two pure memories such that, for every  $l \in \Gamma$ ,  $\mu_0(l) \sim_{\Gamma(l),\Gamma,\emptyset} \mu_1(l)$ . For every memory  $\mu'_0$ , expression  $e'_0$ , and  $\alpha_0$  such that

$$\langle e_0, \mu_0 \rangle \xrightarrow{\alpha_0} \langle e_0, \mu'_0 \rangle$$

there exist a memory  $\mu'_1$ , an expression  $e_1$ , an environment  $\Gamma'$  with  $\Gamma \subseteq \Gamma'$  and  $\alpha_1$  such that

$$\langle e_1, \mu_1 \rangle \xrightarrow{\alpha_1} \langle e_1, \mu_1' \rangle$$

 $e_0 \sim_{\tau,\Gamma',\Upsilon} e_1$ , and for every  $l \in \Gamma'$ ,  $\mu'_0(l) \sim_{\Gamma'(l),\Gamma',\emptyset} \mu'_1(l)$ .

*Proof of Lemma 10:* The proof is by structural induction on  $e_0$ , with a first pass on pure expressions so that we can apply the lemma on memory reductions.

- If there is a memory reduction in μ<sub>0</sub>, we mirror the reduction in μ<sub>1</sub>.
- If  $e_0$  is a value, there is no reduction, we conclude.
- If e<sub>0</sub> is of the form v<sub>0</sub><sup>0</sup> ∧ v<sub>0</sub><sup>1</sup>, then e<sub>1</sub> is of the form v<sub>0</sub><sup>0</sup> ∧ v<sub>0</sub><sup>1</sup>. Both have only one possible reduction, and we conclude (similarly for ∨ and ¬.
- If  $e_0$  is of the form  $e_0^0 e_0^1$ , then  $e_1$  is of the form  $e_1^0 e_1^1$ , with  $e_0^0 \sim_{\tau',\Gamma,\Upsilon} e_1^0$  and  $e_0^1 \sim_{\tau'',\Gamma,\Upsilon} e_1^1$ . If  $e_0^0$  is not a value, we conclude by induction. If the reduction is inside  $e_0^1$  we conclude by induction. Otherwise,  $e_0^1$  is a tree of locations, hence  $e_1^1$  is a tree of locations, the transition can happen on both sides.
- If e<sub>0</sub> is of the form If<sub>L</sub> v<sub>0</sub> then e<sup>0</sup><sub>0</sub> else e<sup>1</sup><sub>0</sub>, then e<sub>1</sub> is of the form If<sub>L</sub> v<sub>1</sub> then e<sup>0</sup><sub>1</sub> else e<sup>1</sup><sub>1</sub>, with v<sub>0</sub> ~<sub>τ',Γ,Υ</sub> v<sub>1</sub>, e<sup>0</sup><sub>0</sub> ~<sub>τ'',Γ,Υ</sub> e<sup>1</sup><sub>1</sub>, and e<sup>1</sup><sub>0</sub> ~<sub>τ''',Γ,Υ</sub> e<sup>1</sup><sub>1</sub>. There is only one possible reduction, and it works as expected.
- If e<sub>0</sub> is of the form If<sub>H</sub> v<sub>0</sub> then e<sub>0</sub><sup>0</sup> else e<sub>0</sub><sup>1</sup>, then e<sub>1</sub> is of the form If<sub>H</sub> v<sub>1</sub> then e<sub>1</sub><sup>0</sup> else e<sub>1</sub><sup>1</sup>, with v<sub>0</sub> ~<sub>τ',Γ,Υ</sub> v<sub>1</sub>, e<sub>0</sub><sup>0</sup> ~<sub>τ'',Γ,Υ</sub> e<sub>1</sub><sup>0</sup>, and e<sub>0</sub><sup>1</sup> ~<sub>τ''',Γ,Υ</sub> e<sub>1</sub><sup>1</sup>. If at least one of e<sub>0</sub><sup>0</sup> and e<sub>0</sub><sup>1</sup> is not an expression, there is one or two possible reductions, and they work as expected by induction. If e<sub>0</sub><sup>0</sup> and e<sub>1</sub><sup>1</sup> are both lambda abstraction or booleans, then e<sub>1</sub><sup>0</sup> and e<sub>1</sub><sup>1</sup> also are lambda abstraction; there is one possible reduction, which works correctly. Otherwise, they are both locations and there is no possible reductions.
- If  $e_0$  is of the form  $e_0^0 := e_0^1$ , then  $e_1$  is of the form  $e_1^0 := e_1^1$ , with  $e_0^0 \sim_{\tau',\Gamma,\Upsilon} e_1^0$  and  $e_0^1 \sim_{\tau'',\Gamma,\Upsilon} e_1^1$ . If  $e_0^0$  or  $e_0^1$  is not a value or an high if, we conclude by induction. If  $e_0^0$  is an high if, then  $e_1^0$  is too, we verify the reduction. If  $e_0^1$  is an high if, then  $e_1^1$  is too, we verify the reduction. Otherwise, there is one reduction, that works as expected. We conclude.
- We conclude similarly for ! and ref .

*Proof:* The proof of Theorem 9 is direct from Lemma 10.