

Keynote & Invited Speaker's Biography

Keynote Speaker's Biography



Bert Gyselinckx
General Manager
IMEC, Netherlands

Bert Gyselinckx is General Manager of Holst Centre - imec. Bert was instrumental in defining the technical strategy of the Holst Centre at its creation in 2005. He brought to the Holst Centre his innovation management experience and know how in wireless research from imec. Bert is well known in the scientific community for his pioneering contributions to wireless OFDM communications leading to our current generation of WiFi modems. Bert lives by the golden rule “working hard, playing hard”. In 2001, he replaced his office chair for a bike saddle and went on a 12 month odyssey in the Asia Pacific region. After 15000km in “under” developed countries, he was inspired to create technologies that can have a true impact on society. For this purpose, he established the Human++ program within imec. This program develops disruptive technologies for health and comfort monitoring. As the exponent of the Human++ program, Bert became known as a thought leader in the area of body area networks. Bert is also a board member of NanoLabNL, a Dutch national facility for nanotechnology research. Bert received the M.S. degree in Electrical Engineering from the Rijksuniversiteit Gent, Belgium, and the DEA degree in Air and Space Electronics from the École nationale supérieure de l'aéronautique et de l'espace, Toulouse, France. He is also a trainee at the Research and Development group of Siemens in Munich, Germany.

Keynote Speaker's Biography



Hiroshi Kanayama, Ph.D.

IBM

Hiroshi Kanayama joined IBM Research - Tokyo just after receiving Master Degree in 2000. His research interest is natural language processing, mainly syntactic and semantic analysis of Japanese language. His research outcomes on Japanese syntactic parser and sentiment analysis were integrated with a software product IBM Content Analytics, and they differentiated IBM's text mining solutions. From 2008 he joined Watson project (Jeopardy! challenge) and provided lexical resources for type matching using information extraction techniques. In 2012 he received Ph.D. degree from the University of Tokyo with his work on sentiment analysis.

Keynote Speaker's Biography



Michael McCool, Ph.D.
Principal Engineer
Intel, USA

Michael McCool (Intel Principal Engineer) has degrees in Computer Engineering (University of Waterloo, BSc) and Computer Science (University of Toronto, M.Sc. and PhD.) with specializations in mathematics (BSc) and biomedical engineering (MSc) as well as computer graphics and parallel computing (MSc, PhD). He has research and application experience in the areas of data mining, computer graphics (specifically sampling, rasterization, path rendering, texture hardware, antialiasing, shading, illumination, function approximation, compression, and visualization), medical imaging, signal and image processing, financial analysis, and parallel languages and programming platforms. In order to commercialize research work into many-core computing platforms done while he was an Associate Professor at the University of Waterloo, in 2004 he co-founded RapidMind, which in 2009 was acquired by Intel. Currently he is a software architect with Intel working on parallel programming languages, applications, and mobile computing. In addition to his university teaching, he has presented numerous tutorials at Eurographics, SIGGRAPH, and SC on graphics and/or parallel computing, and has co-authored three books. The most recent book, *Structured Parallel Programming*, was co-authored with James Reinders and Arch Robison. It presents a pattern-based approach to parallel programming using a large number of examples in Intel Cilk Plus and Intel Threading Building Blocks.

Keynote Speaker's Biography



Shintaro Momose, Ph.D.
Manager
NEC

Shintaro Momose is a manager of HPC Division at NEC Corporation, Tokyo, Japan. He is a hardware architect of SX vector supercomputer. Especially, his research focuses on building a grand design of HPC system and analyzing future HPC trends. He received the B.E. Degree in Mechanical Engineering, and the M.S. and the Ph.D. Degrees in Information Sciences from Tohoku University in 1999, 2003, and 2005 respectively.

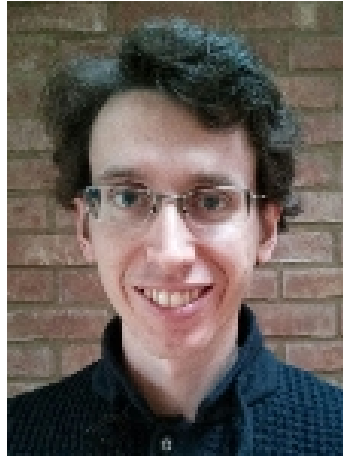
Invited Speaker's Biography



Takeshi Kataoka
Director
Renesas Mobile

Takeshi Kataoka is a Director of Mobile SoC Design Department in Renesas Mobile Corporation. He received the B.E. and M.E. degrees in Applied Physics from University of Tokyo, Tokyo, Japan in 1991 and 1993, respectively. Also he received the M.S. degree in Electrical Engineering from Stanford University, California, United States in 2001. He was a designer and architect of 32bit RISC CPU core, SH-series in Hitachi Ltd. and Renesas Technology Corporation, and a co-author of papers in CoolChips VII, VIII and XI. Also he had been engaging in development and business of Microcontroller in Automotive field, as power train, car multimedia and dash board for many years. Now he is engaging in development and business of SoC for Mobile Application and Baseband Field.

Invited Speaker's Biography



James Myers, Ph.D.
Staff Engineer
ARM, UK

James Myers is a Staff Engineer in ARM's Silicon R&D group. He joined ARM in 2007 where he was initially responsible for developing reference implementation flows for the various ARM soft processor cores. Joining R&D full time in 2009, he has since focused on deployable techniques for reduction of CPU and SoC power. His current research interests include low power circuits, advanced power gating, low voltage and better than worst-case design. James holds an MEng from Imperial College, London.

Invited Speaker's Biography



Toshio Yoshida
Director
Fujitsu

Toshio Yoshida is a director of LSI Development Division in the Next Generation Technical Computing unit at Fujitsu. His technical interests include microprocessor architecture. Yoshida received his MS in physics from the Faculty of Science and Graduate School of Science at the University of Tokyo.