

# Preface

This book contains the proceedings of the 33rd edition of the International Conference on Field Programmable Logic and Applications (FPL) held in Gothenburg on September 4th to 8th, 2023.

The end of Moore's law reinforce the interest on field-programmable logic that has shown performance and energy efficiency benefits over conventional general-purpose computing machines for decades. The development of new tools allows to reach the programmability of reconfigurable devices, which is a step forward in the usage of this technology. Today, reconfigurable computing is considered in emerging computing paradigms and applied for new challenging applications offering simultaneously the flexibility of software and the high performance of hardware.

This tremendous growth of reconfigurable computing and the opportunities created by new emerging systems and applications is reflected in the papers of this year's conference. This year, we received 139 submissions, from which the program committee selected 33 (23.7%) full papers and 18 short papers. In addition, we continued the successful tradition of the Ph.D. forum and Demo Night selecting 9 Ph.D. students to present their work and 4 research groups to present their demos in the conference.

We would like to thank all authors for submitting their papers. We also gratefully acknowledge the tremendous reviewing work done by the Technical Program Committee (TPC) members and many additional reviewers who contributed their time and expertise. We are pleased that we have received a total of 538 reviews, which translates to an average of 3.9 reviews per paper. We followed a double-blind review process, which included a rebuttal phase. An online discussion phase moderated by the Program co-Chairs was performed after the rebuttal. As the last step in the paper selection process, an online TPC meeting was organized, similarly to last years, which gave all TPC members the opportunity to participate in the final paper discussion and selection process.

We would also like to thank the members of the Organising and Steering Committees for their hard work over the past year. The members of the Program, Organisation, and Steering Committees are listed on the following pages.

Finally, we would like to thank our sponsors for supporting the conference: our Gold sponsor AMD, our Silver sponsors Frontgrade Gaisler, Intel, and Zeropoint Technologies, and our Bronze sponsor Two Sigma. We would also like to thank IEEE CPS for taking care of this publication.

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