# Full-Duplex Transmission Optimization for Bi-directional MIMO links with QoS Guarantees

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Abstract—We consider a bi-directional Full-Duplex (FD) Multiple-Input Multiple-Output (MIMO) communication system in which nodes are capable of performing transitter (TX)-Receiver (RX) digital precoding/combining and multi-tap analog cancellation, and have individual Signal-to-Interference-plusnoise Ratio (SINR) requirements. We present an iterative algorithm for the TX powers minimization that includes closedform expressions for the TX/RX digital beamformers at each algorithmic iteration step. Our representative simulation results demonstrate that the proposed algorithm can reduce residual Self-Interference (SI) due to FD operation to below -110 dB, which is the typical noise floor level for wireless communications. In addition, our design outperforms relevant recent solutions proposed for 2-user MIMO systems (the so called MIMO X channel) in terms of both power efficiency and computational complexity.

Index Terms—Full-duplex MIMO, Beamforming (BF), self-interference cancellation, power minimization, optimization.

## I. INTRODUCTION

Motivated by the exponentially increasing demand for higher information rate under limited wireless resources, and propelled by recent advances in radio frequency (RF) hardware, in-band Full-Duplex (FD) radio has emerged as a key technology for future wireless applications from fifth generation (5G) mobile communication systems to Internet of Things (IoT) [1]–[3].

Practical communication in FD mode requires dedicated solutions to mitigate the Self-Interference (SI) caused by leakage of TX signals into the Receiver (RX) chain, due to the close proximity between transitter (TX) and RX antennas [4], [5]. Ironing out this fundamental issue of FD technology is one of the main research topics in this field, motivating various authors to contribute with several SI cancellation techniques for FD systems [1], [6]–[8].

Thanks to the added Degrees of Freedom (DoF) afforded by multiple antennas, bi-directional FD radio systems with high spectral efficiency can be designed exploiting MIMO technology [9]–[12]. In particular, hybrid MIMO SI suppressing techniques combining analog and digital cancellers have been proposed for FD radios [13]–[16] which proved very effective from a theoretical standpoint. From a practical implementation standpoint, however, it has been recently demonstrated in realworld experiments that such MIMO approaches are not devoid of its own technical challenges [1], [17], [18], one of which

is the excessive cost incurred by the use of large numbers of antennas.

One approach to keep the hardware cost of hybrid MIMO SI suppressing techniques for FD radios under control is to reduce the number of antennas while introducing temporal DoFs by means of Tap Delay Line (TDL) processing in order to maintain the DoF required to achieve the desired performance [19]. In [19], for instance, a joint hybrid TX-RX BF design with limited hardware costs was proposed, in which the sum rate of a system with one MIMO FD radio communicating with two MIMO Half-Duplex (HD) nodes was optimized.

In this paper, we contribute to the area of effective and feasible SI canceller designs for MIMO FD radios as follows. First, we combine the joint hybrid TX-RX approach of [19] with the analog cancellation technique referred to as multi-tap analog canceller previously presented in [20]. The result is a new multi-tap hybrid (analog and digital) TX-RX MIMO FD SI cancellation scheme, in which the number of hardware components for analog cancellation becomes independent of the number of antennas. Secondly, instead of maximizing the sum rate (which is of less practical interest), we formulate our problem to minimize the TX power while guaranteeing (when possible) prescribed Quality of Service (QoS) targets defined in terms of maximum Signal-to-Interference-plus-noise Ratio (SINR). Thirdly and finally, we present a low-complexity solution to the latter problem in which the TX employs Maximum Ratio Transmission (MRT) with powers optimized in closed-form via a Perron-Frobenius (PF) method, while the RX maximizes the SINR by computing corresponding closed-form RX BF vectors from a Rayleigh Quotient (RQ), iteratively. Our results show that our algorithm can outperform the similar methods previously proposed for 2-user MIMO systems in terms of both power efficiency and computational complexity.

#### II. SYSTEM MODEL

Consider the two-way FD MIMO communication system illustrated in Figure 1. This system consists of two node in which each equipped with M TX and N receive antennas. Both nodes are assumed to TX and receive simultaneously to/from one another in the same resource unit.

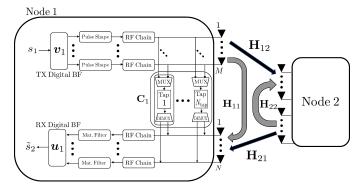


Fig. 1: System model of two-way full duplex MIMO with reduced hardware multi-tap analog cancellation.

A generic k-th node, with  $k \in \{1, 2\}$ , is assumed to employ the digital TX precoding vector  $v_k \in \mathbb{C}^{M \times 1}$  and the digital RX BF vector  $u_k \in \mathbb{C}^{1 \times N}$ , as well as the multi-tap analog cancellation matrix  $C_k \in \mathbb{C}^{N \times M}$  [19], [20]. It is capable of performing TX-RX digital BF and analog SI cancellation with the aim at suppressing SI and maximizing rate simultaneously. Finally, in order to model practical limitations, it is assumed that the TXted signal at the k-th node has a power upper bound, such that  $\operatorname{Tr}\left(\boldsymbol{v}_{k}\boldsymbol{v}_{k}^{\operatorname{H}}\right)=P_{k}\leq P_{\max}.$ Referring to Figure 1, let  $\boldsymbol{H}_{k\ell}\in\mathbb{C}^{N\times M}$  and  $\boldsymbol{H}_{kk}\in\mathbb{C}^{N\times M}$ 

be the intended channel matrix between the two nodes and the SI channel matrix at the k-th node, respectively, with  $k \neq \ell \in$  $\{1,2\}$ . It is also assumed throughout this paper that each node has full knowledge of the Channel State Information (CSI) of both the communication links and their own SI link. Extension to imperfect CSI knowledge is left for future work.

From all the above, the received signal at the k-th node after applying analog SI cancellation can be written as

$$y_{k} = H_{\ell k} v_{\ell} s_{\ell} + (H_{kk} - C_{k}) v_{k} s_{k} + n_{k}$$

$$= \underbrace{H_{\ell k} v_{\ell} s_{\ell}}_{\text{Intended}} + \underbrace{\tilde{H}_{kk} v_{k} s_{k}}_{\text{SI}} + \underbrace{n_{k}}_{\text{Noise}},$$
(1)

where the multi-tap analog cancellation matrix  $C_k$  consists of  $N_{
m tap}$  non-zero components and  $MN-N_{
m tap}$  zeros,  $m{n}_k\sim$  $\mathcal{CN}\left(0,\sigma^2\mathbf{I}_N\right)$  denotes the complex Additive White Gaussian Noise (AWGN) vector under the assumption that  $n_k$  is independent from the TXted signal  $s_{\ell}$ , and  $H_{kk} \triangleq H_{kk} - C_k$  is the SI channel matrix after performing the considered analog cancellation.

After digital down conversion and combining by the RX BF vector  $u_k$ , the estimated signal  $\tilde{s}_{\ell}$  corresponding to the intended signal  $s_{\ell}$  at the k-th node can be expressed as

$$\tilde{s}_{\ell} = u_k y_k 
= u_k H_{\ell k} v_{\ell} s_{\ell} + u_k \tilde{H}_{kk} v_k s_k + u_k n_k.$$
(2)

Similarly, the received signal and symbol estimate at node  $\ell \neq k$  after analog cancellation and RX BF are given, respectively, by

$$y_{\ell} = H_{k\ell} v_k s_k + \tilde{H}_{\ell\ell} v_{\ell} s_{\ell} + n_{\ell}, \tag{3}$$

$$\tilde{s}_k = \boldsymbol{u}_{\ell} \boldsymbol{y}_{\ell}$$

$$= u_{\ell} H_{k\ell} v_k s_k + u_{\ell} \tilde{H}_{\ell\ell} v_{\ell} s_{\ell} + u_{\ell} n_{\ell}, \qquad (4)$$

where  $n_{\ell} \sim \mathcal{CN}(0, \sigma^2 I_N)$  is the AWGN vector that is assumed independent from the TXted symbol  $s_k$ .

Assuming that unit power information signals  $s_k$  and  $s_\ell$  are used, the average SINR estimates at the two nodes in Figure 1 can be, respectively, written as

$$\gamma_{k} = \frac{\left| \boldsymbol{u}_{k} \boldsymbol{H}_{\ell k} \boldsymbol{v}_{\ell} \right|^{2}}{\left| \boldsymbol{u}_{k} \tilde{\boldsymbol{H}}_{k k} \boldsymbol{v}_{k} \right|^{2} + \sigma^{2}} \quad \text{and} \quad \gamma_{\ell} = \frac{\left| \boldsymbol{u}_{\ell} \boldsymbol{H}_{k \ell} \boldsymbol{v}_{k} \right|^{2}}{\left| \boldsymbol{u}_{\ell} \tilde{\boldsymbol{H}}_{\ell \ell} \boldsymbol{v}_{\ell} \right|^{2} + \sigma^{2}},$$
(5)

where we assume that the channel matrices in equation (5) are constant for a number of signal transmissions and the RX combining vector  $u_k$ ,  $\forall k$  has a unit norm, i.e.,  $||u_k||^2 = 1$ .

# III. QoS-Guaranteed Transmissions

Signal processing techniques for the joint TX-RX linear precoding/combining and adaptive TX power allocation with the aim of maximizing data rate while suppressing the residual SI power level have been proposed in the past [21], [22] demonstrating the feasibility of two-way FD MIMO systems.

Maximizing data rate is, however, not typically required by actual users, which instead tend to perceive the quality of a communication system by comparing it to a given level of expectation dictated by the intended application. We therefore consider instead the TX-RX beamformer optimization problem aiming at minimizing the individual TX powers while satisfying individual target SINR requirements:

$$\min_{\boldsymbol{v}_k, \boldsymbol{v}_\ell} \qquad \sum_{k=1}^2 ||\boldsymbol{v}_k||^2 
\text{s.t.} \qquad \gamma_k \ge \Gamma_k \ \forall k,$$
(6a)

s.t. 
$$\gamma_k \ge \Gamma_k \ \forall k,$$
 (6b)

where  $\Gamma_k$  is the target SINR for the k-th node.

# A. TX Power Minimization with SINR Constraints

Let us define the normalized precoding vector  $ar{oldsymbol{v}}_k riangleq$ and the TX power  $P_k = ||\boldsymbol{v}_k||^2$  such that the optimization problem in (6) can be rewritten as

$$\min_{P_1, P_2} \qquad \sum_{k=1}^{2} P_k \tag{7a}$$

s.t. 
$$\gamma_k \ge \Gamma_k \ \forall k.$$
 (7b)

The optimization problem described by equation (7) is wellknown to be non convex due to the SINR constraints [23], although approximate solutions can be obtained for it with basis on convex optimization algorithms, such as interior point methods if the constraint can be convexified [24]. In addition to the losses due to convex relaxation, such solutions tend also to be computationally demanding. Therefore, we propose instead a low complexity alternating minimization method based on closed-form expressions of the optimal TX powers  $P_1$  and

In order to obtain the desired closed-form expressions for  $P_1$  and  $P_2$ , notice that from equation (5) and (7) we readily

$$P_2 |\mathbf{u}_1 \mathbf{H}_{21} \bar{\mathbf{v}}_2|^2 \ge \Gamma_1 (P_1 |\mathbf{u}_1 \tilde{\mathbf{H}}_{11} \bar{\mathbf{v}}_1|^2 + \sigma^2),$$
 (8a)

$$P_1 |\boldsymbol{u}_2 \boldsymbol{H}_{12} \bar{\boldsymbol{v}}_1|^2 \ge \Gamma_2 \left( P_2 |\boldsymbol{u}_2 \tilde{\boldsymbol{H}}_{22} \bar{\boldsymbol{v}}_2|^2 + \sigma^2 \right).$$
 (8b)

The latter inequalities can be re-expressed in matrix form a

$$(\mathbf{I} - \mathbf{\Gamma} \mathbf{M}) \, \mathbf{p} \ge \sigma^2 \mathbf{\Gamma} \mathbf{m}, \tag{}$$

where we define the TX power vector  $\boldsymbol{p} \triangleq [P_1, P_2]^T$  and the table of ta auxiliary matrices  $\Gamma$ , M and m respectively by

$$\Gamma = \begin{bmatrix} 0 & \Gamma_2 \\ \Gamma_1 & 0 \end{bmatrix}, \tag{10}$$

$$M = \begin{bmatrix} \frac{|u_1 \tilde{H}_{11} \tilde{v}_1|^2}{|u_1 H_{21} \tilde{v}_2|^2} & 0\\ 0 & \frac{|u_2 \tilde{H}_{22} \tilde{v}_2|^2}{|u_2 H_{12} \tilde{v}_1|^2} \end{bmatrix}, \quad (10)$$

$$m = [|u_1 H_{21} \tilde{v}_2|^{-2} |u_2 H_{12} \tilde{v}_1|^{-2}]^{\mathrm{T}}. \quad (10)$$

$$m = \begin{bmatrix} \left| u_1 H_{21} \bar{v}_2 \right|^{-2} & \left| u_2 H_{12} \bar{v}_1 \right|^{-2} \end{bmatrix}^{\mathrm{T}}.$$
 (10)

Taking advantage of the PF theorem [25] and the fact th  $\Gamma M$  is a non negative matrix, the optimal TX power vect can be computed in closed form as

$$\boldsymbol{p}^* = \sigma^2 \left( \mathbf{I} - \boldsymbol{\Gamma} \boldsymbol{M} \right)^{-1} \boldsymbol{\Gamma} \boldsymbol{m}. \tag{11}$$

### B. Optimal BF Design for SINR Maximization

With possession of a closed-form optimal solution to the TX power vector p as per equation (11), as well as a given analog cancellation matrix  $C_k$  obtained for example as discussed in [19], [22], we seek optimal BF designs for  $v_k$  and  $u_k \, \forall k$ , such that the average SINR at each node is maximized, while minimizing the effect of the SI. Taking into account the fact that the role of TX-RX beamformers is to minimize the effect of SI while maximizing the downlink rate, we consider the MRT TX beamformer with perfect CSI known at the nodes, such that the instantaneous SINR at each node is maximized under the assumption that the SI power level can be significantly reduced after processing by the proposed optimal RX combiner.

# 1) Design of RX Combiner $u_k \forall k$ :

The role of the RX combining vector  $u_k$  at the k-th node is to maximize the power of the signal from the  $\ell$ -th node, while supressing the interference-plus-noise signal. In other words, the RX BF vector  $u_k$  must be designed so as to maximize the ratio between the power of the intended signal and that of interference-plus-noise term of equation (2), which can be mathematically expressed as

$$\max_{\substack{\mathbf{u}_{k}\\||\mathbf{u}_{k}||^{2}=1}} \frac{\mathbf{u}_{k} \underbrace{\mathbf{H}_{\ell k} \mathbf{v}_{\ell} \mathbf{v}_{\ell}^{\mathrm{H}} \mathbf{H}_{\ell k}^{\mathrm{H}} \mathbf{u}_{k}^{\mathrm{H}}}_{\mathbf{u}_{k}^{\mathrm{H}} \mathbf{u}_{k}^{\mathrm{H}} + \sigma^{2} \mathbf{I} \underbrace{\mathbf{u}_{k}^{\mathrm{H}} \underbrace{(\tilde{\mathbf{H}}_{k k} \mathbf{v}_{k} \mathbf{v}_{k}^{\mathrm{H}} \tilde{\mathbf{H}}_{k k}^{\mathrm{H}} + \sigma^{2} \mathbf{I})}_{\triangleq \mathbf{W}_{\mathbf{u}_{k}}} \mathbf{u}_{k}^{\mathrm{H}}},$$
(12)

which holds a generalized RQ structure, such that the optimal solution to  $u_k$  is obtained by [26]

$$\boldsymbol{u}_{k}^{*} = \operatorname{eigv}_{\max} \left( \boldsymbol{W}_{\boldsymbol{u}_{k}}^{-1} \boldsymbol{Q}_{\boldsymbol{u}_{k}} \right)^{\mathrm{H}}.$$
 (13)

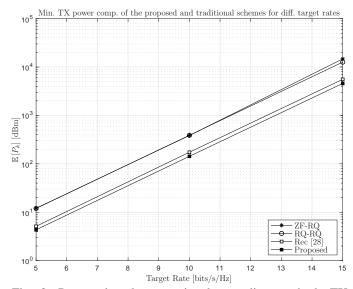


Fig. 2: Proposed and conventional preceding methods TX power comparison for different target rates.

### Algorithm 1 Alt. TX Power Min. with SINR Guarantees.

- 1: **Input**: $P_k$ ,  $H_{kk}$ ,  $H_{\ell k}$ ,  $C_k \forall k$  given by [19].
- 2: Set  $P_k = P_{\text{max}} \forall k \in \{1, 2\}$  and make arbitrary unit-norm vectors as initial RX BF vectors  $u_k \forall k$ .
- 3: repeat
- Compute  $\bar{\boldsymbol{v}}_k \forall k$  from equation (14). 4:
- 5: Compute  $u_k^* \forall k$  from equation (13).
- Compute  $p^*$  from equation (11). 6:
- 7: until convergence or reach maximum iterations.

#### 2) Design of TX Precoder $v_k \forall k$ :

Assuming that the strong SI caused by the leakage of own TX signals due to the close proximity of TX and RX antennas can be sufficiently suppressed by the RX combining vector  $u_k$ , the role of the TX precoder  $v_k$  is only to direct the TX beams so as to maximize the downlink rate. For this purpose, it suffices to apply a simple MRT TX precoder, namely

$$\bar{\boldsymbol{v}}_k = \frac{\boldsymbol{H}_{kl}^{\mathrm{H}} \boldsymbol{u}_{\ell}^{\mathrm{H}}}{||\boldsymbol{H}_{kl} \boldsymbol{u}_{\ell}||}.$$
 (14)

Taking into account all the steps described in this section, the proposed algorithm for the optimization of TX powers, as well as TX and RX BF vectors can be compactly described by the pseudo-code offered in Algorithm 1.

#### IV. SIMULATION RESULTS

In this section, we evaluate the proposed iterative algorithm in terms of consumed TX power for different required SINR constraints via software simulation. The downlink communication channels  $H_{12}$  and  $H_{21}$  are assumed to be block Rayleigh fading with 110dB path loss, while the SI channels  $H_{11}$ ,  $H_{22}$ are assumed to be block Ricean with path loss of 40dB and K-factor 35dB [19], [27].

Each node is assumed to be equipped with 4 TX and receive antennas, i.e., M = N = 4, with a noise floor of -110 dBm, and the number of analog cancellation taps  $N_{\rm tap}$  is set to 8, which corresponds to 50% reduction in the number of elements in the analog cancellation matrix  $C_k$  compared to [1], [6].

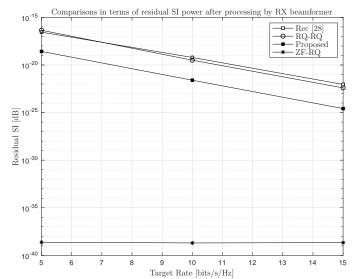


Fig. 3: Residual SI power comparisons of the proposed and conventional methods for different target rates after RY RF

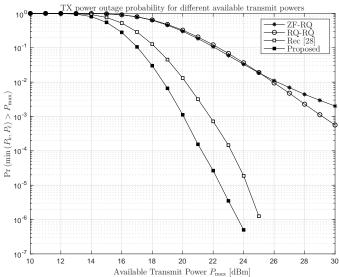


Fig. 4: TX power outage probability for different available TX powers with the fixed target rate  $R_k = R_\ell = 8$  [bps/Hz].

For the modeling practical situations, the multi-tap analog canceller is assumed to be subjected to amplitude imperfection uniformly distributed between  $-0.01 \, \mathrm{dB}$  and  $0.01 \, \mathrm{dB}$  and phase noise uniformly distributed between  $-0.065^\circ$  and  $0.065^\circ$  [19], [20].

In all figures that follow, we compare the proposed TX power minimization method in Algorithm 1 with 100 maximum iterations against the conventional Zero-Forcing (ZF) TX precoder, in which the proposed PF power optimization is applied. In addition, by noticing that the considered bidirectional FD MIMO corresponds to a special form of the MIMO X channel, we deploy relevant algorithms [28]–[30] targeting at TX-RX BF design yielding sum rate maximization. Particularly, our considered system is a MIMO X channel having  $\boldsymbol{H}_{21}$  and  $\boldsymbol{H}_{12}$  as the intended channels and  $\tilde{\boldsymbol{H}}_{11}$  and  $\tilde{\boldsymbol{H}}_{22}$  as the interference channels, having possibly larger powers than the intended ones.

TABLE I: Run time comparisons for different methods.

Methods	ZF	RQ-RQ	Rec	Proposed
Average run time [s]	0.0025	0.0028	0.1309	0.0022

First, average TX power comparisons of the proposed algorithm for different target rates  $\log_2(1+\Gamma_k)\forall k$  is shown in Figure 2, where ZF-RQ, RQ-RQ [30] and the Reconfigurable sum rate maximization algorithm [28] are employed as a benchmark. In order to fairly compare those algorithms, we adopt an alternating recalculation between TX-RX BF for each algorithms until convergence or maximum number of iterations reached. It is shown in Figure 2 that the proposed method can decrease the TX power by about  $-4.5 \mathrm{dB}$  compared to the conventional ZF-RQ, RQ-RQ methods and about  $-0.8 \mathrm{dB}$  compared against the Reconfigurable method.

Secondly, Figure 3 outlines that the interference cancellation performance in terms of residual SI power levels after processing by the RX BF are compared for the different TX-RX BF schemes. From Figure 2 and 3, one can notice that although the ZF method can perfectly suppress the effect of SI at the RX baseband, the proposed method can outperform the other schemes due to the fact that not only the residual SI level of the proposed method is suppressed below the noise floor level but also it aims at maximizing the data rate performance. In other words, the other methods devote too much available DoFs to suppressing SI power level at the RX baseband.

Thirdly, the TX power outage probability of the proposed method for different available TX powers  $P_{\rm max}$  with target data rate fixed at  $R_k=R_\ell=8$  [bps/Hz] is compared with the outage performance of the other conventional methods in Figure 4, where we define the TX power outage probability as  $\Pr\left(\min\left(P_k,P_\ell\right)>P_{\rm max}\right)$ . Lastly, the average run time comparisons until the convergence for each different algorithms are depicted in TABLE I, where we take an average from 500 channel realizations. From Figure 2, 3 and 4 and Table I, it can be observed that the proposed method can has much fast convergence rate compared with the Reconfigurable method and outperform the conventional ZF and RQ TX BF methods in terms of the TX power outage probability performance.

#### V. CONCLUSION

In this paper, we considered bi-directional FD MIMO communications systems with limited number of analog canceller taps and designed TX-RX BF vectors with the goal to minimize TX power under SINR constraints.

The proposed TX power minimization BF design was investigated in terms of system performance and complexity, and the PF TX power minimization approach was jointly offered with the proposed beamformers. Simulation results demonstrate the capability of our proposed algorithm to suppress the SI level to below  $-110 \mathrm{dB}$  which is the typical noise floor for wireless communications, while maximizing the downlink rate, and consequently, it minimizes the average TX power for different target data rate.

# VI. ACKNOWLEDGEMENT

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