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Full-Duplex meets Reconfigurable Surfaces: RIS-assisted SIC for Full-Duplex Radios

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Abstract—Reconfigurable intelligent surfaces (RIS) are a key enabler of various new applications in 6G smart radio environments. By utilizing an RIS prototype system, this paper aims to enhance self-interference (SI) cancellation for in-band full-duplex (FD) communication systems. SI suppression is a crucial requirement for FD communication as the SI severely limits the performance of a node by shadowing the received signal from a distant node with its own transmit signal. To this end, we propose to assist SI cancellation by exploiting an RIS to form a suitable cancellation signal in the analog domain.

Building upon a 256-element RIS prototype, we present results of RIS-assisted SI cancellation from a practical testbed. Given an initial analog isolation of $44 \, dB$ provided by the antenna design, we are able to cancel the leaked signal by an additional $59 \, dB$ in the narrowband case, resulting in an overall SI suppression of $103 \, dB$ without additional digital cancellation. The presented case study shows promising performance to build an FD communication system on this foundation.

Index Terms—reconfigurable intelligent surface (RIS), fullduplex communication, self-interference cancellation

I. INTRODUCTION

The ever-growing communication demand is expected to reach 5.3 billion total internet users in 2023 according to the Cisco annual internet report (2018-2023) [1]. With this evolution of wireless data transfer and rapidly increasing numbers of devices, e.g., through the internet of things, new communication standards are evolving. With the ongoing standardization of 5G, a new generation of communication systems is currently rolled out. The research attention, therefore, is already drawn to the next generation wireless communications, commonly referred to as 6G. Among many other promising applications and new techniques, research on 6G investigates the use of reconfigurable intelligent surfaces (RIS). The RIS is a synthetic surface able to passively manipulate the reflected signal depending on an electrical reconfiguration of the elements. For the first time, this promising concept allows to deviate from the paradigm of a determined, passive communication channel and rather allows to optimize the channel for a specific application or user. These innovative features qualify the RIS to be a part of an upcoming communication standard [2].

In this paper, we focus on applying the RIS in an inband full-duplex (FD) communication system to enhance selfinterference (SI) cancellation. FD describes the simultaneous transmission and reception on the same carrier frequency. FD is a key enabler for secure key generation [3] and exchange over the air [4], jamming resistant communications [5] and data throughput maximization [2]. However, current state-ofthe-art FD implementations suffer from high implementation cost and high integration effort of SI cancellation techniques in the analog domain.

Addressing this issue, we propose a novel concept which utilizes an RIS to shape a phase-inverted cancellation signal directed to the receive antenna of an FD-node, adaptively reducing the SI by cancellation. This enables a relaxed transceiver design, which reduces implementation costs and takes advantage of the RIS infrastructure that likely will be deployed in 6G networks. Our approach provides all the advantages of FD connectivity while reducing UE complexity, potentially even allowing conventional transceiver architectures to be used for FD communication.

A. Prior work

In the past several concepts for SI cancellation in FD communication systems have been shown [6]–[9]. In this section, we briefly introduce the core concepts and current state-of-the-art.

In [10], Bharadia et al. have shown a customized SI cancellation printed circuit board (PCB) containing a number of fixed delays and variable attenuators. These delayed and attenuated signals bypass an external RF circulator that combines the transmitter and receiver to a single antenna. A control algorithm is used to parameterize the attenuators in order to generate a cancellation signal combating the leakage in the setup. Additionally, a digital cancellation step is employed to eliminate residual leakage. Even though, this setup shows good performance in the presented scenarios, it involves a high number of analog RF components, that, on the one hand, are pricey and, on the other hand, may not be easily integrated into SoCs. Our approach does not rely on external RF components, but rather uses the RIS infrastructure of future radio network environments reducing user equipment (UE) costs significantly.

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In [11], Vogt et al. have shown a scheme, where an external circulator was used to combine the transmitter and receiver on a single antenna. Additionally, an auxiliary transmitter was used to eliminate the residual SI in the analog domain by adding a cancellation signal with an RF combiner to it. Furthermore, a digital cancellation step is employed. This setup, however, requires an external circulator, combiner, and an auxiliary transmitter adding significant cost to the design which we spare in our proposed approach.

In SoftNull [12], the authors present a MIMO approach to FD SI cancellation. They have shown an antenna array of 72 antennas, where some of the antennas are used for transmitting and receiving, while others are used for cancellation. In their paper, the authors exploit knowledge of the channel between the antennas H_{Self} to actively use a subset of antennas to reduce SI on selected antennas of the array. This precoding procedure enables FD on some antennas, however sacrificing some transceiver chains and antennas in the array for cancellation. Furthermore, the array is physically large, limiting the use on mobile UEs. In our approach, we only need a single transmit and a single receive antenna and no additional transceivers.

All of these previously outlined concepts use specialized RF hardware [10], [11], or large antenna arrays [12]. This is why we propose to utilize existing RISs in future 6G networks, relaxing the requirements to specialized hardware on the UE side. This can significantly reduce costs and save complexity at the UE. We propose to turn a regular half-duplex transceiver with two antennas to a full-duplex transceiver temporarily, when needed, e.g., for secret key-exchange. By using channel estimation, the RIS is tuned so that the reflected signal cancels the cancels the SI at the UE and thus enables FD communication.

The remainder of the paper is structured as follows. First, in Section II we introduce our approach to RIS-assisted SI cancellation. Then, we detail the setup of the conducted experimental study and the used hardware. Next, we propose a greedy algorithm for generating a suitable RIS configuration. Finally, in Section IV we provide the results of the experiments and present the insights drawn from them.

II. RIS FOR ANALOG SELF-INTERFERENCE CANCELLATION

The main challenge in in-band FD reception is to cancel the simultaneously transmitted signal of the FD node. If the leakage of the transmitted signal into the receive-chain of the transceiver is high, the utilizable dynamic range of the analog to digital converter in the receiver is reduced drastically. This leakage either overwhelms the received signal of the distant node or the degraded SNR reduces data throughput significantly.

Here, our approach is to reduce the SI by utilizing an RIS to form a suitable cancellation signal combating the leakage. An FD node with two antennas is considered, one antenna to transmit and one to receive, respectively. Utilizing two spatially separated antennas on an FD node yields an amount of initial analog isolation between transmitter and receiver



Fig. 1: System Overview

chain due to antenna design. This setup results in a leaked signal to the receiver, with a signal power P_{leaked} given by

$$P_{\text{leaked}} = P_{\text{tx}} - \alpha_{\text{iso}},\tag{1}$$

where α_{iso} (in dB) is the initial analog isolation representing the total leakage between the transmit and receive path, including path loss, antenna gains, cable losses, and internal coupling. P_{tx} (in dBm) is the transmit power of the FD node. The received signal power stemming from the distant FD node at the receive antenna is attenuated by approximately the free space path loss between both FD nodes.

The received signal power (also in dBm) is given by

$$P_{\rm rx} = P_{\rm tx} - 20 \log_{10}(d) - 20 \log_{10}(f) -20 \log_{10}\left(\frac{4\pi}{c_0}\right) + G_t + G_r,$$
⁽²⁾

where d is the distance between both FD nodes, f is the carrier frequency and G_t and G_r (in dBi) are the antenna gains, respectively. The received power $P_{\rm rx}$ is usually orders of magnitude lower than the received power due to SI, $P_{\rm leaked}$. That is why we propose to utilize the RIS to form a phase-shifted cancellation signal to further reduce $P_{\rm leaked}$. The reflected signal of the RIS r can be modeled as [13]

$$r = \left[\sum_{i=1}^{N} h_i e^{j\phi_i} g_i\right] x,\tag{3}$$

where ϕ_i is the phase shift introduced by the *i*th element of the RIS, h_i and g_i are the channels from the transmitter to the *i*-th element of the RIS and from the *i*-th element to the receiver, respectively. N is the number of total elements of the RIS and x is the transmit signal. From a link budget perspective, given a perfect RIS cancellation signal and neglecting non-linearities, this leads to

$$P_{\rm si} = P_{\rm tx} - \alpha_{\rm iso} - P_{\rm RIS},\tag{4}$$

where P_{si} is the residual self-interference power and P_{RIS} is the power reflected from the RIS to the receive antenna (both in dBm).



Fig. 2: Physical dimensions, and driver circuit of an RIS unit cell.

The goal of the case study presented in this paper is to determine an RIS parameter setting, at which P_{leaked} and P_{RIS} cancel each other out as much as possible. To achieve maximum cancellation, the reflected signal must not only be phase inverted with respect to the leaked signal, but also have the same amplitude. If the power of the reflected signal is higher than the power of the leaked signal, then the reflected signal overshoots and the minimum cannot be reached. These combined requirements make finding a good RIS setting very challenging. In the remainder of the paper, we present results gathered from a prototypical lab setup.

III. EXPERIMENTAL STUDY SETUP

In this section, we describe the RIS, the transceiver system, the methodology of RIS configuration, and the experimental setup.

A. The RIS prototype

The RIS used is a 16×16 array of unit cells with identical structure embedded on a PCB. The resonance frequency of each individual unit cell is binary-switchable, resulting in different reflection coefficients with different phase for the two switching states. The unit cell consists of a rectangular patch reflector and a contiguous ground plane. The length of the patch is in the order of half a wavelength and thus defines the resonance frequency of the unloaded patch reflector. A via connects the patch on the top side to the anode of a PIN diode on the bottom side of the RIS. The cathode of the PIN diode is connected to the ground plane. The via is not positioned in the center of the patch, leading to an altered electrical length of the patch reflector when the PIN diode is biased and shorts the via to ground. This results in a binary-switchable resonance frequency of the unit cell.

The physical dimensions as well as the driver circuitry of the unit cell are shown in Fig. 2. For the substrate, standard low-cost FR4 with a dielectric constant of $\epsilon_r \approx 4.5$ and a dissipation factor of $\tan \delta \approx 0.02$ is used. The PIN diode is of type SMP1320-079LF from manufacturer Skyworks. The biasing resistor has a value of $R = 4.7 \,\mathrm{k\Omega}$, resulting in a forward current through the PIN diode of $I_f = 1 \,\mathrm{mA}$. The decoupling capacitor has a value of $C = 100 \,\mathrm{pF}$. A biasing voltage of $V_b = 5 \,\mathrm{V}$ or $V_b = 0 \,\mathrm{V}$ is applied in the ON state or the OFF state, respectively. This unit cell is an evolution of our prior work from [14].



Fig. 3: Measured phase response of the RIS.

The measured phase response of the RIS is shown in Fig. 3. This is the phase difference of the surface reflection coefficient between the RIS configured as all-ON and all-OFF states. The measurement was taken under the condition that the incident wave vector and the reflected wave vector are perpendicular to the surface.

B. Transmitter and Receiver

To verify the effect of the RIS on the SI we use a vector network analyzer (VNA) of type E5071C ENA from Manufacturer Agilent Technologies [15] first and later transfer our ground truth measurements from the VNA to software-defined radios (SDRs). The SDRs are Ettus Research X310 [16] with CBX-120 daughterboards [17] suitable for the 5 GHz passband frequency of the RIS. The self-interference due to leakage in the antenna and transceiver assembly is investigated by analysis of the transmission coefficient s_{21} between transmit and receive antenna.

C. Methodology of RIS switching

The 16×16 RIS used during our experiments offers discrete switching states for all 256 elements. This results in 2^{256} possible configuration states. Due to this enormous set of configurations, an exhaustive search of all possible combinations is infeasible. We therefore propose a greedy heuristic to converge to a local optimal RIS setting. The pseudocode of the proposed algorithm is given in Algorithm 1.

We consider two variants of the algorithm. First, the 'Narrowband'-variant, where the algorithm minimizes the SI at a single frequency point, which is 5.385 GHz throughout this paper. Second a variant, that evaluates the SI at multiple equidistand frequency points. The boolean variable 'Narrowband' in algorithm 1 distinguishes both variants. In the remainder, we label the corresponding results 'Narrowband* or with the configured bandwidth '5 MHz' or '10 MHz' The greedy algorithm utilizes two buffers of size *B*. The buffer $i_{B\times 1}$ holds the *B* best SI magnitude readings found so far, the buffer $S_{B\times N_x\times N_y}$ contains the corresponding RIS configuration. The greedy search starts with an initialization phase, where *B* random RIS configurations are iteratively generated and the

Algorithm 1 Greedy algorithm for RIS setting optimization

Input: Buffer size *B*, RIS dimension *N*, f_{tone} , termination threshold t_e

Output: Best RIS setting and corresponding self-interfence

Initialisation : Termination counter $t_c = 0$ for q = 0 to B - 1 do S[q,:,:]=randomBoolean $(0.5 \cdot \text{ones}(N_x, N_y))$ i[q]=evaluateRISsetting(S[q,:,:]) end for ▷ Sort according to SI magnitude [i, idx] = sort(i)S=S(idx)Main Loop : while $t_c < t_e$ do $\boldsymbol{S}_w = \boldsymbol{S} \cdot (1:100)$ \triangleright Linear weighting $P = sum(\boldsymbol{S}_{\boldsymbol{w}}, 1)$ ▷ Sum along first dimension $P_{\text{norm}} = P./\frac{B^2 + B}{2}$ S_{c} =randomBoolean(P_{norm}) $I_{\rm c}$ =evaluateRISsetting($S_{\rm c}$) if $I(0) < I_c$ then ▷ New best SI found? $t_c = t_c + 1$ else $t_c = 0$ end if if $i(B-1) > I_c$ then ▷ Add to buffer $\boldsymbol{i}(B-1) = I_{\rm c}$ $S(B-1,:,:) = S_{c}$ [i, idx] = sort(i)S = S(idx)end if end while **return** [**S**[0, :, :]; **i**(0)] ▷ Return best Setting and SI function evaluateRISsetting(RISsetting) Set RIS hardware state to RISsetting Evaluate SI-Amplitude with VNA or SDR

return max(SI-Amplitude) end function

corresponding SI magnitude for every setting is measured with the VNA. The configurations and SI magnitudes are added to the buffers i and S. After acquiring B initial measurements, the buffered data is used to calculate a ratio P for every element, expressing how many times the surface is active.

Therefore, in a loop, first the buffers *i* and *S* are sorted by means of the ascending SI magnitude. The sorted settings buffer *S* is now linearly weighted, where the setting with the lowest SI magnitude in the buffer gets the highest weight *B* and the setting with the highest SI magnitude in the buffer is weighted with 1. The weighted settings buffer S_w is now summed up along the first dimension, and normalized in the range of 0 to 1 by dividing by $\frac{B^2+B}{2}$. The resulting $N_x \times N_y$ dimensional array P_{norm} contains a ratio, how often each



Fig. 4: Photography of the RIS setup in a typical office environment. The distance between antennas on the right side and the RIS on the left side is 1 m.

element was active with higher weighting on the settings with lower SI magnitude. The ratios P_{norm} are now used to generate a new configuration for the RIS. With the new setting applied to the surface, the SI magnitude is measured. If the new reading is lower than the highest SI magnitude in the buffer the new reading replaces this reading in both buffers Sand i. The loop repeats until a predefined number of iterations t_e , without a new best setting found have passed.

For a narrowband system a single test tone / frequency point is used to evaluate the SI magnitude. To broaden the bandwidth for OFDM systems multiple equidistant frequency points across the desired bandwidth are considered, of which the highest SI magnitude in each loop is used. By always using the maximum over all frequency points we can ensure the SI magnitude to be at least as low or lower across the specified band, as the value our algorithm converges to.

D. Experimental setup:

The lab setup we investigate is shown in Fig. 4 and Fig. 5a. The RIS is mounted vertically on a flat office desk in a typical office space of 18 m^2 .

In a distance of 100 cm to the surface, two vertically polarized planar elliptical dipole ultra wideband antennas, specifically BroadspecTM antennas of manufacturer Time Domain, now Humatics, [18] are placed. The two dipole antennas are used as the transmitting and receiving antenna interchangeably. They connect to the VNA or SDR with 1 m RG-142 cables. The normalized antenna characteristic of the antennas at the carrier frequency of 5.385 GHz used during the experiments is shown in Fig. 5b. As can be seen from the diagram at an angle of 180°, the antenna' pattern is attenuated by approx. 22 dB. Thus, it is a good choice to place both antennas facing outwards, as shown in Fig. 5a. The transmit and receive antenna therefore exploit the antenna pattern to yield good initial analog isolation, while keeping the antennas SMAconnectors at a distance of 25 mm, which is approximately $\lambda/2$. This is a realistic spacing for mounting the antennas on a regular UE.





(a) Antenna assembly

Fig. 5: On the left, a detailed photography of the antenna arrangement is shown. The distance between the SMA connectors is 25 mm, approx. $\lambda/2$. On the right, the measured antenna diagram in the elevation plane is given for a frequency of 5.385 GHz. 180° of elevation refers to the SMA-Connector side and 0° to the upper edge of the pcb antenna.

Both the VNA or SDR and the RIS connect to a host computer running LabView ensuring proper synchronization in between switching of the RIS and measurement of the channel.

IV. EXPERIMENT RESULTS

In this section, we present the experimental results of the conducted case study.

A. Antenna initial isolation

To determine the baseline performance of the antenna assembly in realistic indoor scenarios with multi-path reflections, we conduct a brief field trial. The antenna assembly is hooked up to the VNA and is moved around in the office space on a lab cart. While the transmission parameter s_{21} between the antennas is measured 2 times per second, the cart is pushed around on a randomized trajectory. In total 1200 samples are collected within 10 min. We found the average leakage from the transmit to the receive antenna to be around -44 dB, with a maximum reached at -32 dB for rare cases. This gives us a baseline performance for the analog isolation of approximately 44 dB provided by the antenna setup alone, which we aim to further reduce by utilizing the RIS.

B. RIS assisted SI cancellation

With the RIS separated by 1 m from the antenna assembly, as shown in Fig. 4, the residual SI is measured by evaluating the transmission parameter s_{21} . First, the RIS is configured with plain random configurations, while the best SI magnitude value is kept in the buffer. The cumulative minimum of this approach, indicating the highest SI cancellation, is shown in the convergence plot in Fig. 6 as the graph 'Random'.



Fig. 6: Convergence plot of the greedy algorithm on the lab setup. The shown numbers are averaged from 100 runs of the greedy algorithm for each case. The noise floor of the VNA is $-124 \, \text{dBm/Hz}$ in the evaluated frequency band.



Fig. 7: Measured transfer function at the antenna from transmitter to the receiver for the best RIS configuration found with the greedy algorithm.

It is visible that by guessing random configurations of the RIS no significant improvement is achieved. The minimal SI magnitude reached with this approach is -46 dB. That is why we propose to use a greedy approach, as described in section III-C. The setup is kept exactly the same, but this time our greedy algorithm is used to optimize the RIS setting. The results are shown in Fig. 6 'Narrowband'. The cumulative minimum of our proposed algorithm converges within approximately 1500 iterations to a very low residual SI magnitude of -95 dBm, which further decreases with more iterations to -103 dBm.

Since our algorithm has a stochastic component, the num-

bers shown in Fig. 6 are averages of a total of 100 runs of the greedy algorithm to obtain meaningful data. Each run of the algorithm is run until no improvement is found for t_e iterations. Fig. 6 shows the averaged values for the first 5000 iterations. In our current LabView implementation with the USRPs, we reach a loop rate of 25 Hz, which is mainly limited by the host-driven setup and interface latency. Converging to a good SI cancellation therefore is achieved under a minute with potential for improvement. The utilizable bandwidth for the best setting found by our algorithm is given in Fig. 7 'Narrowband'. The algorithm optimizes the residual SI at the center frequency of 5.385 GHz. It is visible for the narrowband case that the best cancellation is achieved at a very small bandwidth, which is expected since our algorithm optimizes to a single frequency.

To broaden the usable bandwidth for, e.g., an OFDM signal we modify our algorithm to use a set of equidistant frequency points within a desired bandwidth. From all considered frequency points in every iteration the highest SI magnitude is used for the next greedy steps. This ensures, that the residual SI magnitude is at least the converged value over the desired bandwidth. We evaluate this approach for $5 \,\mathrm{MHz}$ and $10 \,\mathrm{MHz}$ of desired bandwidth. In Fig. 6, we can see that for the $5 \,\mathrm{MHz}$ and $10 \,\mathrm{MHz}$ case, the algorithm is still able to add a significant amount of SI cancellation over the purely random case. However, the lowest achievable SI is $-76 \,\mathrm{dBm}$ for a $10 \,\mathrm{MHz}$ band and $-80 \,\mathrm{dBm}$ for a $5 \,\mathrm{MHz}$ band. Fig. 7 shows the corresponding transfer function, where it is visible, that the bandwidth constraint is met. The 5 MHz band and 10 MHzband are layed out symmetrical around the center frequency of 5.385 GHz. Within the band the SI magnitude is at least the convergence value given in Fig. 6, but often is lower in the considered interval. We can conclude that we can broaden the bandwidth in exchange for higher SI magnitude. However, the cancellation effect of the RIS is still high with an additional $30 \,\mathrm{dB}$ cancellation over the case without the RIS for $10 \,\mathrm{MHz}$ of bandwidth.

All described measurements were conducted two times, first with the VNA and later on with the X310 to speed up the implementation moving towards a real-time application of the concept. We were able to reproduce the measurements with the USRPs at 5.385 GHz, while holding the gain setting of them fixed. For brevity and accuracy, we only show the results of the VNA.

CONCLUSION

In this paper, we have shown that SI cancellation for FD communication systems can be significantly enhanced by utilizing an RIS. We conducted a case study on a prototype system, which showed very promising performance in a lab scenario. Furthermore, we have proposed a greedy algorithm to generate an optimized RIS configuration in reasonable time. In addition, we modified the proposed algorithm to allow higher bandwidth and have shown that in exchange for some SI cancellation we can broaden the utilizable bandwidth. The presented approach seems to be very promising with

the potential to save costs in FD transceivers and reduce the complexity of analog transceiver hardware, possibly even adopt commodity hardware in the future.

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