# Synthesizing Averaged Virtual Oscillator Dynamics to Control Inverters with an Output LCL Filter

M. Ali\*, H. I. Nurdin and J. E. Fletcher

School of Electrical Engineering and Telecommunications, University of New South Wales, Australia

\*m.ali@unsw.edu.au

Abstract-In commercial inverters, an LCL filter is considered an integral part to filter out the switching harmonics and generate a sinusoidal output voltage. The existing literature on the averaged virtual oscillator controller (VOC) dynamics is for current feedback before the output LCL filter that contains the switching harmonics or for inductive filters ignoring the effect of filter capacitance. In this work, a new version of averaged VOC dynamics is presented for islanded inverters with current feedback after the LCL filter thus avoiding the switching harmonics going into the VOC. The embedded droopcharacteristics within the averaged VOC dynamics are identified and a parameter design procedure is presented to regulate the output voltage magnitude and frequency according to the desired ac-performance specifications. Further, a power dispatch technique based on this newer version of averaged VOC dynamics is presented to simultaneously regulate both the active and reactive output power of two parallel-connected islanded inverters. The control laws are derived and a power security constraint is presented to determine the achievable power setpoint. Simulation results for load transients and power dispatch validate the proposed version of averaged VOC dynamics.

*Index Terms*—Virtual oscillator control, averaged model, LCL filter, inverter control, power dispatch, security constraint.

#### I. INTRODUCTION

Power systems are going through a transitional period where the conventional synchronous generators are being replaced by the renewable energy sources (RESs). The rapid integration of RESs into the power system has resulted in a need to investigate control techniques to stabilise and regulate the voltage and frequency for systems with low or zero inertia. Existing techniques for systems with RESs include droop control [1], [2], proportional resonant (PR) control [3], [4] and recently proposed virtual oscillator control (VOC) [5]–[7].

Virtual oscillator is a nonlinear Van der Pol oscillator that exhibits nearly sinusoidal oscillations in the steady-state. In contrast to existing inverter control techniques, virtual oscillator controller only requires the inverter output current as a feedback and regulates the output voltage (magnitude and frequency) according to the desired ac-performance specifications. In [5], an averaged VOC model is derived to explicitly identify the P - V and  $Q - \omega$  droop-characteristics embedded within the averaged dynamics of the virtual oscillator controller. Recent literature on the dispatchability of virtual oscillator controlled inverters can be found in [8]–[14].

The averaged VOC model presented in [5] is derived when there is a current feedback to the controller before the output

filter. However, this model is not an accurate representation when there is a current feedback after the output LCL filter. In case of commercial inverters, an output LCL (or LC) filter is always considered an essential part to filter out the switching harmonics and improve the quality of output voltage. In this work, a new version of averaged VOC dynamics is presented that takes into account the LCL filter at the output of an inverter. In the proposed control scheme, the current is feedback after the LCL filter thus avoiding the switching harmonics going into the virtual oscillator controller. The contribution of this work is twofold. At first, an averaged VOC model is derived for an inverter with current feedback after the output LCL filter. The updated embedded droop-characteristics within the proposed averaged VOC dynamics are presented and corresponding equilibrium points are derived. A system's parameter design procedure has been presented to regulate the inverter's output voltage magnitude and frequency according to the desired ac-performance specifications. In order to validate the proposed averaged VOC dynamics, simulation results are presented for a number of scenarios including rise time, harmonics analysis, droop characteristics and load transients. It has been shown that for all the scenarios, the proposed averaged VOC model predicts the actual VOC (Van der Pol) dynamics accurately. A comparison with the previously reported averaged VOC model [5] is also presented. The previously derived averaged VOC dynamics [5] starts to differ more from the actual VOC dynamics (with current feedback after the LCL filter) for a large value of filter capacitance. This is due to the fact that a higher value of filter capacitance draws more current and results in a significant difference between the current before and after the filter.

The second contribution is the extension of power dispatch technique [9] to this new version of averaged VOC dynamics. The updated control laws are derived based on this new model to determine the control inputs corresponding to a particular power set-point. Further, an updated power security constraint is derived to determine the feasible power set-points. Using this constraint, it can be determined a-priori if a particular power set-point is achievable or not. Simulation results are presented for a number of power dispatch scenarios to validate the proposed power dispatch technique.

The rest of the paper is organised as follows. In Section II, an overall system description is presented. In Section III, an averaged VOC model is derived for inverters with current

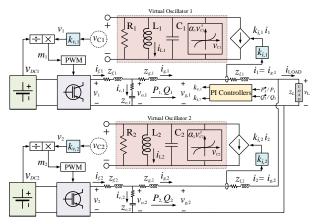


Figure 1. A overview of the overall system consisting of two parallelconnected virtual oscillator controlled inverters. The inverter 1 can dispatch power using the PI controllers while inverter 2 supplies the remaining power.

feedback after the LCL filter. In Section IV, a VOC parameter design procedure is discussed. In Section V, a power dispatch technique is presented. In Section VI, simulation results are presented. In Section VII, conclusion is drawn.

## **II. SYSTEM DESCRIPTION**

The system considered consists of two virtual oscillator controlled inverters with current feedback after the output LCL filter. A complete system overview is shown in Fig. 1. The filter parameters are  $z_f = R_f + j\omega^* L_f$ ,  $z_c = R_c + \frac{1}{j\omega^* C_f}$ and  $z_g = R_g + j\omega^* L_g$ . The line impedance is represented as  $z_l = R_l + j\omega^* L_l$ . The inverters are connected to a common RL load  $z_L = R_L + j\omega^* L_L$  through the point of common coupling (PCC). Moreover, inverter 1 can dispatch power by the use of two additional PI controllers.

## **III. SYSTEM MODELLING**

In this section, the VOC dynamics are presented. Further, an averaged VOC model is derived for inverters with current feedback after the output LCL filter. The dynamics of the PI controllers used for power dispatch are the same as in [9].

## A. Virtual Oscillator Controller

A virtual oscillator consists of an LC harmonic oscillator with a fundamental frequency  $\omega^* = 1/\sqrt{LC}$ . It consists of a negative resistance element  $R = -1/\sigma$  and a nonlinear current source with a positive constant  $\alpha$  as shown in Fig. 1. The feedback current *i* is scaled by the current feedback gain  $k_i$  before entering the virtual oscillator. The  $v_C$  denotes the capacitor voltage and  $i_L$  denotes the inductor current. The capacitor voltage  $v_C$  is scaled by the voltage scaling factor  $k_v$  to generate the inverter output voltage *v*. The actual VOC dynamics are given by the following dynamic equations [5]:

$$\frac{dV}{dt} = \frac{\epsilon\omega^*}{\sqrt{2}} \left( \sigma g \left( \sqrt{2}V \cos\left(\phi\right) \right) - k_v k_i i \right) \cos(\phi), \\ \frac{d\phi}{dt} = \omega^* - \frac{\epsilon\omega^*}{\sqrt{2}V} \left( \sigma g \left( \sqrt{2}V \cos(\phi) \right) - k_v k_i i \right) \sin(\phi).$$
(1)

A detailed derivation and parametric description of the dynamical model for the inverter RMS output voltage magnitude V and instantaneous phase angle  $\phi = \omega t + \theta$  where  $\theta$  is the phase offset with respect to  $\omega t$ , can be found in [5].

## B. Averaged VOC Model for Inverters with Output LCL Filter

A new version of averaged VOC dynamics is presented for inverters with current feedback after the output LCL filter. A brief derivation is in Appendix A. The proposed averaged VOC model is given by the following dynamic equations:

$$\frac{d}{dt}\overline{V} = \frac{\sigma}{2C}\left(\overline{V} - \frac{\beta}{2}\overline{V}^3\right) - \frac{k_v k_i}{2C}\left(\frac{C_\alpha \overline{P}}{\overline{V}} + \frac{S_\alpha \overline{Q}}{\overline{V}} + C_\beta \overline{V}\right), \quad (2)$$

$$\frac{d}{dt}\overline{\theta} = \omega^* - \omega + \frac{k_v k_i}{2C} \left( \frac{C_\alpha \overline{Q}}{\overline{V}^2} - \frac{S_\alpha \overline{P}}{\overline{V}^2} - S_\beta \right),\tag{3}$$

where  $\beta = \frac{3\alpha}{k_v^2 \sigma}$ ,  $\overline{V}$  denotes the averaged RMS output voltage magnitude and  $\overline{\theta}$  denotes the averaged phase offset with respect to  $\omega t$ . The averaged active and reactive power are denoted by  $\overline{P}$  and  $\overline{Q}$ , respectively.  $C_{\alpha}, C_{\beta}, S_{\alpha}$  and  $S_{\beta}$  are the impedance constants (as defined in Appendix A).

1) Voltage Regulation Characteristics: The equilibrium value for the averaged RMS voltage magnitude  $\overline{V}_{eq}$  can be determined by setting the left hand side of (2) to zero and solving for  $\overline{V}$  as follows:

$$\overline{V}_{eq} = k_v \sqrt{\frac{\sigma_\beta \pm \sqrt{\sigma_\beta^2 - 6\alpha \left(k_i/k_v\right) \left(C_\alpha \overline{P}_{eq} + S_\alpha \overline{Q}_{eq}\right)}{3\alpha}}, \quad (4)$$

where  $\sigma_{\beta} = (\sigma - k_v k_i C_{\beta})$ . The  $\overline{P}_{eq}$  and  $\overline{Q}_{eq}$  denote the active and reactive power at the equilibrium. Both the roots of (4) are real valued if the following inequality holds:

$$\left(C_{\alpha}\overline{P} + S_{\alpha}\overline{Q}\right) < S_{cr} := \frac{\sigma_{\beta}^2}{6\alpha \left(k_i/k_v\right)},$$
(5)

where  $k_i > 0$  and  $k_v > 0$ .  $S_{cr}$  denotes the critical power constant with corresponding critical value of the inverter RMS output voltage  $\overline{V}_{cr} = k_v \sqrt{\frac{\sigma_{\beta}}{3\alpha}}$ . A local stability analysis of the high voltage solution of (4) (similar to [5]) is not considered. In the subsequent analysis, we assume that the high voltage solution of (4) is locally asymptotically stable and with a slight abuse of notation, we denote it by  $\overline{V}_{eq}$ . Using (4), the open circuit voltage  $\overline{V}_{oc} = k_v \sqrt{\frac{2\sigma_{\beta}}{3\alpha}}$  for the VO-controlled inverter (i.e.  $\overline{P}_{eq} = \overline{Q}_{eq} = 0$ ). 2) Frequency Regulation Characteristics: Solving the

2) Frequency Regulation Characteristics: Solving the phase angle dynamics (3) for equilibrium point gives the system's frequency  $\omega_{eq}$  in steady-state as follows:

$$\omega_{eq} = \omega^* + \frac{k_v k_i}{2C} \left( \frac{C_\alpha \overline{Q}_{eq}}{\overline{V}_{eq}^2} - \frac{S_\alpha \overline{P}_{eq}}{\overline{V}_{eq}^2} - S_\beta \right), \tag{6}$$

where  $\overline{V}_{eq}$  is the high voltage solution of (4).

3) Dynamic Response: In order to quantify the dynamic response of the VO-controlled inverter, the time taken by the inverter to reach its open circuit voltage  $\overline{V}_{oc}$  is considered. The following voltage dynamics of interest in a variable-separable ODE form are obtained from (2) by replacing  $\overline{P} = \overline{Q} = 0$ :

$$\frac{d}{dt}\overline{V} = \frac{\sigma}{2C}\left(\overline{V} - \frac{\beta}{2}\overline{V}^3\right) - \frac{k_v k_i C_\beta}{2C}\overline{V}.$$
(7)

The rise time  $t_{rise}$  is the time taken by the inverter to buildup output voltage from  $0.1\overline{V}_{oc}$  to  $0.9\overline{V}_{oc}$ . The  $t_{rise} \approx \frac{6}{\omega^* \epsilon \sigma_{\beta}}$ is determined by separating variables in (7) and integrating under the limits from  $0.1\overline{V}_{oc}$  to  $0.9\overline{V}_{oc}$ , where  $\epsilon = \sqrt{L/C}$ .

# C. PI Controller Dynamics

The PI controller dynamics used to regulate active power are  $k_v = K_P^p(\overline{P} - P^*) + e_p$  where  $\dot{e}_p = K_I^p(\overline{P} - P^*)$ . Similarly, the PI controller dynamics used to regulate reactive power are  $k_i = K_P^q(\overline{Q} - Q^*) + e_q$  where  $\dot{e}_q = K_I^q(\overline{Q} - Q^*)$ . The  $e_p$  and  $e_q$  are the PI controllers' states [9].

# IV. VOC PARAMETER DESIGN PROCEDURE

A parameter design procedure is presented for VOcontrolled inverters with current feedback after the LCL filter. The parameters are selected such that the VO-controlled inverter satisfy the desired ac-performance specifications.

#### A. Designing the Scaling Factors

The voltage scaling factor  $k_v$  is chosen equal to  $\overline{V}_{oc}$  to standardise the design procedure such that the virtual oscillator capacitor voltage is equal to 1 V RMS when the inverter's output voltage is equal to  $\overline{V}_{oc}$ . The current feedback gain  $k_i$ is chosen as the ratio of  $\overline{V}_{min}$  to  $\overline{S}_{max}$ . The  $\overline{V}_{min}$  corresponds to the constant  $\overline{S}_{max}$  defined by:

$$\overline{S}_{max} = \max_{\overline{P}^2 + \overline{Q}^2 \le |\overline{S}_{rated}|^2} \left( C_\alpha \overline{P} + S_\alpha \overline{Q} \right).$$
(8)

By choosing the gains as:

$$k_v := \overline{V}_{oc}, \qquad k_i := \frac{\overline{V}_{min}}{\overline{S}_{max}},$$
(9)

the parallel-connected inverters in a system share the power proportional to their power ratings [5], [6].

## B. Designing the Voltage Regulation Parameters

A design procedure for the virtual oscillator negative resistance element  $R = \frac{-1}{\sigma}$  and the coefficient of nonlinear current source  $\alpha$  is presented. The proposed design procedure ensures the RMS output voltage to stay within the range:  $\overline{V}_{min} \leq \overline{V}_{eq} \leq \overline{V}_{oc}$  for  $\overline{S}_{max} \geq (C_{\alpha}\overline{P}_{eq} + S_{\alpha}\overline{Q}_{eq}) \geq 0$ . The definition of  $V_{oc}$  and the choice of  $k_v$  in (9) results in  $\sigma = \frac{3\alpha}{2} + k_i k_v C_{\beta}$ . Substituting  $(C_{\alpha}\overline{P}_{eq} + S_{\alpha}\overline{Q}_{eq}) = \overline{S}_{max}$ ,  $\overline{V}_{eq} = \overline{V}_{min}$ ,  $k_v$  and  $k_i$  as in (9),  $\alpha = \frac{2}{3}\sigma_{\beta}$  and  $\sigma_{\beta} = (\sigma - k_v k_i C_{\beta})$  in the high voltage solution of (4), we get:

$$\sigma = \frac{\overline{V}_{oc}}{\overline{V}_{min}} \frac{\overline{V}_{oc}^2}{\overline{V}_{oc}^2 - \overline{V}_{min}^2} + \frac{\overline{V}_{min}\overline{V}_{oc}C_{\beta}}{\overline{S}_{max}}.$$
 (10)

# C. Designing the Harmonic Oscillator Paramters

In order to derive a set of constraints to determine the parameters L and C, the frequency regulation characteristics (6), the rise time  $t_{rise}$  and the ratio of the amplitude of the third harmonic to the fundamental [5, Eq. 41] are considered. While designing the harmonic oscillator parameters, one of the design input is the maximum permissible frequency deviation  $|\Delta \omega|_{max}$ . Let us start with the frequency regulation characteristics in (6) and define the following constant:

$$\overline{S}_{|\Delta\omega|_{max}} = \max_{\overline{P}^2 + \overline{Q}^2 \le |\overline{S}_{rated}|^2} \left( C_\alpha \overline{Q} - S_\alpha \overline{P} \right).$$
(11)

Using the worst-case operating condition for the output voltage (corresponding to  $\overline{S}_{max}$  that results in the minimum permissible voltage  $\overline{V}_{min}$ ) and substituting the scaling factors from (9) into (6), the lower bound on capacitance C is given by:

$$C \ge \frac{1}{2|\Delta\omega|_{max}} \left( \frac{\overline{S}_{|\Delta\omega|_{max}} \overline{V}_{oc}}{\overline{V}_{min} \overline{S}_{max}} - \frac{S_{\beta} \overline{V}_{oc} \overline{V}_{min}}{\overline{S}_{max}} \right) =: C_{|\Delta\omega|_{max}}^{min}.$$
(12)

Define the maximum permissible rise time  $t_{rise}^{max}$  as the design input. Now, considering the  $t_{rise}$  of an unloaded inverter and (10), the upper bound on the capacitance C is defined as:

$$C \le \frac{t_{rise}^{max}}{6} \frac{\overline{V}_{oc}}{\overline{V}_{min}} \frac{\overline{V}_{oc}^2}{\overline{V}_{oc}^2 - \overline{V}_{min}^2} =: C_{t_{rise}^{max}}.$$
 (13)

Finally, the third design input is the maximum-permissible ratio of the amplitude of third harmonic to the fundamental  $\delta_{3:1}^{max}$  where  $\delta_{3:1} = \frac{\epsilon\sigma}{8}$  as defined in [5, Eq. 41]. Replacing (10) in the expression for  $\delta_{3:1}^{max}$ , we get another lower bound on the capacitance C given by:

$$C \ge \left(\frac{1}{8\omega^*\delta_{3:1}^{max}}\right) \left(\frac{\overline{V}_{oc}}{\overline{V}_{min}}\frac{\overline{V}_{oc}^2}{\overline{V}_{oc}^2 - \overline{V}_{min}^2} + \frac{\overline{V}_{min}\overline{V}_{oc}C_{\beta}}{\overline{S}_{max}}\right) =: C_{\delta_{3:1}}^{min}.$$
(14)

The constraints (12)-(14) define a permissible range for the capacitance satisfying the desired frequency regulation, rise time and harmonic distortion specifications. The permissible range of capacitance C can be written as:

$$max\{C^{min}_{|\Delta\omega|_{max}}, C^{min}_{\delta_{3:1}}\} \le C \le C_{t^{max}_{rise}}.$$
(15)

Once the value is chosen for the capacitance C, the inductance can be determined as  $L = \frac{1}{C(\omega^*)^2}$ . Note that it may be possible (15) does not hold for the set of design inputs  $\{|\Delta \omega|_{max}, t_{rise}^{max}, \delta_{3:1}^{max}\}$  and necessitates a design trade-off.

# V. POWER DISPATCH

The power dispatch technique presented in [9] is extended to the averaged VOC model with current feedback after the output LCL filter. Inverter 1 simultaneously regulates both the active and reactive power according to the desired power setpoint  $(P_1^*, Q_1^*)$  using the PI controllers that continuously tune the VOC parameters  $k_{v,1}$  and  $k_{i,1}$  as in [9].

# A. Power Security Constraint

An updated power security constraint is derived to determine the achievable power set-points and guarantee the existence of real valued control inputs.

**Theorem 1.** Assuming the averaged model of two VOC inverters with current feedback after the output LCL filter that synchronise to a common frequency, and are connected to a common fixed impedance load  $z_L$  through line impedance values  $z_{l,1}$  and  $z_{l,2}$ , respectively, the desired output power set-point  $\overline{P}_1^*$  and  $\overline{Q}_1^*$  for inverter 1 can be achieved and there exists corresponding real-valued current feedback gain  $k_{i,1}^t$  and voltage scaling factor  $k_{v,1}^t$ , if the following security constraint is satisfied for the averaged VOC-dynamics:

$$\sigma_1 \overline{V}_1^{\iota^2} - \mu \left( C_{\alpha,1} \overline{P}_1^* + S_{\alpha,1} \overline{Q}_1^* + C_{\beta,1} \overline{V}_1^{\iota^2} \right) > 0, \quad (16)$$

where,

$$\mu = k_{v,1}^{\iota} k_{i,1}^{\iota} = k_{v,2} k_{i,2} \frac{\left(\frac{C_{\alpha,2} \overline{Q}_{2}^{\iota} - S_{\alpha,2} \overline{P}_{2}^{\iota}}{\overline{V}_{2}^{\iota 2}} - S_{\beta,2}\right)}{\left(\frac{C_{\alpha,1} \overline{Q}_{1}^{*} - S_{\alpha,1} \overline{P}_{1}^{*}}{\overline{V}_{1}^{\iota 2}} - S_{\beta,1}\right)}.$$
 (17)

 $\overline{V}_2^{\iota}$  and  $\overline{Q}_2^{\iota}$  denote the steady-state averaged output voltage magnitude and reactive power supplied by inverter 2, respectively, corresponding to the desired power set-point  $(\overline{P}_1^*, \overline{Q}_1^*)$ .

The proof of this theorem is similar to [9, Theorem 1] and is omitted due to the space limitation. The updated control laws

Table I System Parameters

Symbol	Parameter	Inverter 1	Inverter 2	Unit
$k_i$	Current feedback gain	0.15225	0.15225	A/A
$k_v$	Voltage scaling factor	126	126	V/V
$\sigma$	Conductance	6.09256	6.09256	$\Omega^{-1}$
$\alpha$	Cubic-current source coefficient	4.06184	4.06184	$A/V^3$
L	Oscillator inductance	34.661	34.661	$\mu H$
C	Oscillator capacitance	0.203	0.203	F

Table II AC PERFORMANCE SPECIFICATIONS

Symbol	Parameter	Value	Unit
$\overline{V}_{oc}$	RMS open-circuit voltage	126	v
$\overline{V}_{min}$	RMS rated power voltage	114	v
$\overline{P}_{rated}$	Rated real power	750	W
$ \overline{Q}_{rated} $	Rated reactive power	750	VAr
$\omega^*$	Nominal oscillator frequency	$2\pi 60$	rad/s
$ \Delta \omega _{max}$	Maximum frequency offset	$2\pi 0.5$	rad/s
$t_{rise}^{max}$	Maximum rise time	0.2	s
$\delta_{3:1}^{max}$	3 <sup>rd</sup> to 1 <sup>st</sup> harmonic ratio	1	%

to determine the control inputs  $k_{v,1}^{\iota}$  and  $k_{i,1}^{\iota}$  corresponding to the desired power set-point  $(\overline{P}_{1}^{*}, \overline{Q}_{1}^{*})$  are:

$$k_{v,1}^{\iota} = \pm \sqrt{\frac{(\sigma_{1} - \mu C_{\beta,1}) \overline{V}_{1}^{\iota \, 4}}{(\sigma_{1} - \mu C_{\beta,1}) \overline{V}_{1}^{\iota \, 2} - \mu \left(C_{\alpha,1} \overline{P}_{1}^{*} + S_{\alpha,1} \overline{Q}_{1}^{*}\right)}}, k_{i,1}^{\iota} = \frac{\mu}{k_{v,1}^{\iota}}$$
(18)

# VI. SIMULATION RESULTS

Simulation results in MATLAB (with ideal voltage sources) are presented for a number of scenarios to validate the proposed averaged model. The line, load and filter parameters  $R_f = R_l = 0.15 \Omega$ ,  $L_f = L_l = 2.48$  mH,  $R_c = 3.3 \Omega$ ,  $C_f = 4.7 \,\mu\text{F}$ ,  $R_g = 0.13 \Omega$ ,  $L_g = 0.97$  mH,  $R_L = 22.1 \Omega$  and  $L_L = 14.4$  mH. The system parameters and ac-performance specifications are as in Table I and Table II.

## A. Embedded Droop Characteristics

In order to validate the embedded droop characteristics (4), (6) within the averaged VOC dynamics, a comparison is made with the actual VOC dynamics (1) with current feedback after the LCL filter. In Fig. 2, it can be seen that the embedded droop characteristics are close to the actual VOC dynamics. In order to obtain the  $\overline{V} - \overline{P}$  and  $\omega - \overline{Q}$  droop characteristics, the LCL filter is assumed to be ideal (i.e.  $R_f = R_c = R_g = 0$ ) resulting in  $S_{\alpha} = 0$  and the parameters in Table I are rederived according to the design procedure in Section IV.

# B. Rise Time and Harmonics Analysis

In Fig. 3, the rise time and harmonics analysis is presented for an unloaded inverter. It can be seen that the VO-controlled inverter closely follows the design inputs  $t_{rise}^{max}$  and  $\delta_{3:1}^{max}$ , thus validating the parameter design procedure in Section IV.

# C. Models Comparison

In Fig. 4, a comparison is made between the previously reported averaged VOC model [5], the proposed averaged

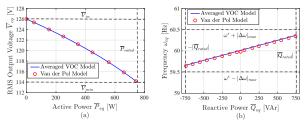


Figure 2. A comparison between the embedded droop characteristics within the averaged VOC dynamics (2)-(3) and actual VOC dynamics (1).

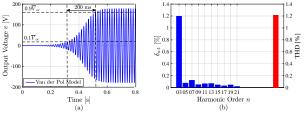


Figure 3. The rise time and harmonics analysis shows that the virtual oscillator controlled inverter closely follows the desired design inputs  $t_{rise}^{max}$  and  $\delta_{3:1}^{max}$ .

VOC model (2)-(3) and actual VOC dynamics (1). Note that the proposed averaged VOC model follows the actual VOC dynamics more closely as compared to the previously reported averaged VOC model. For comparison, the LCL filter is chosen with higher values of filter parameters ( $L_f^{\iota} = 10L_f, C_f^{\iota} =$ 19.75 $C_f$ ) and the epsilon is chosen to be  $\epsilon^{\iota} = \frac{\epsilon}{8} = \frac{\sqrt{L/C}}{8}$ . The  $z_L = 6.9 + j16.6 \Omega$  with  $z_S = 44.24 + j10.85 \Omega$  inparallel to implement the step-up and step-down changes. The system parameters in Table I are re-derived according to the design procedure in Section IV. Note that the difference between the previously reported averaged VOC model and actual VOC dynamics starts to increase for a higher value of filter capacitance. The output power is computed using a moving average window where window's length is updated based on the frequency recovered from actual VOC dynamics.

# D. Power Dispatch

In Fig. 5, the power dispatch results are presented for a number of scenarios as listed in Table III. Inverter 1 simultaneously regulates both the active and reactive power using the two PI controllers [9] that continuously tune the voltage scaling factor  $k_{v,1}$  and current feedback gain  $k_{i,1}$ . The inverter 2 supplies the remaining power to the load acting like a slack bus. The PI controllers' proportional and integral gains are  $K_P^p = -0.001$ ,  $K_I^p = -0.15$ ,  $K_P^q = 0.0001$  and  $K_I^q = 0.01$ . In Fig. 5, inverter 1 tracks the desired power set-points effectively while satisfying the security constraint (16). The corresponding changes in the inverter 2 power, load power, control inputs and voltages are also presented.

#### VII. CONCLUSION

The LCL filter is considered an essential part of commercial inverters to filter out the harmonics and improve the output voltage. Keeping this in view, an averaged VOC model is derived for inverters with current feedback after the output LCL filter. The averaged model uncovers the embedded droopcharacteristics within the averaged VOC dynamics and simpli-

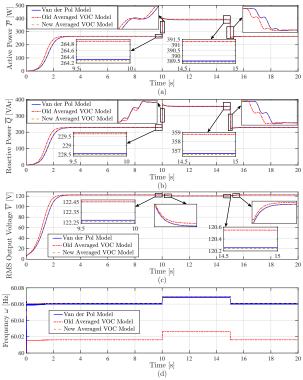


Figure 4. Comparison between previously reported averaged VOC model [5], the proposed averaged VOC model (2)-(3) and actual VOC dynamics (1): (a) active power, (b) reactive power, (c) voltages, (d) frequency.

Table III POWER DISPATCH SET-POINTS

Case No.	$P_1^*$ [W]	$Q_1^*$ [VAr]	Time [s]
1	_	_	0 - 5
2	500	83	5 - 15
3	500	120	15 - 25
4	500	50	25 - 35
5	100	50	35 - 55
6	100	120	55 - 65

fies the analysis. The voltage and frequency regulation characteristics are determined. Further, to enable the VO-controlled inverter to satisfy the desired ac-performance specifications, a parameter design procedure is presented. Moreover, a recently reported power dispatch technique is extended to the proposed averaged VOC model with current feedback after the output LCL filter. The updated control laws are derived to determine the control inputs corresponding to a particular power setpoint. An updated power security constraint is derived to determine the feasible operating region. The power security constraint is helpful in planning the optimal power flow in an electric grid. The simulation results validate the proposed averaged VOC model and power dispatch technique. Future work can include generalising the method to multiple inverters, stability analysis and experimental validation.

# APPENDIX

A. Derivation of the Averaged VOC Model with an LCL Filter Consider an inverter with current feedback after the output LCL filter as shown in Fig. 1. Let us denote a phasor associated

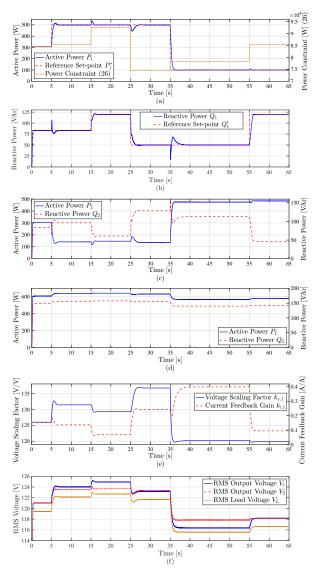


Figure 5. Power dispatch results for inverter 1 while inverter 2 supplies the remaining load power: (a) inverter 1 active power, power security constraint and reference set-point, (b) inverter 1 reactive power and reference set-point, (c) inverter 2 output power, (d) load power, (e) control inputs, (f) voltages.

with a time domain variable (.) by  $(\vec{.})$ . The  $\vec{V}$  denotes the voltage before the LCL filter, the  $\vec{V_o}$  denotes the filter capacitor voltage and the  $\vec{V_g}$  denotes the voltage after the LCL filter. Similarly, the  $\vec{I_f}$  denotes the current flowing through filter inductor  $L_f$ , the  $\vec{I_c}$  denotes the filter capacitor current and the  $\vec{I_g}$  is the current flowing through filter inductor  $L_g$ . Solving the network equations, we have:

$$\overrightarrow{I_g} = \begin{bmatrix} Z_{\alpha} \angle \theta_{\alpha} & Z_{\beta} \angle \theta_{\beta} \end{bmatrix} \begin{bmatrix} \overrightarrow{I_f} \\ \overrightarrow{V} \end{bmatrix},$$
(19)

where  $z_{\alpha} = Z_{\alpha} \angle \theta_{\alpha} = \frac{z_c + z_f}{z_c}$  and  $z_{\beta} = Z_{\beta} \angle \theta_{\beta} = \frac{-1}{z_c}$  are the impedance constants defined at  $\omega^*$ . Further, we have [5]:

$$\frac{d}{dt}\phi = \omega^* + \frac{d}{dt}\theta^* = \omega + \frac{d}{dt}\theta,$$
(20)

where the instantaneous phase angle is denoted by  $\phi$ , the nominal grid frequency is denoted by  $\omega^*$  and the load dependent steady-state frequency is denoted by  $\omega$ . The angles

 $\theta^*$  and  $\theta$  denote the phase offset with respect to  $\omega^* t$  and  $\omega t$ , respectively. The inverter output voltage is given by:

$$v(t) = \sqrt{2}V(t)\cos\left(\omega^* t + \theta^*(t)\right). \tag{21}$$

The instantaneous active and reactive output power of inverter in terms of v(t) and i(t) is given by:

$$P(t) = v(t)i(t), \qquad Q(t) = v\left(t - \frac{\pi}{2}\right)i(t).$$
 (22)

The averaged active and reactive power over an ac-cycle  $\frac{2\pi}{\omega^*}$  is defined as:

$$\overline{P}(t) = \frac{\omega^*}{2\pi} \int_{s=t}^{t+2\pi/\omega^*} P(s) ds, \quad \overline{Q}(t) = \frac{\omega^*}{2\pi} \int_{s=t}^{t+2\pi/\omega^*} Q(s) ds.$$
(23)

In order to derive the averaged dynamics (2)-(3), a change of variable is made from  $t \to \tau = \omega^* t$  and  $\theta^*(\tau) = \phi(\tau/\omega^*) - \tau$ . Expressing the actual VOC dynamics (1) as a function of  $\theta^*$ , we have [5]:

$$\frac{dV}{d\tau} = \frac{\epsilon}{\sqrt{2}} \left( \sigma g \left( \sqrt{2} V \cos \left( \tau + \theta^* \right) \right) - k_v k_i i \right) \cos \left( \tau + \theta^* \right), \\ \frac{d\theta^*}{d\tau} = -\frac{\epsilon}{\sqrt{2}V} \left( \sigma g \left( \sqrt{2} V \cos \left( \tau + \theta^* \right) \right) - k_v k_i i \right) \sin \left( \tau + \theta^* \right).$$
(24)

The dynamics in (24) are  $2\pi$  periodic functions in  $\tau$ . The averaged dynamics in the quasi-harmonic limit  $\epsilon \searrow 0$  (following [5, Eq. 12]) are given by:

$$\begin{bmatrix} \dot{\overline{V}} \\ \dot{\overline{\theta}}^* \end{bmatrix} = \frac{\epsilon\sigma}{2\pi\sqrt{2}} \int_0^{2\pi} g\left(\sqrt{2\overline{V}}\cos\left(\tau + \overline{\theta}^*\right)\right) \begin{bmatrix} \cos\left(\tau + \overline{\theta}^*\right) \\ \frac{-1}{\overline{V}}\sin\left(\tau + \overline{\theta}^*\right) \end{bmatrix} d\tau - \frac{\epsilon k_v k_i}{2\pi\sqrt{2}} \int_0^{2\pi} i \begin{bmatrix} \cos\left(\tau + \overline{\theta}^*\right) \\ \frac{-1}{\overline{V}}\sin\left(\tau + \overline{\theta}^*\right) \end{bmatrix} d\tau, = \frac{\epsilon\sigma}{2} \begin{bmatrix} \overline{V} - \frac{\beta}{2}\overline{V}^3 \\ 0 \end{bmatrix} - \frac{\epsilon k_v k_i}{2\pi\sqrt{2}} \int_0^{2\pi} i \begin{bmatrix} \cos\left(\tau + \overline{\theta}^*\right) \\ \frac{-1}{\overline{V}}\sin\left(\tau + \overline{\theta}^*\right) \end{bmatrix} d\tau.$$
(25)

Changing the coordinates from  $\tau$  to t in (25) and keeping the  $\mathcal{O}(\epsilon)$  terms only, we have:

$$\frac{d}{dt} \begin{bmatrix} \overline{V} \\ \overline{\theta}^* \end{bmatrix} = \frac{\sigma}{2C} \begin{bmatrix} \overline{V} - \frac{\beta}{2} \overline{V}^3 \\ 0 \end{bmatrix} - \frac{k_v k_i \omega^*}{2\pi \sqrt{2C}} \int_0^{\frac{2\pi}{\omega^*}} i(t) \begin{bmatrix} \cos\left(\omega^* t + \overline{\theta}^*\right) \\ \frac{-1}{\overline{V}} \sin\left(\omega^* t + \overline{\theta}^*\right) \end{bmatrix} dt. \quad (26)$$

Lets define the impedance constants  $C_{\alpha} = Z_{\alpha} \cos \theta_{\alpha}$ ,  $C_{\beta} = Z_{\beta} \cos \theta_{\beta}$ ,  $S_{\alpha} = Z_{\alpha} \sin \theta_{\alpha}$  and  $S_{\beta} = Z_{\beta} \sin \theta_{\beta}$ . The current  $i = \sqrt{2}I \cos (\omega^* t + \theta_i^*)$  where I is the RMS current magnitude and  $\theta_i^*$  is the phase-offset with respect to  $\omega^* t$ . Let  $\overrightarrow{I_g}^{\Re}$  denotes the real part of the current  $\overrightarrow{I_q}$  (19) and is given by:

$$\overrightarrow{I_g}^{\mathfrak{R}} = \sqrt{2} \left( Z_{\alpha} I \cos\left(\omega^* t + \theta_i^* + \theta_{\alpha}\right) + Z_{\beta} V \cos\left(\omega^* t + \theta^* + \theta_{\beta}\right) \right).$$
(27)

In order to derive the averaged VOC model, the current  $i = i_g$  is replaced by its real part  $\overrightarrow{I_q}^{\Re}$  in (26), we get:

$$\frac{d}{dt} \begin{bmatrix} \overline{V} \\ \overline{\theta}^* \end{bmatrix} = \frac{\sigma}{2C} \begin{bmatrix} \overline{V} - \frac{\beta}{2} \overline{V}^3 \\ 0 \end{bmatrix} - \frac{k_v k_i \omega^*}{4\pi C} \int_0^{\frac{2\omega^*}{\omega^*}} \begin{bmatrix} \frac{Z_\alpha}{V_i} \sqrt{2}V(t) \cos\left(\omega^* t + \theta^*\right) \sqrt{2}I(t) \cos\left(\omega^* t + \theta^*_i + \theta_\alpha\right) + \frac{-Z_\alpha}{\overline{V}^2} \sqrt{2}V(t) \sin\left(\omega^* t + \theta^*\right) \sqrt{2}I(t) \cos\left(\omega^* t + \theta^*_i + \theta_\alpha\right) - 2Z_\beta \overline{V} \cos\left(\omega^* t + \theta^* + \theta_\beta\right) \cos\left(\omega^* t + \theta^*\right) \end{bmatrix} dt.$$
(28)

Using the trigonometric identities and definition of instantaneous active and reactive power (22), we get:

$$\frac{d}{dt} \begin{bmatrix} \overline{V} \\ \overline{\theta}^* \end{bmatrix} = \frac{\sigma}{2C} \begin{bmatrix} \overline{V} - \frac{\beta}{2} \overline{V}^3 \\ 0 \end{bmatrix} - \frac{k_v k_i \omega^*}{4\pi C} \int_0^{\frac{2\pi}{\omega^*}} \begin{bmatrix} \frac{1}{\overline{V}} (C_\alpha P(t) + S_\alpha Q(t)) + Z_\beta \overline{V} (\cos \theta_\beta + \cos (2\omega^* t + 2\theta^* + \theta_\beta)) \\ \frac{1}{\overline{V}^2} (C_\alpha Q(t) - S_\alpha P(t)) - Z_\beta (\sin (2\omega^* t + 2\theta^* + \theta_\beta) - \sin \theta_\beta) \end{bmatrix} dt.$$
(29)

The averaged VOC dynamics (2)-(3) are recovered from (29).

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### REFERENCES

- K. De Brabandere, B. Bolsens, J. Van den Keybus, A. Woyte, J. Driesen, and R. Belmans, "A voltage and frequency droop control method for parallel inverters," *IEEE Transactions on power electronics*, vol. 22, no. 4, pp. 1107–1115, 2007.
- [2] Y. A.-R. I. Mohamed and E. F. El-Saadany, "Adaptive decentralized droop controller to preserve power sharing stability of paralleled inverters in distributed generation microgrids," *IEEE Transactions on Power Electronics*, vol. 23, no. 6, pp. 2806–2816, 2008.
- [3] R. Teodorescu, F. Blaabjerg, and M. Liserre, "Proportional-resonant controllers. a new breed of controllers suitable for grid-connected voltage-source converters," *Proc. Optim*, vol. 3, pp. 9–14, 2004.
- [4] B. Shoeiby, R. Davoodnezhad, D. Holmes, and B. McGrath, "Voltagefrequency control of an islanded microgrid using the intrinsic droop characteristics of resonant current regulators," in *Energy Conversion Congress and Exposition (ECCE)*, 2014 IEEE, pp. 68–75, IEEE, 2014.
- [5] B. B. Johnson, M. Sinha, N. G. Ainsworth, F. Dörfler, and S. V. Dhople, "Synthesizing virtual oscillators to control islanded inverters," *IEEE Transactions on Power Electronics*, vol. 31, no. 8, pp. 6002–6015, 2016.
- [6] B. B. Johnson, S. V. Dhople, A. O. Hamadeh, and P. T. Krein, "Synchronization of parallel single-phase inverters with virtual oscillator control," *IEEE Transactions on Power Electronics*, vol. 29, no. 11, pp. 6124–6138, 2014.
- [7] M. Ali, A. Sahoo, H. Nurdin, J. Ravishankar, and J. Fletcher, "On the power sharing dynamics of parallel-connected virtual oscillatorcontrolled and droop-controlled inverters in an ac microgrid," in *IECON* 2019-45th Annual Conference of the IEEE Industrial Electronics Society, vol. 1, pp. 3931–3936, IEEE, 2019.
- [8] M. Ali, H. I. Nurdin, and J. E. Fletcher, "Output power regulation of a virtual oscillator controlled inverter," in 2018 IEEE 18th International Power Electronics and Motion Control Conference (PEMC), pp. 1085– 1090, IEEE, 2018.
- [9] M. Ali, H. I. Nurdin, and J. E. Fletcher, "Simultaneous regulation of active and reactive output power of parallel-connected virtual oscillator controlled inverters," in *IECON 2018-44th Annual Conference of the IEEE Industrial Electronics Society*, pp. 4051–4056, IEEE, 2018.
- [10] M. Ali, J. Li, L. Callegaro, H. I. Nurdin, and J. E. Fletcher, "Regulation of active and reactive power of a virtual oscillator controlled inverter," *IET Generation, Transmission & Distribution*, vol. 14, no. 1, pp. 62–69, 2019.
- [11] D. Groß, M. Colombino, J.-S. Brouillon, and F. Dörfler, "The effect of transmission-line dynamics on grid-forming dispatchable virtual oscillator control," *IEEE Transactions on Control of Network Systems*, 2019.
- [12] D. Raisz, T. T. Thai, and A. Monti, "Power control of virtual oscillator controlled inverters in grid-connected mode," *IEEE Transactions on Power Electronics*, 2018.
- [13] G.-S. Seo, M. Colombino, I. Subotic, B. Johnson, D. Groß, and F. Dörfler, "Dispatchable virtual oscillator control for decentralized inverter-dominated power systems: Analysis and experiments," in 2019 IEEE Applied Power Electronics Conference and Exposition (APEC), pp. 561–566, IEEE, 2019.
- [14] M. Ali, H. I. Nurdin, and J. E. Fletcher, "Dispatchable virtual oscillator control for single-phase islanded inverters: Analysis and experiments," *IEEE Transactions on Industrial Electronics*, 2020. [Early Access] Online Available: doi: 10.1109/TIE.2020.2991996.