

## Workshop 14: iWAPT

### Automatic Performance Tuning

#### Description

iWAPT (International Workshop on Automatic Performance Tuning) is a series of workshops that focus on research and techniques related to performance sustainability issues. The series provides an opportunity for researchers and users of automatic performance tuning (AT) technologies to exchange ideas and experiences acquired when applying such technologies to improve the performance of algorithms, libraries, and applications; in particular, on cutting edge computing platforms. Topics of interest include performance modeling; adaptive algorithms; autotuned numerical algorithms; libraries and scientific applications; empirical compilation; automated code generation; frameworks and theories of AT and software optimization; autonomic computing; and context-aware computing.

iWAPT 2020 is the fifteenth in a series of successful workshops devoted to AT. The series started in Tokyo in 2006. Since then, it has been held every year: six times in Japan, twice in the USA, and once in Singapore, India, Spain, Canada and Brazil. In particular, iWAPT has been hosted by IPDPS since 2015. Papers presented at previous workshops have been published by Springer in Lecture Notes in Computer Science, by Elsevier in Procedia Computer Science, and also in the book *Software Automatic Tuning: From Concepts to State-of-the-Art Results* (Naono, K., Teranishi, K., Cavazos, J., Suda, R., Eds., 2010). Submitted papers are selected for presentation based on a peer-review process by leading researchers in the AT community. This year the virtual workshop will be held online with 8 selected paper presentations.

#### Program Chair

I-Hsin Chung, IBM T. J. Watson Research Center, USA

#### Program Vice-chair

Kazuhiko Komatsu, Tohoku University, Japan

#### Program Committee

Ray-Bing Chen, National Cheng Kung University, Taiwan  
 Akihiro Fujii, Kogakuin University, Japan  
 Takeshi Fukaya, Hokkaido University, Japan  
 Michael Gerndt, Technical University of Munich, Germany  
 Jose Gracia, High-Performance Computing Center Stuttgart, Germany  
 Takahiro Katagiri, Nagoya University, Japan  
 Jakub Kurzak, AMD Research, USA  
 Che-Rung Lee, National Tsing Hua University, Taiwan  
 Osni A. Marques, Lawrence Berkeley National Laboratory, USA  
 Boyana Norris, University of Oregon, USA  
 Satoshi Ohshima, Nagoya University, Japan  
 Louis-Noel Pouchet, The Ohio State University, USA  
 Daisuke Takahashi, University of Tsukuba, Japan  
 Hiroyuki Takizawa, Tohoku University, Japan  
 Teruo Tanaka, Kogakuin University, Japan  
 Richard Vuduc, Georgia Institute of Technology, USA  
 Weichung Wang, National Taiwan University, Taiwan

#### Steering Committee Chair

Hiroyuki Takizawa, Tohoku University, Japan

**Steering Committee**

Jonathan T. Carter, NERSC/Lawrence Berkley National Laboratory, USA

Victor Eijkhout, Texas Advanced Computing Center, University of Texas, USA

Akihiro Fujii, Kogakuin University, Japan

Toshiyuki Imamura, RIKEN R-CCS, Japan

Takeshi Iwashita, Hokkaido University, Japan

Takahiro Katagiri, Nagoya University, Japan

Jakub Kurzak, AMD Research, USA

Osni A. Marques, Lawrence Berkley National Laboratory, USA

Reiji Suda, The University of Tokyo, Japan

Richard Vuduc, Georgia Institute of Technology, USA

Weichung Wang, National Taiwan University, Taiwan

R. Clint Whaley, Louisiana State University, USA

Yusaku Yamamoto, The University of Electro-Communications, Japan