A Novel Approach to Fast Discrete Hartley Transform

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ABSTRACT

Discrete Hartley transform (DHT) is an important tool in digital signal processing. In this present paper, we propose a novel approach to perform DHT. We transform DHT into a form expressed in discrete moments via a modular mapping and truncating Taylor series expansion and present a completely new formula for computing DHT. We extend the use of our systolic array for fast computation of moments without any multiplications to one that computes DHT with only a few multiplications and without any evaluations of triangular functions. The multiplication number used in our method is $O(Nlog_2N/log_2log_2N)$ superior to $O(Nlog_2N)$ in the conventional FDT. The execution time of the systolic array is only $O(N\log_2 N/\log_2 \log_2 N)$ for 1-D DHT and $O(N^k)$ for k-**D** $DHT(k \ge 2)$. The systolic array consists of very simple processing elements and hence it implies an easy and potential hardware/VLSI implementation. The approach is also applicable to DHT inverses.

1: Introduction

Discrete Hartley transform (DHT) is widely used in signal processing [1-3]. DHT maps a real-valued sequence to a real-valued spectrum while preserving some of the useful properties of the DFT and is a powerful tool as a substitute to the FFT for computing cyclic convolutions of real data [4-6]. There have been many papers reporting the development with the fast DHT [7-9].

On the other hand, computer vision and image analysis have propelled the advancement of fast computation of discrete moments (DM) [10-13]. These two research threads have been developed independently. Up to now, no fast algorithms proposed for DHT have some connections with moments.

In this paper, we first construct the bridge between DHT and discrete moments (DM) by a modular mapping and making use of Taylor expansions and hence we can transform DHT into computation involving moments. We also discuss the problems of convergence and errors. Based on our approach to the fast calculation of moments [10], a new systolic array to perform 1-D DHT is presented, followed by a complexity analysis. Finally, we give our conclusions.

2: Using moments to computing

The discrete Hartley transform (DHT) is defined for a real valued length-N sequence x(n) (0 $\le n \le N-1$), by the following equations:

$$X(k) = \sum_{n=0}^{N-1} x(n) cas(2\pi nk/N)$$

$$= \sum_{n=0}^{N-1} x(n) (\cos(2\pi nk/N) + \sin(2\pi nk/N))$$

$$0 \le k \le N-1$$
(2.1)

Let us transform Equation (2.1) to look for a new approach to computing it. For every pair of k and i (i, k=0, 1, 2, N-l), and defining S(k,i) by

 $S(k, i)=(n \text{ I kn} \equiv i \pmod{N} \text{ } 0 \leq n \leq N-1)$ then defining $x_{k,i}(i, k=0,1,2,\ldots,N-1)$ by

$$x_{k,i} = \begin{cases} \sum_{j \in S(k,i)} S(k,i) \neq \emptyset \\ \mathbf{0} & \text{otherwise.} \end{cases}$$
i, k=0, 1, 2,, N-1 (2.2)

For example, for N=4, For example,
$$S_{0,0} = \{0, 1, 2, 3\}, S_{0,1} = S_{0,2} = S_{0,3} = \Phi;$$

$$S_{1,0} = \{0\}, S_{1,1} = \{1\}, S_{1,2} = \{2\}, S_{1,3} = \{3\};$$

$$S_{2,0} = \{0, 2\}, S_{2,1} = \Phi, S_{2,2} = \{1, 3\}, S_{2,3} = \Phi;$$

$$S_{3,0} = \{0\}, S_{3,1} = \{3\}, S_{3,2} = \{2\}, S_{3,3} = \{1\}.$$

$$x_{0,0} = x(0) + x(1) + x(2) + x(3), x_{0,1} = 0, x_{0,2} = 0, x_{0,3} = 0;$$

$$x_{1,0} = x(0), x_{1,1} = x(1), x_{1,2} = x(2), x_{1,3} = x(3);$$

$$x_{2,0} = x(0) + x(2), x_{2,1} = 0, x_{2,2} = x(1) + x(3), x_{2,3} = 0;$$

$$x_{3,0} = x(0), x_{3,1} = x(3), x_{3,2} = x(2), x_{3,3} = x(1).$$

Thus, by using the periodic properties of sine functions and cosine functions, Equation (2.1) can be rewritten as follows:

$$X(k) = \sum_{i=0}^{N-1} X_{k,i} (\cos(2\pi i/N) + \sin(2\pi i/N)) 0 \le k \le N-1 \quad (2.3)$$
For $0 \le i \le N-1$

$$\cos(2\pi i/N) = 1 - (2\pi i/N)^2 / 2! + \dots + (-1)^p (2\pi i/N)^{2p} / (2p)! + R_{cool}$$

$$R_{cool} = \cos(\xi_i) + (2p+1)\pi / 2(2\pi i/N)^{2p+1} / (2p+1)! \quad 0 < \xi_i < 2\pi i/N$$

$$\sin(2\pi i/N) = (2\pi i/N) / 1! - (2\pi i/N)^3 / 3! + \dots + (-1)^p (2\pi i/N)^{2p+1} / (2p+1)!$$

$$+ R_{cool}$$

$$R_{cool} = \sin(\xi_i) + (p+1)\pi / (2\pi i/N)^{2p+2} / (2p+2)! \quad 0 < \xi_i < 2\pi i/N$$

$$R_{cool} = \sin(\xi_i) + (p+1)\pi / (2\pi i/N)^{2p+2} / (2p+2)! \quad 0 < \xi_i < 2\pi i/N$$

This follows immediately by applying the theorem of extended law of the mean [14] to $\cos(2\pi i/N)$ and $\sin(2\pi i/N)$, where R_i is Taylor remainder term.

Substituting them into Equation (2.3), yields
$$X(k) = x_{k,0} + \sum_{i=1}^{N-1} x_{k,i} \sum_{r=0}^{p} [(-1)^r (2\pi i/N)^{2r} / (2r)! + (-1)^r (2\pi i/N)^{2r+1} / (2r+1)!] + R_p$$

$$= x_{k,0} + \sum_{r=0}^{p} (-1)^r (2\pi)^{2r} / N^{2r} (2r)! \sum_{i=1}^{N-1} x_{k,i} i^{2r} + \sum_{r=0}^{p} (-1)^r (2\pi)^{2r+1} / N^{2r+1} (2r+1)! \sum_{i=1}^{N-1} x_{k,i} i^{2r+1} + R_p$$

$$= x_{k,0} + \sum_{r=0}^{2p+1} a_r m_{k,r} + R_p$$

$$0 \le k \le N-1$$
(2.5)

$$a_{r} = \begin{cases} (-1)^{q} (2\pi)^{2q} / N^{2q} (2q)! & r = 2q \\ (-1)^{q} (2\pi)^{2q+1} / N^{2q+1} (2q+t)! & r = 2q+1 \end{cases} \quad o \leq q \leq p \quad (2.6)$$

$$m_{k,r} = \sum_{i=1}^{N-1} x_{k,i} i' \quad (2.7)$$

$$R_{p} = \sum_{i=1}^{N-1} x_{k,i} [\cos \xi_{\cos i} + (2p+1)\pi/2) (2\pi i/N)^{2p+1} / (2p+1)! + \sin(\xi_{\sin i} + (p+1)\pi) (2\pi i/N)^{2p+2} / (2p+2)!]$$

$$0 < \xi_{\cos i}, \xi_{\sin i} < 2\pi i/N$$

$$(2.8)$$

If R_p is ignored, we have

$$X(k) = x_{k,0} + \sum_{n=0}^{2p+1} a_n m_{k,n} \qquad 0 \le k \le N-1$$
 (2.9)

The absolute value of the error introduced by overlooking R_p is bounded by

$$\left|R_{p}\right| \leq \left|\sum_{i=1}^{N-1} \chi_{k,i} \cos(\xi_{i} + (2p+1)\pi/2)(\pi i/2N)^{2p+1}/(2p+1)!\right| + \sum_{i=1}^{N-1} \chi_{k,i} \sin(\xi_{\min} + (p+1)\pi)(2\pi i/N)^{2p+2}/(2p+2)!\right| \leq \max_{n} \left|\chi(n)\right| (N-1) \left[(2\pi)^{2p+1}/(2p+1)! + (2\pi)^{2p+2}/(2p+2)!\right]$$
(2.10)

From the inequality above, it is clearly shown that R_p converges to zero very rapidly and uniformly. For example, for max $x(n) \le 256, (0 \le n \le N-1), N \le 10$ 2048, p=17, $|R_p| \le 5.13 \times 10^{-7}$

This error can satisfy accuracy requirements of most applications by computing only thirty-six terms.

Furthermore, we can prove that the least upper bound of p is not more than $O(log_2N/log_2log_2N)$ as N tends to infinite [15, 16].

Let
$$f(N, p) = \max_{n} |x(r)| \sqrt{2(N-1)(2\pi)^p} / p! = A(N-1)(2\pi)^p / p!$$
(2.11)

Substituting [2log_N/log_log_N] for p in Equation (2.11)([x] denotes an integer closest to x), we get $f(N, p) = F(N) = A(N-1)(2\pi)^{\lfloor 2\log_2 N/\log_2\log_2 N \rfloor}$ [2log, N/log, log, N]!

then
$$log_2N/log_2log_2N = t,$$
then
$$log_2N = t(log_2log_2N)/2,$$
and
$$F(N) \le AN(2\pi)^{t+1}/[t]!.$$
so
$$N = 2^{t(log_2log_2N)/2} = (log_2N)^{t/2}$$
and
$$\lim_{N \to \infty} t = \infty$$

$$\lim_{N \to \infty} (log_2N)^{1/2}/t = \lim_{N \to \infty} (log_2N)^{1/2} log_2 log_2 N/2 log_2 N$$

$$= \lim_{N \to \infty} (log_2log_2N)^{1/2}/t = \lim_{N \to \infty} (log_2N)^{1/2} log_2 log_2N)^{1/2}/t = \lim_{N \to \infty} (log_2N)^{1/2}/t = 0$$

then

Let

(2.4)

$$\lim_{N \to \infty} F(N) \le \lim_{t \to \infty} A(2\pi)^{t+1} (\log_2 N)^{t/2} / [t]!$$

$$= \lim_{t \to \infty} A(2\pi)^{t+1} (\log_2 N)^{t/2} / t^t e^{-t} \sqrt{2\pi t}$$

$$= \lim_{t \to \infty} A(2\pi) (2\pi e(\log_2 N)^{1/2} / t)^t / \sqrt{2\pi t} = 0$$

The final result is obtained by using the well known Stirling formula [17]. The computation of X(k) using the approximation of Equation (2. 9) involves the generation of the r-th order moments of the transformed data sequence $x_{k,i}$ and then performing a dot product of these moments with a constant vector (a,) and an addition with $x_{k,0}$. Thus if the moments can be computed very quickly and efficiently, so can be the above procedure.

3: An algorithm for computing 1-D DHT

According to Section 2, fast DHT includes the following three steps:

- 1. Computing $x_{k,i}$ (i=0, 1, 2, N-1; k=0, 1, 2,, N-1) and $a_r(r=0, 1, 2, \ldots, 2p+1)$;
- 2. Computing $m_{k,r}(r=0, 1, 2, \dots, 2p+1; k=0,1,2, \dots, N-1)$;
- 3. Computing X(k) (k=0,1,2,....N-1).

The procedure of computing $x_{k,i}$ and a, is also called the Preprocessing. The algorithm of computing X(k) can be described below.

Input initial p, N, x(O), x(1), x(N-1) perform Preprocessing for k=O to N- 1 compute $Moment_{2p+1}(x_{k,N-1},x_{k,N-2},\ldots,x_{k,1})$ X(k)=0 for r=0 to 2p+1 $X(k)=X(k)+a_rm_{k,r}$ end for $X(k)=X(k)+x_{k,0}$ end for

Moment_{2p+1}($x_{k,N-1}$, x_k , N-2,, $x_{k,1}$) is a subroutine that produces the moments up to 2p+1 order [10]. Now the complexity of the algorithm can be given. In the Preprocessing, computing a, and index set S(k, i) can be completed upon condition that N and p are given. In real-time systems, these computations can be preprocessed and are not included in execution procedure. The computation of $x_{k,i}$ is $N^2 - \sum_{k=0}^{N-1} N / gcd(k,N)$ additions (greatest common divisor). Computing Moment_{2p+1} N times needs (2p+2)(2p+3)(N-2)N/2 additions. The computation of X(k) ($k=0,1,2,\ldots N-1$) in volves (2p+2)N additions and (2p+1)N

involves (2p+2)N additions and (2p+1)N multiplications. So there are altogether $N^2 - \sum_{k=0}^{N-1} N/gcd(k,N) + (2p+2)(2p+3)(N-2)N/2 +$

(2p+2)N additions and (2p+1)N multiplications in the algorithms. If N is a prime number, $(N^2 - \sum_{k=0}^{N-1} N / gcd(k, N))$ disappears from the above formula.

4: Pre-processing array for computing X_{k,i}

In the general case, we propose to use a special linear array to implement $x_{k,i}$, (i, k=0, 1, ..., N-1), Each element x(r) is tagged with a rank=kr(mod N) before it is sent into the array. For example, for k=2 and N=4, the samples become (x(O), 0), (x(I), 2), (x(2), 0), (x(3), 2). These are sent into the linear array one element at a time. An

element is percolated from left to right until it reaches a cell in the array whose position is identical to its rank. When this happens, the value is accumulated in that cell in case it receives multiple values (i.e. $S_{ki} > 1$). Figure 1 illustrates an example. A cell in a linear array contains an adder only if the corresponding partition S_{ki} has a size greater than 1. In 8 clocks, $x_{2,0}$ will emerge at the right hand side, as shown in Figure 1. In general, it takes 2N clocks for the vector to appear on the right. It is obvious that number of additions required in the preprocessing array for each k is equal to $N-N/\gcd(k,N)$. So the total number of addition required in the preprocessing array is equal to $\sum_{k=0}^{N-1} (N-N/\gcd(k,N) = N^2 - \sum_{k=0}^{N-1} N/\gcd(k,N)$.

x(r) to $x_{k,i}$ is noteworthy: If (k, N)=1, (i.e., k and N are coprime) then $(ki \mid i=0, 1, 2, \ldots, N-1)$ is a complete system of residues modulo N [18] and S_{ki} I=1. It means $x_{k,i}$ is a permutation o It remains to consider the generation of $x_{k,i}$. One interesting property of the mapping off x(r). Particularly, if N is prime, then $S_{00}=\{0,1,\ldots,N-1\}$ and $S_{0j}=\emptyset$ for j#0, but $|S_{ki}|=1$ for all $k\neq 0$. In other words, $x_{k,i}$ is a permutation of x(r) whenever $k\neq 0$. In the remaining case, $y_0(0)=\sum_{r=0}^{N-1}x(r)$ and $x_{0,j}=0$ for j#0. So in the case where the sample size is a prime

in the case where the sample size is a prime number, a preprocessor can be used to produce the $\mathbf{x}_{k,i}$ via permutations of the sampled sequence $\mathbf{x}(\mathbf{r})$, after producing $\mathbf{y}_{0}(0) = \sum_{i=1}^{N-1} \mathbf{x}(\mathbf{r})$ in the first pass.

5: Systolic array for computing DHT

The general network for implementing DHT is drawn in Figure 2. It consists of the moment generator [10], the processing arrays and a dot product array. The moment generator formed of N-2 (2p+1)-networks with a row of adder-latch could be used to generate the 1-D moments. It receives $x_{k,i}$, (i, k=0, 1, ... N-1) that preprocessing arrays produce. The dot product array performs the dot product and produces X(k) at the only output. The numbers in square brackets denote the amount of time delay to keep synchronous pace in Figure 2. The scheduling of the dataflow is such that m_k (0)* a_0 moves from left to right, accumulates with $m_k(1)$ * a₁ to produce the partial sum and this repeats until X(k) emerges at the far right of the output. As $m_k(i)$ is produced earlier one cycle than $m_k(i+1)(i=0,$ $1, \dots, 2p+1$), as shown in Figure 2, so the most right term $m_k(p)$ takes 2 cycles to merge into X(k). It is just time cost for the dot product.

The total execution time of the computing procedure is:

N+(2p+2)(N-2)+2+N-1=2(p+2)N-(4p+3) clock cycles. Where N is for preprocessing, and (2p+2)(N-2) for computing moments, and 2 for the dot product, and N-1 for collecting all X(k), $(k=1, 2, \ldots, N-1)$.

Since the DHT is self-inverse, the approach is also applicable to DHT inverse. The method can also be extended to to multi-dimensional DHT due to the separability of the DHT kernel. It can also be proved by mathematical induction that the execution time of the systolic array is $O(N^k)$ for k-D DHT.

6: Conclusions

New fast algorithms and systolic arrays for one-dimensional DHT and their inverse are proposed and the results are extended to k-dimensional DHT (k≥2), and their computation complexities are analyzed. The relationships between these discrete transforms and discrete moments are presented and the fast discrete transforms are converted into fast discrete moment transforms. It is a completely new approach to DHT.

The methods proposed have the advantages below:

- 1. Many multiplications have been converted into additions, and triangular functions have been replaced by simple polynomial functions. It decreases the computational cost and memory requirement. Comparing with the direct method with computation complexity $O(N^2)$ and the conventional FHT which originates from FFT with O(Nlog₂N), the methods proposed are superior to the direct method and but inferior to FHT) if the calculations are executed on a sequential machine (for the example given in Section 2: $N \le 2k$, $log_2N \le$ 11, p=17). When N is large enough, p can take a value much less than log₂N and can still satisfy the accuracy requirement. In other words, in the case of N large enough, the number of multiplication in our method is less than that in the conventional FHT though the number of addition in our method is still more than that in the conventional FHT;
- 2. The systolic arrays consist of only latches, adderlatches and a few multipliers. It results in easy hardware implementation and is very suitable for a real-time system;

- 3. It can cope with arbitrary data length and not limited to data length in the form of 2^k or any other forms. It has a very good accuracy and convergence feature;
- 4. The multiplication number for 1-D DHT in our method is $O(Nlog_2N/log_2log_2N)$ superior to $O(Nlog_2N)$ in the conventional FHT;
- 5. The methods are very easily extended to multidimensional DHT and their inverses. The computational complexity is $O(n^k)$ for kdimensional DHT in the systolic arrays.

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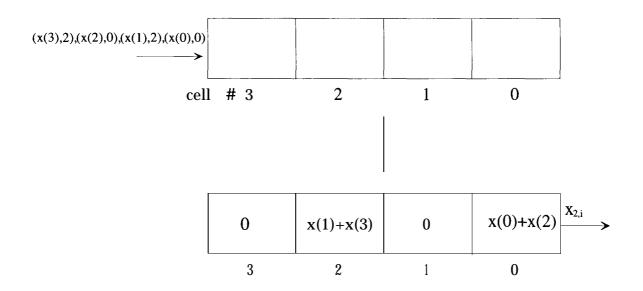


Figure 1. The linear array to implement $x_{2,i}$.

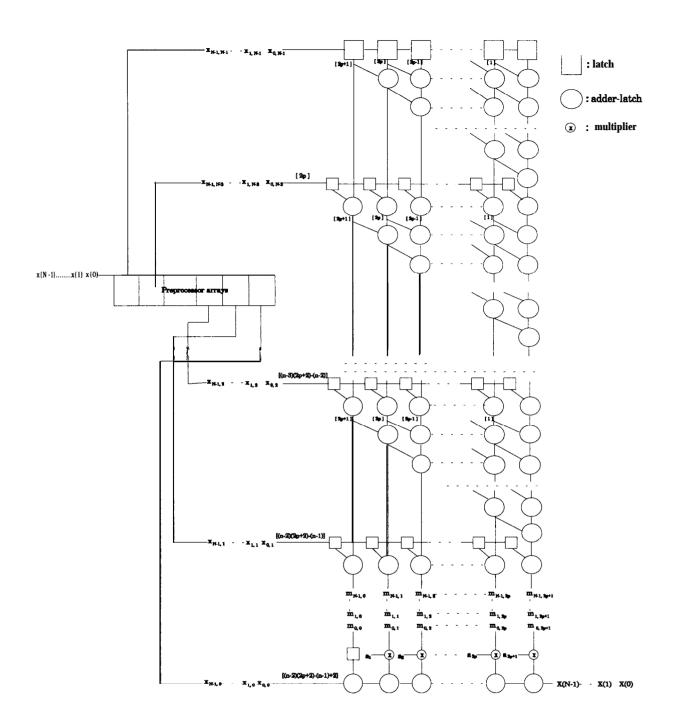


Figure 2. The systolic array for 1-D DHT.