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A Fully Integrated CMOS Fluorescence Biochip for DNA and RNA Testing

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Abstract

Design and successful implementation of a fully-integrated CMOS fluorescence biochip for DNA/RNA testing in molecular diagnostics (MDx) is presented. The biochip includes a 32×32 array of continuous wave fluorescence detection biosensing elements. Each biosensing element is capable of having unique DNA probe sequences, wavelength-selective multi-dielectric emission filter (OD of 3.6), resistive heater for thermal cycling, and a high performance and programmable photodetector. The dimension of each biosensor is 100μ m×100 μ m with a 50μ m×50 μ m N_{well}-P_{sub} photodiode acting as the optical transducer, and a Σ modulator based photocurrent sensor. The measured photodetector performance shows ~116 dB detection dynamic range (10fA – 10nA) over the 25°C – 100°C temperature range, while being ~1 dB away from the fundamental shot-noise limit. To empirically demonstrate the compatibility of this biochip with MDx applications, we have successfully utilized the array and its thermal cycling capability to adopt a 7-plex panel for detection of 6 human upper respiratory viruses.

Index Terms

Biosensor; Fluorescence Spectroscopy; Microarray; Molecular Diagnostics; Optical Filter; Biochip; Polymerase Chain Reaction; Nucleic Acid; Infectious Disease; Image Sensors

I. Introduction

Leveraging the specificity of bio-molecular interactions to detect the presence and abundance of biological analytes such as DNA, proteins, toxins, hormones and other biomolecules is the underlying principle of biosensing [1]. Today, there are a multitude of biosensors categorized by their probe structure and transduction mechanism (e.g., optical [2–6], electronic [7–8], or mechanical [9–10]). Yet, the most widely used biosensors include DNA or antibody probes and adopt optical readouts, specifically continuous wave (CW) fluorescence transduction methods [4–6, 11–12]. Such broad choice of probes is mainly due to the versatility of DNA and antibody probes and the available methods to biochemically synthesize or engineer them to recognize "almost" any target analyte biomolecule [13–14]. The rationale behind using the CW fluorescence method is the availability of versatile fluorophore molecules as molecular tags (labels) [15] to identify and quantify bio-

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molecules. Fluorescent labels, unlike their electrochemical, mechanical, or magnetic counterparts, are bio-compatible, are easy to adopt in molecular biology, are easy to conjugate with probes and target analytes, have a high detection contrast (*i.e.*, high signal-to-background in aqueous medium), have multiple colors, and are measurable using conventional microscopy instrumentation and scanners.

Here, we present an integrated CMOS biosensor array (biochip), designed and implemented specifically for CW fluorescence detection and nucleic acid (DNA or RNA) testing. In the past decade, there has been numerous reported CMOS biochips utilizing non-optical techniques such as electro-analytical [16–18] and magnetic [19–20]. One can correctly claim that such non-optical biochips offer superior transducer compatibility for integration in standard CMOS. Nevertheless, such biosensors by and large require unconventional labels that offer no "color" and cannot be used in real-time detection assays that are common in molecular biology. Moreover, bio-compatible surfaces (*e.g.*, SiO₂, or Si₃N₄) are not always compatible with non-optical transducers. Examples are CMOS electro-analytical biosensors that require Faradaic electrodes built using Au or Pt [21, 22]. Hence, their adoption in applied fields such as molecular diagnostics (MDx), genomics and proteomics research will be inferior to biochip platforms utilizing "gold standard" fluorescence techniques.

The focus of this paper is the system level design of the CMOS biochip and the implementation techniques to satisfy the stringent requirements of state-of-art DNA/RNA detection assays in MDx. In Section 2, we first present the formulations and theoretical models of on-chip CW fluorescence-based detection. Next, we discuss the proposed system architecture which includes the integrated circuit components, the reaction chamber, surface chemistry and the assay. In Section 3, we discuss the circuit design implementation of the system in detail and report its measured performance, followed by Section 4, in which we explain the post-CMOS fabrication steps. In Section 5, we report a successful implementation of an MDx panel for upper respiratory viruses using a polymerase chain reaction (PCR [23]) based assay.

II. Specifications

A. CW fluorescence detectors

Fluorescence-based detection relies on excitation of electrons in the valence band of unique molecules (fluorophores [24]) by applying photon flux F_x at wavelength λ_x and detecting the return of the electrons to the ground state by monitoring the emitted photon flux F_e at wavelength λ_e ($\lambda_x < \lambda_e$). In fluorescence biosensors, depicted in Fig. 1, fluorophores are used as reporter molecules (labels) for the targets, while capturing probes are immobilized on a surface. The capturing of the targets by the probes results in aggregation of fluorophores on the surface which increases F_e at the probe coordinates.

To formulate this process, we use Beer-Lambert Law [25] which describes the transmitted photon flux, F_x^T (photons cm⁻² sec⁻¹), through a medium with path length *I*(cm) that includes *n* absorbing fluorophore species in the sample with concentration C_n (M) as

where e (cm⁻¹M⁻¹) is the fluorophore absorption (extinction) coefficient at λ_x . Now in biosensors (Fig. 2), all the fluorophores are immobilized at the surface (i.e., $I \rightarrow 0$). Therefore, (1) becomes

$$F_x^T = F_x \cdot 10^{-\varepsilon \left(\frac{n \times 10^3}{N_A \cdot S \cdot l}\right)l} = F_x \cdot 10^{-\varepsilon \frac{n_s}{N_A} 10^3}, \quad (2)$$

where N_A , n_s , S are the Avogadro's number, fluorophore surface concentration and surface area, respectively. The photon absorption rate in this situation, denoted by A_x , is formulated by

$$A_{x} = F_{x} - F_{x}^{T} = F_{x} \left(1 - 10^{-\varepsilon \frac{n_{s}}{N_{A}} 10^{3}} \right).$$
(3)

Now, assuming that $Q_Y(\lambda_e)$ is the quantum yield function of the fluorophore, described as the photon emission probability distribution function at λ_e , then the isotropic (in all directions) photon emission flux, $F_e(\lambda_e)$, becomes

$$Fe(\lambda_e) = Q_Y(\lambda_e)A_x.$$
 (4)

As depicted in Fig. 2, we can introduce parameter $K_{\theta,\phi}$ as the transmittance efficiency of $F_e(\lambda_e)$ originated from probe area *S*, to the photodetector. If the quantum efficiency of the photodetector is $Q_E(\lambda)$, then the emission-induced photocurrent, I_{em} , can be formulated by

$$I_{em} = q K_{\theta,\phi} S A_x \int_{\lambda=0}^{\infty} Q_Y(\lambda) Q_E(\lambda) d\lambda, \quad (5)$$

where q is the charge of an electron. In practical systems, a fraction of F_x will always "leak" to the photodetector and create the background photocurrent, I_{bk} , such that

$$I_{bk} = qK'_{\theta,\phi}SF_x Q_E(\lambda_x), \quad (6)$$

where $K'_{\theta,\phi}$ is the transmittance efficiency of F_x^T . By combining (5) and (6), we can thus derive the signal-to-background ratio of the system as

$$\frac{I_{em}}{I_{bk}} = \left(\frac{K_{\theta,\phi}}{K'_{\theta,\phi}}\right) \left(1 - 10^{-\varepsilon \frac{n_s}{N_A} 10^3}\right) \left(\frac{\int_{\nu=0}^{\infty} Q_Y(\nu) Q_E(\nu) d\nu}{Q_E(\lambda_x)}\right).$$
(7)

The significance of (7) is that it demonstrates the independence of I_{em}/I_{bk} to F_x . Furthermore, it demonstrates that the optimal approach to achieve high I_{em}/I_{bk} , is to increase $K_{\theta,\phi}/K'_{\theta,\phi}$, the wavelength selectivity of the detector as the other parameters in (7) (e.g., ε or n_s) are constrained by the assay and the involved chemistry and not the detector.

Another important parameter to consider in our models is the signal-to-noise ratio (SNR). Providing a closed-form equation for SNR of a detector is involved; however, calculating its upper limit, SNR_{max} , generally referred to as the shot-noise limit, is straightforward. If we assume that the detector dark current is I_{dc} , then SNR_{max} for the detection bandwidth of BW becomes

$$SNR_{\rm max} = \frac{I_{em}^2}{2q(I_{em} + I_{bk} + I_{dc})BW}$$
. (8)

In Table I, we have provided some numerical examples to better illustrate the design tradeoffs according to the formulations above. As expected from (7), the wavelength selectivity (e.g., having a sharp emission filter) has a first-order effect on the SNR. Yet, at elevated temperatures, I_{dc} , becomes significant. This indicates that photodetector is required to have a high detection dynamic range (DDR) to be able to measure I_{em} in presence of large $I_{bk} + I_{dc}$ values. It is also suitable to build a photodetector with an input-referred noise smaller than the shot-noise of the background with a R.M.S. value of $\sqrt{2q(I_{bk} + I_{dc})BW}$.

B. DNA/RNA testing

Generally speaking, DNA/RNA analytes and probes have little bearing on the optical design of fluorescence biosensors as they only show absorption at wavelengths < 310 nm [26], which ensures their non-interference with fluorescence detection within the 450–800 nm "useful" range. However, there are key specifications with respect to DNA probe immobilization that the biochip should always meet to ensure compatibility with DNA/RNA testing assays in MDx. The first is having a SiO₂ surface in contact with the aqueous sample [27]. The second requirement is the thermal stability of probes in pH buffer solutions (e.g., PBS or TBS [28]). The probes need to be stable up to 95°C to enable DNA/RNA amplification assays such as PCR processes [23]. The third specification is related to maximum probe density for target accessibility. In the case of DNA microarrays the "optimal" distance is suggested to be 6–9 nm (~1.2–2.8×10⁴ μ m⁻²) [27].

The number of elements within a biosensor array is a function of the number of analytes that one needs to detect multiplied by the required redundancy. In our CMOS biochip system, we

C. Proposed architecture

In Fig. 3a, we show the proposed and implemented biochip module, which includes a CMOS IC, integrated emission filter, DNA probes, and an optically transparent fluidic cap which not only confines the aqueous sample on top of the chip, but also facilitates its insertion and removal using an inlet port and an outlet port, respectively. The CMOS IC not only provides the high DDR photocurrent sensing, but also integrated heating and temperature sensing. The biochip and the fluidic cap are both assembled on a single PCB substrate with an edge I/O connector on its periphery to access the chip electronics.

In Fig. 3b, we illustrate the functional layers of an integrated CW fluorescence-based biosensor element and in Table II we listed the key system requirements and their justifications. The DNA capture probes are immobilized on top of the filter (surface SiO₂). The fluorophores are located within the probe regions and are excited by an external LED-based photon flux that passes through the fluidic cap and the sample. Beneath the filter, we have the IC metal layers (4 metal layers in our 0.25 μ m CMOS process). The top metal (M4) includes a passive heater structure, while M1-M3 are used as interconnects as well as creating an optical aperture for an N_{well}-P_{sub}.

III. Circuit implementation

A. Photodetector Implementation

In our biochip, we implemented a 1st-order Σ current-sensing modulator as the detector and a reverse-biased CMOS N_{well}-P_{sub} diode as the photon-to-electron transducer [31] within each biosensing element. The sensor input is the reverse-biased diode current, $I_D = I_{em} + I_{bk}$ + I_{dc} , which first feeds into a high DC gain integrator (Σ operator) followed by an ADC (quantizer), as shown in Fig. 4a. The digital output of the ADC which is the sensor output, also controls a DAC to subtract an output-dependent current from the input (operator).

There are multiple advantages of adopting this architecture in our system. First, it can achieve very high DDR (>100dB [32]) compared to 3T or 4T architectures used CMOS image sensor [33–34]. Second, it achieves superior noise performance due to Σ operation [35–37]. Third, the architecture provides a digital output that can be easily read sequentially (scanned) in an array format. Finally, it can be easily integrated in the allocated 100µm × 100µm allocated silicon area. The disadvantages are the need for large oversampling ratios (*OSR*) and a fast oversampling clock to obtain the high DDR and the presence of idle-tones for very low input currents [38].

The high-level circuit diagram of the photodetector is shown in Fig. 4b. The integrator is a capacitive transimpedance amplifier (CTIA) which integrates I_D on its feedback capacitor, C_F , while pinning the reverse-bias voltage of the photodiode to V_D . The ADC is a clocked comparator, comparing the output of the CTIA with V_C , at every oversampling clock, CLK_{Σ} , which has the frequency of f_{Σ} . The current DAC is implemented as a charge packet subtractor, which depending on the comparator output being 1 or 0, subtracts Q_1 or Q_2

from the CTIA input, respectively. It is important to note here that the Σ modulator is insensitive to the offset at the V_C node as the offset is suppressed by its noise transfer function (NTF), given by equation (9), where *f* is the frequency[38]. In our system, we set $V_C = V_D$.

$$NTF(f) = 1 - e^{\left(-j2\pi \frac{f}{f\Sigma\Delta}\right)}.$$
 (9)

In Fig. 5, we show the detailed transistor and gate-level circuit implementation of the photodetector and its timing diagram. The core element of CTIA is a folded-cascode **operational trans-conductance amplifier (OTA)** with an open loop gain of ~80dB and an output swing between 0.5–2V. The capacitive feedback path of the integrator has the option to place $C_{F1}(20\text{fF})$ or both C_{F1} and C_{F2} (180fF) in the feedback loop by using the gain switch transistors M_{N10} - M_{N11} . A two-transistor switch structure M_{N7} - M_{N8} with a keeper switch M_{N9} is used for resetting and ensuring output-independent leakage current.

A Yukawa clocked comparator [39] is utilized for comparing V_{OUT} , output of the CTIA, with input V_c on the rising edge of CLK_{Σ} . Its output is then captured by an S–R latch, creating D_{OUT} ; that can be read through column line $D_{OUT}[j]$ when ROW[i] is selected.

The charge subtraction DAC path consists of a fixed current source, generating I_{REF} , and a pulse generator circuit applying I_{REF} periodically, in short pulses, to the CTIA input terminal. In this circuit, M Φ_1 and M Φ_2 serve as the current steering switches, *i.e.*, when $\Phi_1 = 1$ and $\Phi_2 = 0$, the current is steered into the CTIA input, to perform the charge = subtraction operation. When $\Phi_1 = 0$ and $\Phi_2 = 1$, the current sinks into V_D node. The pulse generation circuit first selects between CLK_1 and CLK_2 depending on whether D_{OUT} is 1 or 0 respectively using gates I_1-I_4 . The resulting output is then used to generate non-overlapping current steering signals, Φ_1 and Φ_2 . The timing diagram of the system is shown in Fig. 5. Depending on CLK_1 and CLK_2 , the applied pulse durations for current steering pulses are t_{p1} and t_{p2} , corresponding to subtracted charge of $Q_1 = t_{p1}I_{REF}$ and $Q_2 = t_{p2}I_{REF}$ from the charge stored in C_F .

As shown in Fig. 5, an analog bottom-plate sampling circuit has also been implemented for the test mode. This circuit samples V_{OUT} onto a sampling capacitor $C_S(100\text{fF})$ which can then be read though the column analog output line $A_{OUT}[j]$, when ROW[i] is selected. The analog sampling circuit is used for debugging and characterization in the test mode. In this mode, one can directly observe V_{OUT} to measure voltage and charge distribution offsets.

In Fig. 6a and Fig. 6b, we show the layout and micrograph of the implemented photodetector. The total area is $100\mu m \times 100\mu m$ with a photodiode fill-factor of 25%.

B. Photodetector theoretical performance

1) Saturation level (well capacity)—The maximum detectable input current, $I_{D_{max}}$,

occurs when the Σ modulator feedback is continually subtracting, where

$$I_{D_{\text{max}}} = \Delta Q_1 f_{\Sigma \Delta} = I_{REF} t_{\text{p1}} f_{\Sigma \Delta} \,. \tag{10}$$

Consequently, the "well capacity", Q_{well} , for sampling period of $T_s = 1/BW$ and oversampling ratio, $OSR = f_{\Sigma}$ T_s , becomes

$$Q_{well} = \frac{I_{REF} t_{\rm p1} OSR}{q} \,. \tag{11}$$

In our design, I_{REF} is set to $1\mu A$, $f_{\Sigma} = 100$ KHz, and t_{p1} can be programmed to be 100ns (low gain) or 10ns (high gain), making $I_{D_{max}}$ equal to 10nA or 1nA, respectively.

2) Noise formulations—The main noise contributors in this photodetector are the photodiode and the current DAC. The total fluctuation in I_D can be formulated in the form of shot noise. The variance of the current fluctuations, σ_D^2 is computed by

$$\sigma_{\rm D}^2 = 2q(I_{em} + I_{bk} + I_{dc})BW. \quad (12)$$

To derive the noise contribution of current DAC, we need to first consider the feedback current, I(t) as the multiplication of I_{REF} and a current switching pulse train p(t). Assuming that a_i 's to be the fourier series coefficients of p(t) and f_P being its fundamental frequency, it can shown that the $I_{\Delta}^{2^-}(f)$, the power spectral density (PSD) of I(t) is

$$\overline{I_{\Delta}^2(f)} = \sum_{k=0}^{\infty} \alpha_k^2 \overline{I_{REF}^2(f - kf_P)}, \quad (13)$$

which can be used to formulate, σ_{Δ}^2 the variance of the noise contributed DAC, by

$$\sigma_{\Delta}^2 = \int_0^{BW} \sum_{k=0}^{\infty} \alpha_k^2 \overline{I_{REF}^2(f - kf_P)} df \,. \tag{14}$$

In our design, we considered the design criteria of $\sigma_D^2 > \sigma_\Delta^2$ to ensure that the photodetector be shot-noise limited. The consequence of that, is that the transistor sizes become large in the current source to reduce the flicker-noise of I_{REF} .

C. Array architecture

The top-level chip architecture of the biochip and the die photo are shown in Fig. 7a and Fig. 7b, respectively. The biochip has an array of 32×32 pixels including 13 temperature sensor pixels. These 13 temperature sensor pixels have the same circuitry as regular pixels, except that we cover the in-pixel photodiode with metal to block F_x^T and use I_{dc} to measure the temperature. The temperature dependence of I_{dc} [40] is given by

$$I_{dc} = I_0 e^{\left(-\frac{E_g}{xkT}\right)}, \quad (15)$$

where I_0 is a constant, E_g is the bandgap energy, k is the Boltzmann constant, x is a number between 1 and 2, and T is the absolute temperature.

Each element can be individually addressed through row and column decoders. The column multiplexer output $D_{OUT}[i, j]$ signal is sent off chip through low voltage differential signaling (LVDS) buffer. The analog output chain makes use of analog column amplifiers, which are multiplexed at column level to generate the $A_{OUT}[i, j]$ signal. All the clocks, row and column select signals, the analog reference voltages and power supply (3.3V) are applied from off chip.

The on-chip heating element is a 2.5Ω passive multi-finger metal resistor, implemented using the top metal layer (M4) to deliver up to 10W of power from a 5V source. The resistive lines are evenly distributed out across the chip, passing through both the array and its periphery.

D. Off-chip components and analysis

The reader electronics consists of a Spartan 6 XC6SLX16 FPGA, DACs, ADCs, chip edge connector, and drivers for the LED electronics and heater modules (Fig. 8). The single bit output data-stream $D_{out}[n, i, j]$, is fed into an array of sinc² filters in the FPGA for decimation and filtering. The FPGA then transfers the filtered data to the application software through a USB interface.

In the experimental setup, we create F_x using a low power blue LED module (~1W of at 470nm). The chip is mounted on an active heat sink consisting of a copper heat spreader and a fan. The active heatsink arrangement facilitates fast cooling of the chip when the on-chip heater is off. The temperature of the reaction chamber mounted on chip is controlled using the on-chip heater, active heatsink, and the I_{dc} temperature sensors.

One practical challenge in measuring low levels of photocurrent is the ever presence of I_{dc} . To address this, we use correlated double sampling (CDS) scheme, implemented off-chip using the FPGA and the application software. The LED module is periodically turned on and off and frames are collected from the chip during both ON ("bright" frame) and OFF ("dark" frame) phase. The dark frames are used to estimate I_{dc} and subtract it from I_D to obtain the $I_{em} + I_{bk}$. To remove the non-informative I_{bk} from the measurements, we then make use of

array elements where no probe is immobilized and use the estimated I_{bk} to derive I_{em} . To reduce the error in estimation of I_{bk} , we average I_{bk} of several neighboring pixels to reduce the effect of spatial variation and additionally, perform a gain calibration at each pixel site, by recording the pixel values at different LED intensities.

E. Measurement results

Fig. 9 shows the measured external quantum efficiency $Q_E(\lambda)$ of the photodiode. In Fig. 10, we report the mean I_{dc} measured across all pixels vs. temperature. A calibrated thermocouple was placed very close to the chip surface utilizing thermal epoxy to record the die temperature. The error bars specify the minimum and maximum I_{dc} across the array measured at each temperature. Histograms of pixel dark current distribution at 30°C and 90°C are also shown in Fig. 10. As evident, at higher temperatures, "hot pixels" emerge at the outer edge of the array.

Fig. 11 shows the linearity performance of the photodetector, defined as $(I_D - I_{dc})$ vs. F_x . Different F_x values were generated by utilizing a combination of neutral density filters (10¹–10⁶ attenuation) along with varying LED power settings. Measurements were taken using two different modes: (a) low gain mode: $t_{p1} = 100$ ns, $t_{p2} = 0(I_{D_{max}} = 10$ nA); and (b) high

gain mode: $t_{p1} = 10$ ns, $t_{p2} = 0$ ($I_{D_{max}} = 1$ nA). The *OSR* was set to 65535 with $f_{\Sigma} = 100$ kHz.

As expected, we obtained very linear photo-detection performance, over 5 orders of magnitude change (~10fA - 10nA) in F_x .

Fig. 12 shows both the simulated and measured *SNR* vs. F_x . The simulations adopt the formulations discussed in Section 3.2 to model the predicted *SNR*. Both the simulated and measured *SNR* drop from the shot-noise limit, near I_Q , defined as the input-current quantization limit, given by $I_Q \cong I_{D_{\text{max}}}/OSR$. Below this limit, not even a single transition should occur in Σ modulator during the entire CLK_{Σ} period. The lower limit of F_x occurs around 9×10^9 photons cm⁻² s⁻¹ and 9×10^{10} photons cm⁻² s⁻¹ corresponding to I_Q =15.2fA and 152fA for high gain and low gain modes, respectively. The *SNR* is close to its shot-noise limit at higher F_x , within ~1 dB. Deviation from the limit at $F_x > 10^{12}$ photons cm⁻² s⁻¹, is believed to be due to additional fluctuation added by the LED excitation source and its driver electronics.

Table III summarizes the performance of the chip which matches the requirements that we set in Section 2. Measurement techniques for CMOS image sensor characterization [41] were used to perform the aforementioned measurements.

IV. Post-CMOS wafer/chip processing steps

There are two key processing steps that need to be done after the CMOS wafers are manufactured, to convert the CMOS chip into a biochip. The first is the fabrication and patterning of the multi-dielectric emission filter and the second is the bio-functionalization of the surface.

A. Emission filter

We utilize a long-pass multi-dielectric emission filter [42] to selectively reject F_x at λ_x and allow F_e to pass through. The filter is patterned and deposited on top of the CMOS wafer utilizing a lift-off process. The high and low index dielectric materials used for the filter are TiO₂ and SiO₂, respectively with the exposed filter surface (top surface) being SiO₂. The filter coating is index matched to water at the peak of emission while the electric field at λ_x is maximized at the filter-water interface. The latter maximizes the surface-bound fluorophore excitation.

A disadvantage of interference filters is their high sensitivity to the angles of incidence (AOI). To address this challenge, we utilize a two-pronged solution. First, we have optimized the layer thicknesses in the emission filter to minimize AOI sensitivity. For optimization, we have extensively used Essential McLeod Software (Thin Film Center, Tucson AZ) and chosen the solution-filter interface layer to maximize the E-field. Second, we created a dual fluorophore construct that offers 85nm Stokes' shift, defined as the distance between peak emission and excitation.

Fig. 13a shows the cross-section SEM of the emission filter. The total filter thickness is ~8 μ m. Fig. 13b shows the measured emission filter transmittance vs. wavelength at different AOIs. The on-chip filter characterization is done using a light source with a monochromator. The wavelength is swept between 400 and 700nm and the chip's response to different wavelengths is recorded. In Fig. 13c, the measured excitation and emission spectrum of the fluorophore label is reported. The effective filter optical blocking ratio (OD) at $\lambda_x = 490$ nm (fluorophore peak excitation) is 3.6. For increasing AOIs up to 50°, the filter transmission band shifts only by 10 nm.

B. Surface bio-functionalization

CMOS chips bonded to FR4 PCBs are first cleaned using oxygen plasma and subsequently are exposed to (3-glycidoxypropyl) trimethoxysilane in a chemical vapor deposition (CVD) system without breaking vacuum. At this point, the covalently linked thin film bears epoxy functional groups that can react chemo-selectively with terminally labeled primary amine functionalities of synthetic DNA probes [43]. Non-contact piezo printing is then used to deposit ~80pL droplets of DNA solution onto individual photodetector elements at 100 μ m pitch. The printer's integrated real-time vision system and optical fiducial registration capabilities allow drop placement accuracy verification within ±10 μ m. Unreacted epoxy groups were rendered inert by treatment with a solution containing ethanolamine, a step that also removed excess (non-immobilized) DNA probes.

V. Assay implementation

To demonstrate the capabilities of our CMOS fluorescence biochip platform in detecting DNA/RNA from biological samples, we have successfully implemented a multi-analyte (multiplex) detection panel. This panel adopts a nucleic acid amplification testing (NAAT) method that includes PCR. The target analytes, to be detected by the chip, are seven common, unique respiratory pathogens listed in Table IV.

In Fig. 14a, we show the chip workflow. In the first phase, during reverse transcription PCR (RT-PCR) enzymatic process [44], the sample goes through thermal cycling steps required to replicate (amplify) specific DNA/RNA sequence stretches (amplification factor $>2^{30}$) that are unique to individual pathogens in the sample. This is done by adding two short DNA sequences for each pathogen, generally referred to as primers, in the sample.

To enable the fluorescence detection of amplicons, we took advantage of an inverse fluorescence detection assay [45] (Fig. 14b). In this approach, all the PCR primers are labelled with quencher molecules (BHQ2) for incorporation into generated amplicons. To detect these amplicons, we spot fluorescent labeled DNA capturing probes (see Table IV), targeting individual amplicons, at fixed coordinates of the array (probe map shown in Fig. 15). During the capturing step, the generated amplicons hybridize to the corresponding probes on the array, bringing the quencher in close proximity of fluorescent labels, and thereby temporarily reducing the fluorescence at the particular location. Therefore, by monitoring the signal reduction at different spots in the array, one can identify the target(s) present in the original sample. In addition to the target specific capture probes, there are positive and negative control, quality control and calibration probes present in the array as shown in Fig. 15. The positive control provides an expected positive result due to the intentional addition of a known target nucleic acid to the reaction mix (Polio in our case). The negative controls work in the opposite manner, providing an expected negative result due to the absence of any corresponding target. Positive and negative controls help verify system functionality, determine repeatability and the level of non-specific signal in the system. Quality control probes are used to verify surface functionalization and DNA printing parameters. Calibration probes provide correction for fluorophore properties and any variation in LED illumination uniformity.

To improve the specificity of the signal, we also perform a high-resolution melt (HRM) analysis [46] of the hybridized target-probe complexes to confirm that the correct target is reducing the signal at the corresponding probe coordinates. HRM analysis makes use of the fact that each probe-target DNA complex has a unique dissociation curve as a function of temperature (also known as the "melt" curve). This dissociation curve is a function of different properties of the DNA complex such as GC content, length, sequence and presence or absence of specific mutation and can serve as unique signature for a specific probe-target interaction. As shown in Fig. 14, the melt curve is performed by increasing the temperature from 55°C to 95°C and monitoring the return of the fluorescence signal as a function of temperature, as the probe-target complex 'melts' and the target DNA is dissociated from the surface bound complex.

In Fig. 16, we show the melt curve experimental results, including the array images and the relative fluorescence signal from different array elements for a sample containing FluA and FluB, for only the corresponding probes generate a melt curve signal and the negative control remained flat. In Fig. 17, we show averaged melt-curve results and the melting temperature $T_{\rm m}$ (defined as the temperature at which 50% of captured target-probe complexes detach from the surface), for all replicate spots, based on tests that include only one of the seven targets. The total time for experiment is ~2 hours, with ~1 hour of PCR cycling, ~0.5 hour of hybridization/capturing and ~0.5 hour of melt (Fig. 14a).

VI. Conclusion

In this paper, we demonstrate both in theory and practice that CW biosensors can be built using standard CMOS processes, without compromising any performance requirement that is critical for adoption in MDx or genomics research. As reported, CMOS ICs can be designed to include integrated photo-detectors, thermal controllers, and all the essential electronic components; yet ICs do not offer any meaningful solution for the wavelength selectivity that CW fluorescence detection requires. As a result, we developed and integrated multi-dielectric interference filters to augment the photodetectors. The fabrication of these filters can easily be added to the CMOS process at a later stage.

When it comes to adopting "biology" on CMOS biochips, the focus should be on the surface and its bio-functionalization. In this paper, we show that conventional CVD techniques and non-contact printing methods can be utilized to create reliable and reproducible arrays of DNA probes. It is important to mention that at this point that these steps are not fully ready for high volume manufacturing. Therefore, their manufacturing cost are not comparable to the IC fabrication cost. However, with the reemergence of arrays in biotechnology and the current growth rate in MDx, we believe that this issue will be addressed by itself.

As a proof-of-principle, we reported the successful results of a NAAT panel in this paper. Clearly, the relatively small upper respiratory virus panel utilizes only a small fraction of the capabilities of our CMOS biochip. With 1024 biosensors in our chip, one can offer assays that can detect 100's analytes simultaneously. This goal is exactly what we are aspiring to do and the next phase in this project.

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Biographies

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He has held engineering positions at Qualcomm, Silicon Laboratories and Bosch Research and Technology Center in 2007, 2008 and 2010 respectively. He co-founded InSilixa, a CMOS biochip company, where he is currently employed as an Engineering Manager. His primary research interests include biosensors and bioelectronics, specifically the design and development of CMOS biochip platforms for medical diagnostic applications. **Ritu Raj Singh** received his B.Tech. in Electrical Engineering from the Indian Institute of Technology Kanpur, India, in 2005, the M.A.Sc. Degree in Electrical and Computer engineering from the University of Toronto, Canada, in 2009, and the Ph.D. Degree in Electrical and Computer engineering from the University of Texas at Austin, USA, in 2013.

He has held engineering positions with Texas Instruments, Mentor Graphics and Silicon Laboratories in 2004, 2005-2006 and 2010 respectively. He was with InSilixa from 2012 to 2017, where he was a member of the co-founding team. Dr. Singh's research interests include circuits and systems for biological, medical and consumer sensory applications. He is the recipient of the proficiency medal for the best B.Tech project in electrical engineering at IIT Kanpur, in 2005, and the distinguished award winner as part of team InSilixa, at the Nokia Sensing XChallenge Competition, in 2013.

Mark W. McDermott (S'75, M'78, LM'14) was born in Detroit, MI., USA in 1951. He received the B.S. degree in electrical engineering from the University of New Mexico, Albuquerque, N.M, USA in 1977, the MSE degree in electrical engineering from the University of Texas, Austin, TX, USA in 1988 and the Ph.D. degree in Electrical Engineering from the University of Texas, Austin, TX, USA in 2014.

He has 40 years of industry experience in the product development of silicon systems. This includes: Senior Director at Apple, Inc., VP/GM at Intrinsity, Inc., VP of Engineering at Coherent Logix, CEO of DynaFlow Computing, Inc., VP Engineering at Somerset Embedded Technologies, Inc., VP Engineering at VisionFlow, Inc., GM and Director of the Texas Development Center for Intel Corporation, Director of the PowerPC Somerset Design Center and Director of the Austin Design Center for Cyrix, Inc. He also co-founded DynaFlow Computing, The Learning Labs, Somerset Embedded Technologies, Inc., VisionFlow, Inc., Logical Silicon Solutions, Inc., Accelerated Solutions Corp., and MonoCom Systems, Inc. He is currently an Entrepreneur in Residence and Lecturer in the Electrical and Computer Engineering Department at the University of Texas at Austin where he teaches courses in silicon system design and technical entrepreneurship.

Dr. McDermott is a Registered Professional Engineer in the State of Texas and a member of the IEEE, ACM and TSPE. He has 19 patents and 13 publications in the areas of VLSI design, engineering education and engineering management. Nicholas Wood was born in San Antonio, Texas in 1990. He received the B.S. degree in electrical engineering from the University of Texas, Austin, TX, USA, in 2013. He received the M.S. degree in electrical engineering from the University of Texas at Dallas, Richardson, TX in 2016. He is currently a Staff Engineer at InSilixa Inc. in Sunnyvale, CA specializing in embedded systems and software for CMOS biochips and biomedical instrumentation.

Pejman Naraghi-Arani was born in Tehran, Iran, in 1969. He received the B.S. degree in Biochemistry (1991) and the M.S. degree in Chemistry (1993) from the California State University, Fullerton, CA. He also completed M.S. (1996) and Ph.D. (2000) degrees at the University of California, Davis, CA, USA where he demonstrated the quasispecies nature of grapevine fanleaf nepovirus.

He completed post-doctoral training in the Enterovirus Branch at the Centers for Disease Control and Prevention where he studied viral pathogenicity related to sequence variations. He spent almost 11 years as Group Leader, Molecular Assays and Virology at Lawrence Livermore National Laboratory where he developed some 13 multiplex molecular assay panels in addition to work in biomarker discovery, predictive algorithms for viral evolution, and simplified sample preparation techniques. Since June 2015, he has served as Director of Research and Development at InSilixa, Inc.

Sara Bolouki received the B.S degree from Amir Kabir University of Technology, Tehran, Iran in 2000 and Master's degree from Tehran University, Tehran, Iran in 2002 all in Electrical Engineering. She has a second Master's degree from Stanford University, CA, USA focusing on statistics and machine learning. Sara Bolouki is currently working as a Senior Data Analyst at Insilixa Inc.

Kirsten A. Johnson received the B.S. degree in genetics from the University of California, Davis, CA, USA, in 2004 and the Ph.D. degree in biological sciences from the University of California, Irvine, CA, USA, in 2012.

In 2002 and 2003 she was a Summer Intern at Lawrence Livermore National Laboratory. From 2004 to 2006 she was a Biomedical Scientist at Lawrence Livermore National Laboratory, where she worked on multiplex PCR assay development. She was a Postdoctoral Fellow at the University of California, Irvine from 2012 to 2013, focusing on host-pathogen interactions. She is currently a Senior Scientist at InSilixa in Sunnyvale, CA, where she is engaged in assay development. Dr. Johnson is a member of the American Association for the Advancement of Science.

Robert G. Kuimelis was born in San Francisco, CA, USA in 1966. He received the B.S. degree in chemistry from St. Mary's College, Moraga, CA, USA in 1989 and the Ph.D. degree in organic chemistry from the University of California, Davis, CA, USA in 1993.

He was a Postdoctoral Fellow at Boston College, Chestnut Hill, MA, USA from 1994 to 1996, focusing on non-natural nucleic acids and catalytic RNA mechanism. From 1996 to 1997 he was with Applied Biosystems, Foster City, CA, USA. From 1997 to 2002 he was with Phylos, Lexington, MA, USA. From 2002 to 2011 he was with Affymetrix, Santa Clara, CA, USA as Director of Chemistry. From 2012 to 2015 he was with HealthTell, Chandler, AZ, USA as Senior Director of Chemistry. Since 2015 he has been with InSilixa, Sunnyvale, CA, USA as Vice President of Chemistry. He is the author of several book chapters and many published articles in the areas of nucleic acid chemistry and biophysics, surface chemistry and DNA/peptide/protein microarrays. He holds 27 issued United States Patents. His scientific interests are at the interface of chemistry, biology and technology and the development of innovative platforms with broad potential to impact health. Dr. Kuimelis is a member of the American Chemical Society.

Gary Schoolnik received his M.D. and clinical infectious diseases training at the University of Washington in Seattle in 1972, was Intern, Junior, Senior and Chief Resident in Medicine

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Dr. Schoolnik is currently a Professor of Medicine, (Emeritus, Active) at Stanford Medical School and Attending Physician in Infectious Diseases and Internal Medicine at Stanford University Hospital. Dr. Schoolnik's research career has been focused on the use of genomics, molecular biology and biochemistry to elucidate how bacteria cause disease and the use of this information to develop novel diagnostics and to identify drug targets, biomarkers and antigens. He is also serving as the Chief Medical Officer for InSilixa.

Dr. Schoolnik serves or has served on a number of advisory boards including the National Advisory Allergy and Infectious Diseases Council which is the principal advisory body of the National Institute of Allergy and Infectious Diseases (NIAID).

Arjang Hassibi (SM'99, M'05, SM'10) received his B.Sc. degree (1997) with the highest honors from the University of Tehran, Iran, and both his M.Sc. (2001) and Ph.D. (2005) degrees from Stanford University, all in electrical engineering. He attended California Institute of Technology for his postdoctoral training. Dr. Hassibi's area of expertise is the intersection of biotechnology and engineering, specifically biosensors and bio-electronics, biomedical electronics, and integrated sensors. He was a faculty member at the Electrical and Computer Engineering Department and the Institute for Cellular and Molecular Biology of The University of Texas at Austin (2006-2011) and a visiting professor at IBM Research at Yorktown (2010-11). Prior to that, he has held various R&D positions in both industry and academia including 2nd lieutenant at Iranian Army Research Center (1997-99), Stanford Microwave Integrated Circuits (SMIrC) Lab (1999-2005), Stanford Genome Technology Center (2001-2003), CMOS High Speed Integrated Circuits (CHIC) Lab at Caltech (2005-2006), and Xagros Genomics, a startup company which he co-founded in 2001. Most recently, he was the Healthcare Thrust Lead at the Silicon Research Corporation (SRC). He has been the CEO and Founder of InSilixa, Inc. since 2012.

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Fig. 2.

Simplified cross-section of a chip-based fluorescence biosensors system with an embedded photodetector beneath the capturing probe area.



Fig. 3.

(a) Implemented biochip module; and (b) functional layers of an integrated CW fluorescence-based biosensor element (pixel).

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BLOCK DIAGRAM



































Measured photodiode dark current (I_{dc}) vs. temperature along with minimum and maximum spatial variations.



Fig. 11. Measured photodetector current vs. incident photon flux.









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(a) Scanning electron microscope (SEM) cross-section of the chip + integrated filter; (b) measured filter transmittance vs. AOIs; and (c) excitation and emission spectrum of the dual fluorophore construct.

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Fig. 14.

(a) Temperature profile for NAAT workflow with PCR cycling, capturing (hybridization), and melt steps; and (b) conceptual molecular diagram describing these three steps.



Fig. 15.

Probe map for a 12×12 biosensing block: (1) FluA, (2) FluB, (3) RSV, (4) HPIV, (5) AdvC2, (6) AdvE, (7) Pos. Ctrl, and (8) Neg. Ctrl. C1/C2 are quality control/calibration probes. White spots (–) are blank spots that can be used for I_{bk} subtraction.

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Fig. 16.

Measured melt curves for a sample containing FluA and FluB. The top images shows array snapshots at four different temperatures. The bottom set of images show the melt curves obtained from individual spots containing probes for FluA, FluB and negative control. The data is calibrated for temperature dependence of fluorophore intensity. It is expressed in relative fluorescence units (RFU).

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Fig. 17.

Average melt-curves from all the replicate spots for each of the seven pathogens and probes listed in table IV. T_m refers to the melt temperature, i.e. the temperature at which 50% of hybridized probe-target complex has melted from the surface.

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TABLE I

ⁿ N	merical exa	unples of SI	NR and Sig	gnal-to-bac	kground r:	atio of a biosen	sor system.	
	I _{em} (A)	$\frac{K_{\theta,\varphi}}{K'_{\theta,\varphi}}$	(V) Ibk	T(°C)	$I_{dc}\left(A ight)$	$\frac{I_{em}}{I_{bk}+I_{dc}}$	SNR _{max}	
		102	-01	30	20f	2.29×10^{-5}	3.12	
	364	10-	11.7.1	06	5.5p	2.29×10^{-5}	3.11	
	164	501	-01	30	20f	2.27×10^{-2}	3.02×10^{3}	
		10,	1.9p	06	5.5p	5.89×10^{-3}	$7.97{\times}10^{2}$	
		201	-0-	30	20f	2.29×10^{-3}	3.12×10^{4}	
		10-	11.7.1	06	5.5p	2.29×10^{-3}	3.11×10^{4}	
	4c.4	501	-01	30	20f	2.27	9.45×10^{6}	
		10,	1.9p	06	5.5p	0.589	5.05×10^{6}	
								,
				B. Paran	neters			
	Param	eter		Value		Paramete	L	
Ιđ	oton flux d	ancity (F)	5~1	0172	;		1	L

	B. Parameter	S	
Parameter	Value	Parameter	Value
Photon flux density (F_x)	$5 \times 10^{17} \mathrm{cm^{-2} s^{-1}}$	Max. quantum yield $(\mathcal{Q}_Y(\mathcal{\lambda}_e))^I$	0.04
Probe surface area (S)	$2500 \ \mu m^2$	Transmittance efficiency $(K_{oldsymbol{ heta}}_{oldsymbol{ heta}})$	0.25
Diode surface area	$2500 \ \mu m^2$	Max. photodiode ${\mathcal Q}_E{}^2$	0.38
Max. extinction coefficient ($m{e})^I$	$150,000 \ {\rm cm^{-1} M^{-1}}$	Detection bandwidth (BW)	1 Hz
I_{1}	1001		

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Values are for Cyanine-3 (Cy3) fluorophore [20].

 $^2\mathrm{Typical}$ value for Nwell-P $_{sub}$ diode in 0.25 μm CMOS process

TABLE II

Biochip Requirements

Parameter	Value	Justification
Biosensor dimensions	100µm×100µm	Min. pitch for surface spotting techniques. Min. area for high DDR Σ modulator.
No. of biosensing elements	1024 (32×32)	Enables infectious disease MDx applications. Capable of running 100–200 NAAT tests with sufficient redundancy.
Detection dynamic range (DDR)	>100dB	Need to detect $I_D \sim 20$ fA-10nA (Table 1A).
Frame rate	~1Hz	Suitable for monitoring DNA hybridization reaction ($\tau > 30$ s). Sufficient OSR for high DDR Σ operation.
Operating temperature	25°C – 100°C	Required for PCR (Fig. 14).
Quantum efficiency $(Q_E(\lambda))$	> 0.2 for 500nm λ 600nm	Required for sufficient I _{em} values (Table 1A).
Max. input photocurrent	10nA	Max. $I_{bk} + I_{dc}$ (Table 1A).
Min. input photocurrent	10fA	Min. I _{em} (Table 1A).
Photodetector noise contribution	< Shot-noise	Fundamental noise limit for a current sensor (Section 3.2.1).
Temperature sensor resolution	< 0.5°C	Accuracy of PCR machines ~ $\pm 0.25^{\circ}$ C [23].
On-chip heater power	10W	Required for achieving $> 5^{\circ}C/s$ ramp rates for PCR [23].
Chip output format	Digital	Greatly simplifies array output data handling.
Optical filter rejection	>10 ^{3.5} at λ = 490 nm	I
${}^{K_{oldsymbol{ heta}},oldsymbol{\phi}'K_{oldsymbol{ heta},oldsymbol{\phi}}}$	>1000	Necessary for sufficiently high $\frac{1}{I_{bk} + I_{dc}}$ (signal to background) ratio (Table 1A).
Top surface material	SiO ₂	Most suitable for thermo-stable DNA probe immobilization [27] (Section 2.2).
Surface probe density	$\sim 1.2 - 2.8 \times 10^4 \ \mu m^{-2}$	Optimal distance for target accessibility (Section 2.2).

TABLE III

Chip Performance Summary

Technology	0.25 μm CMOS
Supply voltage	2.5V
Die dimensions	7.0mm × 9.0mm
Array dimensions	32 × 32
Biosensor size	$100 \mu m \times 100 \mu m$
Photodiode	$50\mu m \times 50\mu m (N_{well} - P_{sub})$
Photodetector dark current (I_{dc})	20 fA (30°C)–5.5 pA (90°C)
Photodetector $Q_E(\lambda)$	>0.38 (500nm - 775nm)
Frame rate (BW)	1Hz to 50Hz
Well capacity (Q_{well})	6.25 × 10 ¹⁰ e ⁻ (Low Gain) 6.25 × 10 ⁹ e ⁻ (High Gain)
Maximum photocurrent ($I_{D_{\max}}$)	10nA (Low Gain) at $f_{\Sigma} = 100 \text{ kHz}$ 1nA (High Gain) at $f_{\Sigma} = 100 \text{ kHz}$
CLK ₁ , CLK ₂ pulse width (t _{\$1,2})	10ns - 100ns
Detection linearity	>10 ⁵
Detection dynamic range (DDR)	116 dB
Chip Output	102.4 Mbps LVDS serialized
Heating/cooling rate	4°C/sec
Operating temperature range	25°C – 100°C
Temperature sensor resolution	0.3° C (for chip temperature 45° C – 100° C)
Total power consumption	62mW (array) + 50mW (LVDS) + 6mW (analog column amplifiers) = 118mW

TABLE IV

List of pathogens detected by NAAT panel along with the probe sequences used for solid-phase capturing and melt.

Pathogen Name	Probe Sequence (5'-3')
Influenza A (FluA)	Linker-GACCTAGTTGTTCTCGCCAGTGGAGGTCC-Fluor
Influenza B (FluB)	Linker-CACCGCAGTTTCAGCTGCTCGAATTGGGAGTTGAGGA-Fluor
Respiratory Syncytial Virus (RSV)	Linker-CTGTGTATGTGGAGCCTTCGTGAAGCTTCCATATTTGCC-Fluor
Human Parainfluenza Virus (HPIV)	Linker-GCGATTGATTCCATCACTTAGGTAAATTGGAAATGC-Fluor
Human Adenovirus C (AdvC2)	Linker-GCTCCACATAATCTAACACAAACTCCTCACCCCAGGTAAGATCGA-Fluor
Human Adenovirus E (AdvE)	Linker-TTTAATCATGGTTCTTCCTGTTCTTCCCCCCAAATTGCA-Fluor
Polio (Positive Control)	Linker-TATCCGGTGAAAGTGAGATTCATTATCATTCGCTCCAT-Fluor
Negative Control	Linker-CAAAGTGGGAGACGTCGTTGT-Fluor