

New Associate Editors

It is with pleasure that I introduce three new associate editors who have joined the editorial board of the IEEE JOURNAL OF SOLID-STATE CIRCUITS. Dr. D. Friedman is an authority with more than 20 years of industry experience in wireline communications and millimeter-wave/RF IC design. Prof. P. Heydari brings in extensive experience in the field of terahertz/millimeter-wave/RF and analog integrated circuits, where his research has been focused on emerging applications, including biotechnology, remote sensing, and gigabit wireless and wireline communications, and Prof. J. Kim joins the board

to further strengthen the topics of wireline connectivity and power management.

At the same time, Dr. J. Kenney, Prof. H. Hashemi, and Prof. P. Hanumolu will all retire from their associate editor role, after many years of excellent service. I want to thank them all for the effort and time they have spent for JSSC.

JAN CRANINCKX, *Editor-in-Chief*
imec,
Leuven, Belgium



Daniel Friedman received the Ph.D. degree from Harvard University, Cambridge, MA, USA, in 1992.

He completed his post-doctoral work at Harvard University and consulting work with the MIT Lincoln Laboratory, Lexington, MA, USA, broadly in image sensor design. In 1994, he joined the IBM Thomas J. Watson Research Center, Yorktown Heights, NY, USA, where he developed field-powered RFID tags, before turning to high-data-rate wireline and wireless communication. He is currently a Distinguished Research Staff Member and the Senior Manager of the Communication Circuits and Systems Department, IBM Thomas J. Watson Research Center. He has authored or coauthored more than 75 publications. He holds more than 50 patents. His current research interests include high-speed I/O design, PLL design, mmWave circuits and systems, and circuit/system approaches to enabling new computing paradigms.

Dr. Friedman was a member of the BCTM Technical Program Committee from 2003 to 2008 and the ISSCC International Technical Program Committee from 2009 to 2016. He has been a member of the SSCS Adcom since 2018. He was a co-recipient of the Beatrice Winner Award for Editorial Excellence at the 2009 ISSCC, the 2009 JSSC Best Paper Award in 2011, the 2017 ISSCC Lewis Winner Outstanding Paper Award, and the 2017 JSSC Best Paper Award in 2019. He served as the Wireline Sub-Committee Chair from 2012 to 2016. He has served as the Short Course Chair since 2017.



Payam Heydari (M'01–SM'09–F'17) received the B.S. and M.S. degrees in electrical engineering from the Sharif University of Technology, Tehran, Iran, in 1992 and 1995, respectively, and the Ph.D. degree from the University of Southern California, Los Angeles, CA, USA, in 2001.

He is currently a Full Professor of electrical engineering with the University of California at Irvine (UC Irvine), Irvine, CA, USA, where he is also the Director of the Nanoscale Communication IC (NCIC) Labs. He has coauthored 2 books, 2 book chapters, and more than 150 journals and conference papers. His current research interests include the design of terahertz/millimeter-wave/RF and analog integrated circuits.

Dr. Heydari is an AdCom member of the IEEE Solid-State Circuits Society. He was a recipient of the 2016–2017 UCI's School of Engineering Mid-Career Excellence in Research, the 2014 Distinguished Engineering Educator Award from Orange County Engineering Council, the 2009 Business Plan Competition First Place Prize Award and the Best Concept Paper Award

both from Paul Merage School of Business at UC Irvine, the 2010 Faculty of the Year Award from UC Irvine's Engineering Student Council (ECS), the 2009 School of Engineering Best Faculty Research Award, the 2007 IEEE Circuits and Systems Society Guillemín-Cauer Award, the 2005 IEEE Circuits and Systems Society Darlington Award, the 2005 National Science Foundation (NSF) CAREER Award, the 2005 Henry Samueli School of Engineering Teaching Excellence Award, and the Best Paper Award at the 2000 IEEE Int'l Conference on Computer Design (ICCD). He was recognized as the 2004 Outstanding Faculty of the EECS Department, UC Irvine. His research on novel low-power multipurpose multiantenna RF front ends received the Low-Power Design Contest Award at the 2008 IEEE International Symposium on Low-Power Electronics and Design (ISLPED). He served as an Invited Distinguished Speaker for the 2014 IEEE Midwest Symposium on Circuits and Systems. He was a Distinguished Lecturer of the IEEE Solid-State Circuits Society from 2014 to 2016, and has been a Distinguished Microwave Lecturer of the IEEE Microwave Theory and Techniques Society since 2019. He served on the Technical Program Committee of the International Solid-State Circuits Conference (ISSCC) from 2015 to 2019. He and his group were among the first who introduced the design of millimeter-wave integrated circuits in silicon technologies. He currently serves as the Guest Editor for the IEEE JOURNAL OF SOLID-STATE CIRCUITS (JSSC). He has given keynote speech at the IEEE GlobalSIP 2013 Symposium on Millimeter Wave Imaging and Communications, and gave a tutorial at the 2017 International Solid-State Circuits Conference (ISSCC).



Jaeha Kim (S'95–M'03–SM'09) received the B.S. degree in electrical engineering from Seoul National University, Seoul, South Korea, in 1997, and the M.S. and Ph.D. degrees in electrical engineering from Stanford University, Stanford, CA, USA, in 1999 and 2003, respectively.

From 2001 to 2003, he was a circuit designer with True Circuits, Inc., Los Altos, CA, USA. From 2003 to 2006, he was a Post-doctoral Researcher with the Inter-university Semiconductor Research Center (ISRC), Seoul National University. From 2006 to 2009, he was the Principal Engineer with Rambus, Inc., Los Altos, CA, USA. From 2009 to 2010, he was an Acting Assistant Professor with Stanford University. He is currently an Associate Professor with Seoul National University. In 2015, he founded Scientific Analog, Inc., an EDA company on analog-/mixed-signal verification. He is cited as the Top 100 Technology Leader of Korea in 2020 by the National Academy of Engineering of Korea (NAEK). His current research interests include low-power mixed-signal systems and their design methodologies.

Dr. Kim was a recipient of the Takuo Sugano Award for outstanding Far-East paper at the 2005 International Solid-State Circuits Conference (ISSCC) and the Low Power Design Contest Award at the 2001 International Symposium on Low Power Electronics and Design (ISLPED). He served on the technical program committees of International Solid-State Circuits Conference, Custom Integrated Circuits Conference, Design Automation Conference (DAC), International Conference on Computer Aided Design (ICCAD), and Asian Solid-State Circuit Conference (A-SSCC). He served as an Associate Editor for the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I and the IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS.