

Message From the Outgoing Editor-in-Chief

IT is with mixed feelings of relief and regret that I am writing this last editorial in my role as the Editor-in-Chief (EiC) of the IEEE JOURNAL OF SOLID-STATE CIRCUITS (JSSC). Relief, because it has been a significant workload in the past three years to fulfill my duties as EiC, and I am looking forward to some more free time. I did not meet all my deadlines, sometimes accumulated a lot of papers on my to-do stack, but still, I hope I managed to guide the JSSC smoothly along its traditional tracks and delivered interesting and innovative papers for you to read every month.

Regret also, because it does mark for me the end of an exciting part of my life. Ever since I have been an IEEE and Solid-State Circuits Society (SSCS) member, I have been very proud of “our” red JSSC, and the fact of being able to call it a bit “my” JSSC in the past three years has been very rewarding.

Obviously, all of that would not have been possible without the strong and dedicated team of people which also keeps the JSSC on track. The IEEE journal editors, Elizabeth Stewart and Sara Scudder, take care of the final paper formatting and the composition of each issue. They work closely with the authors and have been very flexible to publish every issue on time, despite my often last-minute inputs. Sonal Parikh and Alison Larkin have supported me in the use of the ScholarOne submission and review system. Abira Altvater has given all the necessary support from the Solid-State Circuits Society and the JSSC webpage. And of course, the JSSC administrative assistants, first Matt Erikson and recently Ania Levinson, have really been the persons who kept me on track. They have helped tremendously by solving many daily questions and issues from authors, reviewers, and associate/guest editors. I would not have survived without all your support, many thanks to you all.

I also want to thank my predecessors, the past EiCs, especially Prof. Mike Flynn, Prof. Un-Ku Moon, and Prof. Bram Nauta. I have often consulted with them on issues an EiC has to deal with, and their guidance and advice have definitively helped me in my duties and decisions. Prof. John Long, the SSCS publications’ chair, has often been called upon for advice and was always there to help. Thanks also go to the SSCS director, Mike Kelly, and to the former and current presidents, Jan Van der Spiegel and Bram Nauta, for their support.

And last but not least, the JSSC would not be what it is without the dedication of the editors. The quality and reputation of the JSSC is ensured by its thorough review process, which is handled by a team of 20 fixed associate editors and complemented with the guest editors for the conference

special issues. Together, they handle ~500 manuscripts submitted to the JSSC each year. Each of them is an expert in his or her technical field and dedicates a significant amount of his/her time to guide the manuscripts through an in-depth and fair review and revision process. I am also indebted to the anonymous reviewers of each manuscript, who voluntarily put their expertise in service of the solid-state circuits’ community. It is the work of the editors and reviewers, not the EiC, that finally determines the quality and relevance of the papers you read.

Now I am glad to pass the helm of the JSSC to Prof. Pavan Hanumolu from the University of Illinois at Urbana-Champaign, Champaign, IL, USA. Prof. Hanumolu has a large experience throughout his research, has made many contributions to the JSSC as an author and a reviewer, and has done an excellent service as an associate editor. I am confident that he will be a very competent lead for the JSSC to further strengthen our reputation as the premier venue for the publication of the state-of-the-art results in solid-state circuits.

Finally, it turns out that my role as the EiC is not yet over, albeit for another journal. Many of you might have heard about plan-S, the initiative of Coalition-S to require scientific publications that result from research funded by public grants to be published in compliant open access journals. To allow researchers constraint by this request to keep on submitting their work to a prestigious journal of the Solid-State Circuits Society, we will be introducing the IEEE Open Journal of Solid-State Circuits (O-JSSC), a full open access journal that will operate fully in tandem with the JSSC, the only difference being the subscription model. The O-JSSC will be set up with the same standards as the regular “red” JSSC. It will have the same quality standards and the same review process. The editorial board will be composed of members who have served on the editorial board of the JSSC to ensure that authors in the new O-JSSC will inherit the same reputation as our trusted JSSC. And hence, I will have the honor to serve as the first EiC of the O-JSSC during its start-up. As the details of plan-S still are under discussion, the pure gold open-access model just started up at IEEE, and SSCS as one of the early adopters, the near future is still unclear. But the O-JSSC will secure our future in publishing and enable us to serve all of our members. So perhaps, we will meet again in the open-access world.

It has been an honor serving you,

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