

New Associate Editors

IT IS with great pleasure that I welcome Danielle Griffith, Shanthi Pavan, and Nick Van Helleputte to the Editorial Board of the IEEE JOURNAL OF SOLID-STATE CIRCUITS as new Associate Editors. Griffith is an expert on low-power wireless circuits, oscillators, and MEMS. Pavan brings expertise in data converters, filters, and mixed-signal circuits. Van Helleputte is an expert on biomedical circuits, power management, and sensor interfaces.

Andrea Baschiroto, Waleed Khalil, and Hoi-Jun Yoo have retired as Associate Editors. We thank them for their dedicated service to the Journal.

PAVAN KUMAR HANUMOLU, *Editor-in-Chief*
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Danielle Griffith received the B.S.E.E. and M.Eng. degrees from the Massachusetts Institute of Technology, Cambridge, MA, USA, in 1996 and 1997, respectively.

She has been with Texas Instruments, Dallas, TX, USA, since 2003, where she is a fellow of the Connectivity Business Unit. She develops circuits and techniques for reducing cost, power consumption, and circuit board area for wireless connectivity products that support standards, such as Bluetooth Low Energy and Zigbee. She has published more than 50 papers, most of them in IEEE journals or conferences. She has written a book chapter titled Synchronization Clocks for Ultra-Low Power Wireless Networks as a part of the book *Ultra-Low-Power Short-Range Radios* (Springer). She holds 18 issued U.S. patents. Her current focus areas are circuits and architectures for efficient wireless systems, low-power oscillators, and MEMS.

Ms. Griffith was a member of the Technical Program Committees for the IEEE RFIC Symposium from 2014 to 2015 and the IEEE International Solid-State Circuits Conference from 2015 to 2019. She has been a member of the VLSI Symposium since 2019. She has given

multiple university and IEEE conference tutorial and workshop sessions.



Shanthi Pavan (Fellow, IEEE) received the B.Tech. degree in electronics and communication engineering from IIT Madras, Chennai, India, in 1995, and the M.S and Sc.D degrees from Columbia University, New York, NY, USA, in 1997 and 1999, respectively.

From 1997 to 2000, he was with Texas Instruments, Warren, NJ, USA, where he worked on high-speed analog filters and data converters. From 2000 to June 2002, he worked on microwave ICs for data communication at Bigbear Networks, Sunnyvale, CA, USA. Since July 2002, he has been with IIT Madras, where he is currently a Professor of electrical engineering. He is the author of *Understanding Delta-Sigma Data Converters* (Second Edition), with Richard Schreier and Gabor Temes. His research interests are in the areas of high-speed analog circuit design and signal processing.

Dr. Pavan is a fellow of the Indian National Academy of Engineering. He was a recipient of several awards, including the IEEE Circuits and Systems Society Darlington Best Paper Award in 2009, the Shanti Swarup Bhatnagar Award in 2012, and the Swarnajayanthi Fellowship in 2009 (from the Government of India). He has served as the Editor-in-Chief for the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—PART I: REGULAR PAPERS. He has served on the Technical Program Committee of the International Solid-State Circuits Conference. He has been a Distinguished Lecturer of the Solid-State Circuits and Circuits-and-Systems Societies.



Nick Van Helleputte (Member, IEEE) received the M.S. degree in electrical engineering from the Katholieke Universiteit Leuven, Leuven, Belgium, in 2004, and the Ph.D. degree from the MICAS Research Group, Katholieke Universiteit Leuven, in 2009. His Ph.D. research focused on low-power ultrawideband analog front-end receivers for ranging applications.

In 2009, he joined imec, Leuven, as an Analog R&D Design Engineer. He is currently the R&D Manager of the Connected Health Solutions, Inc., Brooklyn, NY, USA. His research focus is on ultralow-power circuits for biomedical applications. He has been involved in analog and mixed-signal application-specific integrated circuit (ASIC) design for wearable and implantable healthcare applications. He has developed ultralow-power custom ICs for multimodal vital signs sensing. His research focused on complete system-on-chip solutions covering all aspects, including analog amplification and filtering, analog-to-digital conversion, and digital signal and processing power management. He also worked on neural interfaces in the form of active high-density neural probes for the central and peripheral nervous systems. In addition to the IC

design, his research group has a strong focus on highly miniaturized and ultralow-power systems based on COTS as well as their custom ASICs. His research collaborations included early pathway research (TRL 1–5) as well as bilateral collaborations with industrial partners toward novel product developments (TRL 5–8).

Dr. Van Helleputte is a member of the IEEE Solid-State Circuits Society (SSCS). He served on the Technical Program Committee of the VLSI Circuits Symposium and the International Solid-State Circuits Conference. He was a Distinguished Lecturer of the IEEE SSCS from 2017 to 2018.