

Errata

Erratum to “A High-Voltage Compliance, 32-Channel Digitally Interfaced Neuromodulation System-on-Chip”

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In the above article [1], an earlier version of Fig. 4 was used. Therein, recent state-of-the-art designs are placed according to performance metrics NEF and area, but the references in Fig. 4 do not match the reference section.

The correct Fig. 4 is given below.

REFERENCES

- [1] S. Reich, M. Sporer, M. Haas, J. Becker, M. Schüttler, and M. Ortmanns, “A high-voltage compliance, 32-channel digitally interfaced neuromodulation system on chip,” *IEEE J. Solid-State Circuits*, early access, May 11, 2021, doi: [10.1109/JSSC.2021.3076510](https://doi.org/10.1109/JSSC.2021.3076510).

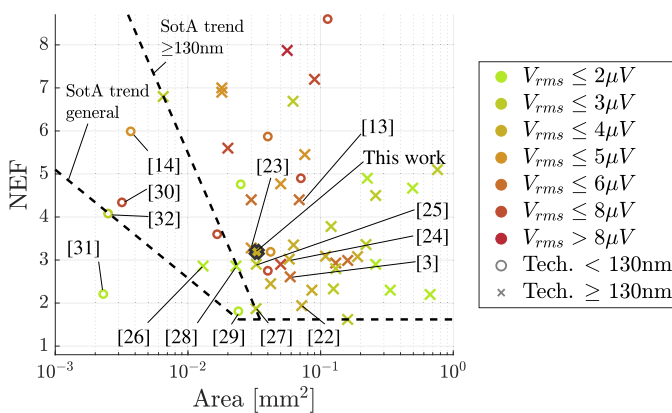


Fig. 4. Performance of the presented neural recorder compared to the SotA (JSSC and TBioCAS, 2010–present).

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