

Message From the Incoming Editor-in-Chief

I WOULD like to thank the Solid-State Circuits Society (SSCS) leadership and Administrative Committee for appointing me as the new Editor-in-Chief (EiC) of the IEEE JOURNAL OF SOLID-STATE CIRCUITS (JSSC). It is both a great honor and opportunity to lead the flagship SSCS journal. I want to additionally thank the outgoing EiC Prof. Pavan Hanumolu of University of Illinois at Urbana-Champaign, who has been very helpful in the transition this year.

The JSSC community, i.e., the integrated circuit design field, consists of both academics, always striving to push the boundaries of a maturing field that is now more than 60 years old, and practicing engineers in industry, developing practical yet innovative designs that define the current state of the art.

For academics, it has long been the top goal to publish the full results of a project in JSSC. This was the case for me as a student, is still the case for my own students today (25 years later), and I hope will continue to be the case for years to come. But in this day and age standing still is not enough; we need to make sure JSSC is providing a fair and inclusive

publication forum across the world for the entire community. I will be looking into data-driven ways to facilitate this during my term as EiC and will be sharing my findings and thoughts on this topic in later editorials.

For industry—we need their contributions. I distinctly recall when I was a Ph.D. student reading the DEC Alpha 21X64 microprocessor papers in JSSC in all their wonderful detail, amazed at what I could learn about bleeding edge processor design. I also want to remind readers that the best-cited JSSC paper of all time was from industry (Dennard's famous scaling paper from 1974). These types of papers help students understand the work happening inside companies and also provide valuable benchmarks for researchers. We need more of these types of papers, and I will be working on initiatives to ensure we get them to maximize the value of JSSC to the community.

In closing, I want to provide my JSSC contact information and ask that anyone with thoughts, ideas, or general feedback about how to improve the journal please reach out to me at jssc.eic.sylvester@gmail.com. I look forward to serving in this important role for the coming three years.



Dennis Sylvester (Fellow, IEEE) received the Ph.D. degree in electrical engineering from the University of California at Berkeley (UC-Berkeley), Berkeley, CA, USA, in 1999.

He is the Edward S. Davidson Collegiate Professor of electrical and computer engineering at the University of Michigan, Ann Arbor, MI, USA, and was the founding Director of the Michigan Integrated Circuits Laboratory (MICL), Ann Arbor, MI, USA, a group of ten faculty and 70+ graduate students. He has held research staff positions at the Advanced Technology Group of Synopsys, Mountain View, CA, USA; Hewlett-Packard Laboratories, Palo Alto, CA, USA; and visiting professorships at the National University of Singapore, Singapore, and Nanyang Technological University, Singapore. He has co-founded Ambiq, Austin, TX, USA, a fabless semiconductor company developing ultralow power mixed-signal solutions for compact wireless devices. He has published over 550 articles along with one book and several book chapters. He holds 51 U.S. patents. His research interests include the design of millimeter-scale computing systems and energy efficient near-threshold computing.

Dr. Sylvester received an NSF CAREER Award, the Beatrice Winner Award at International Solid-State Circuits Conference (ISSCC), the IBM Faculty Award, the Semiconductor Research Corporation (SRC) Inventor Recognition Award, and 15 best paper awards and nominations. He was named one of the Top Contributing Authors at ISSCC, most prolific author at IEEE Symposium on VLSI Circuits, and was awarded the University of Michigan Henry Russel Award for distinguished scholarship. His Ph.D. dissertation was recognized with the David J. Sakrison Memorial Prize as the most outstanding research at the Electrical Engineering and Computer Science (EECS) Department, UC-Berkeley. He previously served on the administrative committee for the IEEE Solid-State Circuits Society. He was an Associate Editor of IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS, and IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION (VLSI) SYSTEMS, and served as an IEEE Solid-State Circuits Society Distinguished Lecturer.