

Editorial

GREETINGS from the IEEE EMBEDDED SYSTEMS LETTERS (ESL) editorial board! As our term draws to a conclusion this year, it is time to reflect on the current status of the journal and the way forward.

ESL's video preview feature was very well received by the researcher community and is now a regular feature—all accepted papers are now accompanied by a short video preview introducing the work. The freely accessible videos target a wider audience and form a succinct summary of ESL papers. The manuscript processing time has hovered around the 4–5 week range; the short time to decisions is a critical metric for the letters format journal. I would like to thank the distinguished and enthusiastic editorial board for their tireless efforts in making this happen.

ESL submissions have increased handsomely from 152 in 2019 to 283 in 2022; we are already exceeding this count by the month of October itself in 2023. We have already received 17 special issue proposals in 2023 alone. ESL now has tie-ups with annual conferences and workshops.

- 1) *Embedded Systems Week (ESWeek)*: Papers accepted in the Late Breaking category are now published in ESL.
- 2) *Argentine Conference on Embedded Systems (CASE)*: Accepted papers are submitted to ESL for review and publication in an annual special issue (Latest Advances in Embedded Systems Research in Latin America).

This connection has contributed to a significant increase in ESL submissions from the Latin America region.

- 3) *Secure Hardware, Architectures, and Operating Systems (SeHAS)*: Authors of ESL publications in the security topic will be invited to present their papers at the workshop.

We hope to increase our connections to appropriate embedded systems conferences. Let us know if you have ideas on reaching out to the embedded systems community. With the increasing submission and publication counts, it may be time to consider moving ESL from the current quarterly publication cycle to a monthly cycle.

With this issue, we would be handing over the responsibility for ESL to a new editorial board led by a new Editor-in-Chief. I would like to thank the current editorial board for their selfless service over the years. The journal is a strong publication forum for embedded systems researchers and engineers, and will, no doubt, grow further as the field continues to evolve. It has been an exciting four years at ESL, and I would like to thank the researcher community for supporting the ESL journey.

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Preeti Ranjan Panda received the B.Tech. degree in computer science and engineering from the Indian Institute of Technology Madras, Chennai, India, in 1990, and the M.S. and Ph.D. degrees in information and computer science from the University of California at Irvine, Irvine, CA, USA, in 1995 and 1998, respectively.

He is currently a Professor with the Department of Computer Science and Engineering and serves as the Dean of Corporate Relations with the Indian Institute of Technology Delhi, New Delhi, India. He has previously worked with Texas Instruments, Bengaluru, India, and the Advanced Technology Group, Synopsys Inc., Mountain View, CA, USA, and has been a Visiting Scholar with Stanford University, Stanford, CA, USA. He is the author of two books: *Memory Issues in Embedded Systems-on-Chip: Optimizations and Exploration* (Kluwer Academic Publishers) and *Power-Efficient System Design* (Springer). His research interests are embedded systems design, CAD/VLSI, post-silicon debug/validation, system specification and synthesis, memory architectures and optimizations, hardware/software codesign, and low-power design.

Prof. Panda is a recipient of the IBM Faculty Award, the IESA Techno Mentor Award, and the Department of Science and Technology Young Scientist Award. Research works authored by him and his students have received several honors, including the Best Paper Nominations and Honorable Mention Awards at CODES+ISSS, DATE, ASPDAC, and VLSI Design Conference; and Most downloaded paper of *ACM Transactions on Design Automation of Electronic Systems*. He has served on the editorial boards of IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS, *ACM Transactions on Design Automation of Electronic Systems*, IEEE TRANSACTIONS ON MULTI-SCALE COMPUTING SYSTEMS, and *International Journal of Parallel Programming*, the General Co-Chair for *VLSI Design*, and the Technical Program Co-Chair for the International Conference on Hardware/Software Codesign and System Synthesis (CODES+ISSS), International Conference on Compilers, Architectures, and Synthesis of Embedded Systems (CASES), and International Conference on VLSI Design and Embedded Systems (VLSI Design). He has also served on the steering committees of CODES+ISSS, CASES, ASPDAC, and VLSI Design, and on the technical program committees of several conferences in the areas of embedded systems and design automation, including DAC, ICCAD, DATE, ASPDAC, and EMSOFT.