

IEEE COMPUTER SOCIETY ANNOUNCES 2020 FELLOWS

The IEEE Computer Society (CS) elevated 46 CS and 12 IEEE Members to the IEEE Fellow grade in 2020. The grade of Fellow recognizes extraordinary contributions to IEEE-designated fields. Congratulations to the following CS members and associates who received Fellow status in 2020:

- › Hussein Abbass—University of New South Wales–Canberra
- › Hari Balakrishnan—Massachusetts Institute of Technology
- › Vaughn Betz—University of Toronto
- › Wei Chen—Microsoft Research Asia
- › Yingying Chen—Rutgers University–Wireless Information Network Laboratory
- › Baoquan Chen—Peking University
- › Lei Chen—Hong Kong University of Science and Technology
- › Sangyeun Cho—Samsung Electronics Company
- › Christopher Clifton—Purdue University
- › Carolina Cruz-Neira—University of Arkansas
- › Gautam Das—University of Texas at Arlington
- › Jack Davidson—University of Virginia
- › Shlomi Dolev—Ben-Gurion University
- › Touradj Ebrahimi—Ecole polytechnique fédérale de Lausanne
- › Rudolf Eigenmann—University of Delaware
- › Ian Foster—Argonne National Laboratory
- › Edward Frank—Brilliant Lime Inc.
- › Richard Fujimoto—Georgia Institute of Technology

Digital Object Identifier 10.1109/MC.2020.2965636
Date of current version: 12 March 2020

IN MEMORIAM: DAN DOBBERPUHL

Marilyn Wolf



Dan Dobberpuhl, a highly influential microprocessor designer, passed away on 26 October 2019. His work spanned the metal-oxide semiconductor microprocessor era from the LSI-II to modern system-on-chip designs; it also spanned the design space from high speed to low power.

Dobberpuhl's designs were both commercially significant and technically innovative. The Digital Equipment Corporation (DEC) Alpha, for instance, tackled clock distribution challenges by combining a mesh distribution network with an extremely wide driver. Alpha

helped demonstrate the effectiveness of careful circuit design in the development of high-performance microprocessors. Dobberpuhl also directed the design of StrongARM, a low-power microprocessor, while at DEC.

In 1985, Dobberpuhl and coauthor Lance Gasser published a textbook on circuit design, *The Design and Analysis of VLSI [Very Large-Scale Integration] Circuits*. In 1998, he helped form SiByte, which designed a 64-bit, million-instructions-per-second system on chip and was eventually bought by Broadcom. Dobberpuhl next founded P.A. Semi to design PWRficient Power ISA microprocessors; that company was purchased by Apple. He was later associated with Agnilitix and Movidius.

Dobberpuhl received his bachelor of science degree in electrical engineering from the University of Illinois at Urbana–Champaign in 1967; the university later gave him two distinguished alumni awards. He was a member of the National Academy of Engineering and received the IEEE Donald O. Pederson Award in Solid-State Circuits.

Digital Object Identifier 10.1109/MC.2020.2976226
Date of current version: 12 March 2020

- › Johannes Gehrke—Microsoft
- › Kristen Grauman—University of Texas at Austin
- › Song Guo—Hong Kong Polytechnic University
- › Mohammad Hajiaghayi—University of Maryland College Park
- › Mary Hall—University of Utah
- › Aaron Hertzmann—Adobe Systems Inc.
- › Alan Hevner—University of South Florida
- › Markus Hofmann—Bell Labs Research
- › Zhenjiang Hu—National Institute of Informatics
- › Weijia Jia—University of Macau
- › William Kaiser—University of California, Los Angeles
- › Ramesh Karri—New York University
- › Sven Koenig—University of Southern California
- › Yun Li—University of Strathclyde
- › Mo Li—Nanyang Technological University
- › Richard Lippmann—Massachusetts Institute of Technology–Lincoln Laboratory