



TTTC News

The TTTC website has been updated to include several more features and information for its visitors! To find out more, please visit the website at <http://www.ieee-tttc.org/>

PAST TTTC EVENTS

The 2015 Asian Test Symposium and Test Week (ATS 2015)

22–25 November 2015

Indian Institute of Technology, Mumbai, India

<https://www.ee.iitb.ac.in/ats15/>

ATS 2015 is the 24th in this series of symposia started in 1992 devoted to testing, fault-tolerant computing, and the design of reliable circuits and systems. ATS is recognized as the main event in Asia that covers the many dimensions of testing and fault tolerance. The symposium focuses on the key test challenge that will arise due to the ability to design complex systems such as robots that encompass sensors, communication systems, processors, transducers and enabling software. In addition to passing postmanufacture test procedures, such systems and relevant devices must exhibit fault-tolerance and survivability characteristics.

UPCOMING TTTC EVENTS

The 19th Design Automation & Test in Europe Conference and Exhibition (DATE 2016)

14–18 March 2016

ICC, Dresden, Germany

<http://www.date-conference.com>

DATE is the major international event for design and engineering of systems-on-chip, systems-on-board, and embedded systems software and states a unique networking opportunity, bringing together designers and design automation users, researchers and vendors, as well as specialists in hardware and

software design, test and manufacturing of electronic circuits and systems.

DATE 2016 will take place 14–18 March 2016, at the International Congress Center, Dresden, Germany. It is located at the banks of the river Elbe at walking distance from the city center and provides state-of-the-art conference facilities. Dresden is a modern and vibrant city with short distances and a cutting-edge technology and economy sector. It is a leading semiconductor site in Europe, concentrating expertise on research, development, and manufacturing of leading-edge technologies which drive semiconductor industry to the top. Major players are, e.g., GLOBALFOUNDRIES, Infineon, and the Technical University, which is part of the German Excellence Initiative. Silicon Saxony is a registered industry association of 300 companies in the microelectronics and related sectors in Saxony, Germany, with around 20 000 employees. It therefore states an excellent venue for DATE 2016.

The 2016 IEEE VLSI Test Symposium

25–27 April 2016

Las Vegas, NV, USA

http://tttc-vts.org/public_html/new/2016/

The IEEE VLSI Test Symposium (VTS) explores emerging trends and novel concepts in testing, debug, and repair of microelectronic circuits and systems. The VTS Program Committee invites original, unpublished paper submissions for VTS 2016. Paper submissions should be complete manuscripts, up to six pages (inclusive of figures, tables, and bibliography) in a standard IEEE two-column format; papers exceeding the page limit will be returned without review. Authors should clearly explain the significance of the work, highlight novel features, and describe its current status. On the title page, please include: author name(s) and affiliation(s), and the mailing address, phone number, and e-mail address of the contact author. A 50-word abstract and five keywords identifying the topic area are also required.

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NEWSLETTER EDITOR'S INVITATION

I would appreciate input and suggestions about the newsletter from the test community. Please forward your ideas, contributions, and information on awards, conferences, and workshops to Theocharis (Theo) Theocharides, Department of Electrical and Computer Engineering, University of Cyprus, 75 Kallipoleos Avenue, PO Box 20537, Nicosia, 1678, Cyprus; ttheocharides@ucy.ac.cy.

Theo Theocharides
Editor, TTTC Newsletter

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CONTRIBUTIONS TO THIS NEWSLETTER: Send contributions to Theo Theocharides, Department of Electrical and Computer Engineering, University of Cyprus, 75 Kallipoleos Avenue, PO Box 20537, Nicosia 1678, Cyprus; ttheocharides@ucy.ac.cy, <http://tab.computer.org/tttc>. For more information, see the TTTC Web page: <http://tab.computer.org/tttc>.