From the EIC

Open-Source Electronic Design Automation (EDA) Tools

OPEN-SOURCE EDA HAS become a major endeavor in the EDA community as it promises a variety of advances, especially with respect to common infrastructures such as internal representations and databases, as well as interoperability of tools. Its success is also due to eco-systems such as RISCV, Chips Alliance, and Free Silicon Foundation. It is, therefore, the right time to be covered by *IEEE Design&Test* through our guest editors Sherief Reda, Pierre-Emmanuel Gaillardon, and Leon Stok. The special issue is structured into four articles covering entire design flows as well as further four articles focusing on specific tools. Thanks to the guest editors for this special issue.

In our General Interest section, we have two articles. The article titled "Design of ⎓ Shape Stub-Based Negative Group Delay Circuit" by Wan et al. presents a type of negative group delay (NGD) circuit based on transmission line resonators.

The second General Interest article titled "Design of Single-Bit Fault-Tolerant Reversible Circuits" by Gaur et al. introduces a generalized architecture for designing fault-tolerant reversible circuits.

ACM/IEEE MLCAD is a new workshop on Machine Learning for CAD. The report covers the first edition from 2019 held in Banff and the second edition from 2020 held in a virtual form. Thanks to the general chairs for the report.

The ACM/IEEE 39th International Conference on Computer-Aided Design (ICCAD 2020) took place as a virtual event. Thanks to Yuan Xie, the General Chair, for his conference report.

And thanks to Massimo Poncino, our Conference Reports editor, for acquiring the reports.

As always, last but not least, thanks to Scott Davidson for The Last Byte titled "The Road to Open-Source EDA."

Enjoy reading! ■

Jörg Henkel
Editor-in-Chief
IEEE Design&Test

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