

From the EIC



Special Issue on 2021 Top Picks in Hardware and Embedded Security

■ **THE ARTICLES IN** this issue are divided into two groups: 1) the first group belongs to the Special Issue on 2021 Top Picks in Hardware and Embedded Security and 2) the second group consists of the general interest articles.

The highlight of this issue is the Special Issue on 2021 Top Picks in Hardware and Embedded Security. Hardware security is a growing research field. This special issue principally focuses on articles spanning across different areas of hardware and embedded security, ranging from side-channel attacks and fault attacks to malware detection across different abstraction layers of an embedded system. The special issue consists of six research articles and a guest editorial. We thank the guest editors, Srinu Devadas and Jeyavijayan “JV” Rajendran, for making this special issue possible.

In addition, we have, in this issue, four general interest articles titled as follows: 1) “On-Chip Jitter Learning for PLL;” 2) “On the Implementation of Fixed-Point Exponential Function for Machine Learning and Signal-Processing Accelerators;” 3) “Improving DNN Hardware Accuracy by In-Memory Computing Noise Injection;” and 4) “Graph-Based Circuit Simulator for Switched Capacitor Circuits.”

Many thanks to Cristiana Bolchini and Ingrid Verbauwhede for their thorough report about the DATE 2022 conference, and to Scott Davidson for The Last Byte titled “Security Arms Race.”

I hope you enjoy reading this issue of *IEEE Design&Test*.



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Editor-in-Chief
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