

HiPEAC Compilation Architecture

Koen De Bosschere¹ and Rainer Leupers²

¹ Ghent University, Belgium

² Aachen Technical University, Germany

Abstract— HiPEAC is a European network of computing experts founded in 2004. It now has around 2000 members, making it the largest such network in the world. The network organizes four major events for its members per year, it encourages job and researcher mobility between members, it drafts a bi-annual vision on the future challenges of computing, and it promotes the activities of its members in the media. HiPEAC wants to actively expand its membership base in the European new member countries.

During the special session, executives of the network will present the HiPEAC network, and the TETRAMAX innovation action, and how members from Eastern and Southern Europe can benefit from it..

CURRICULUM VITAE



Koen De Bosschere is professor of Computing Systems at Ghent University in Belgium, and leader of the Computer Systems Lab. He is the editor-in-chief of ACM Transactions on Architecture and Code Optimization (TACO), and coordinator of HiPEAC, the premier network of computing systems researchers in Europe. He is co-editor of the bi-annual HiPEAC vision document and organizer of the yearly ACACES summer school. At Ghent University, he manages the bachelor and master program in computer engineering, and the university-wide student-entrepreneurship project for which he received the Hermes Award in 2012. He co-authored 100 publications

over the last 10 years. His research interests include computer architecture, system software, code optimization, and software security. He earned B.S. and Ph.D. degrees in Electronics Engineering and Computer Science at Ghent University.



Rainer Leupers received the M.Sc. (Dipl.-Inform.) and Ph.D. (Dr. rer. nat.) degrees in Computer Science with honors from TU Dortmund in 1992 and 1997. From 1997-2001 he was the chief engineer at the Embedded Systems chair at TU Dortmund. In 2002, he joined RWTH Aachen University as a professor for Software for Systems on Silicon. His research comprises software development tools, processor architectures, and system-level electronic design automation, with focus on application-specific multicore systems. He published numerous books and technical papers and served in committees of the leading international EDA conferences. He received various scientific awards, including Best Paper Awards at DAC and twice at DATE, as well as several industrial awards. Dr. Leupers is also engaged as an entrepreneur and in turning novel technologies into innovations. He holds several patents on system-on-chip design technologies and has been a co-founder of LISATek (now with Synopsys), Silexica, and Secure Elements. He has served as consultant for various companies, as an expert for the European Commission, and in the management boards of large-scale projects like HiPEAC and UMIC. He is the coordinator of EU projects TETRACOM and TETRAMAX on academia/industry technology transfer.