SMART4ALL - Technological Challenges and Funding Opportunities in the Areas of Balkans and Eastern Europe

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Abstract— **SMART4ALL is an extensive network of Digital Innovation Hubs aiming at boosting digital technology uptake and corresponding business development across South, Eastern and Central Europe. It builds capacity via the development of self-sustained, cross-border pathfinder application experiments that transfer knowledge and technology between academia and industry. SMART4ALL offers funding of 2,2 Mio Euros via 9 open calls and novel coaching services from world lead experts in ethics, technology, funding and business development. The main domains targeted are digitized environment, digitized agriculture, digitized transport and digitized anything. Through Marketplace-as-a-Service, a one-stop-smart-shop for startups, SMEs and Slightly Bigger Companies, SMART4ALL will revolutionize entrepreneurship and leverage market penetration for startups, spin offs and spin outs. The keynote focuses on the technological challenges addressed by SMART4ALL project and the funding opportunities it offers.**

All regional and national organizations that are interested in following up Open Calls for funded experiments

Keywords- Smart anything everywhere, application experiments, open calls funding

References:

[1] https://smart4all-project.eu.

About the Speaker

Prof. Nikolaos S. Voros*, received his Diploma in Computer and Informatics Engineering, in 1996, and his PhD degree, in 2001, from University of Patras, Greece. His research interests fall in the area of embedded system design, robotics, IoT and cyberphysical system design. Dr Voros is Associate Professor at University of Peloponnese, Department of Electrical and Computer Engineering where he is leading the Embedded Systems Lab. During the last years, Dr Voros has participated in more than 15 research projects funded by European Commission either as project manager or as scientific advisor. He has served as scientific coordinator of FP7 STREP Project 287733 ALMA, as ICT coordinator for FP7 STREP Project 287720 ARMOR, as scientific coordinator of Horizon 2020 Project ARGO and as technical coordinator of Horizon 2020 Project RADIO. Currently, he is the coordinator of SMART4ALL, a four year project with budget of 8,6 Mio for interconnecting DIHs across Europe. Dr Voros has published significant number of books and referred articles in international journals and conferences, while he is also editor of the books «System Level Design with Reuse of System IP», «System Level Design of Reconfigurable Systems-on-Chip» and «Components and Services for IoT platforms: paving the way for IoT standards» published by Springer. He was the General Chair of the IEEE Computer Society Annual Symposium on VLSI, which took place in Kefalonia, Greece on July 2010, while he also served as Program Co-Chair for 24th International Conference on Field Programmable Logic and Applications (FPL 2014), which took place at Technical University of Munich on September 2nd – 4th, 2014.*

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