

From the Editor-in-Chief

Emerging Hot Chips and Systems

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WELCOME TO THE March/April 2019 issue of *IEEE Micro*. This issue features selected papers from the *30th Hot Chips Symposium*, held in Cupertino, CA, USA in August 2018. The *Hot Chips Symposium* has resulted in enjoyable Special Issues for *IEEE Micro* for more than a decade, and the tradition goes on! John Kubiakowicz of the University of California, Berkeley and Stefan Rusu of TSMC served as Guest Editors on this issue. They have compiled an excellent selection of papers on emerging chips and systems from the Symposium. Please read the Guest Editors' Introduction to get a preview of the six papers, which include two papers on security, two on high-end chips from Intel and Samsung, a paper on carbon nanotube memories, and one on the agile design process of the Berkeley BROOM processor. Thanks to the editors, authors, and reviewers who worked hard to put this issue together. Special thanks to Lieven Eeckhout, who coordinated the reviews for the paper submissions to this special issue.

In addition to the six Hot Chips papers, this issue also features a Micro Economics column by

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Shane Greenstein, with an article titled "Where the Frontier Thrives: Bricks, Mix, and Zip," discussing the frontier locations for today's information technology. Greenstein describes the "symptoms" of cities that nurture entrepreneurship and high aspiration. He also notes the similarities (or lack thereof) with the frontier cities at various points during history, such as Pittsburgh for the steel industry and Detroit for the auto industry.

Let me also provide an overview of what to expect in upcoming issues. The May/June issue will be the popular "Top Picks" Special Issue. Sandhya Dwarakadas and a selection committee from industry and academia have selected 12 papers from the 122 papers that were submitted in response to the Top Picks call for papers. Readers can look forward to an amazing collection of excellent papers in May/June.

Two thematic special issues are also planned for 2019. The July/August issue will be a Special Issue on Secure Architectures. Simha Sethumadhavan of Columbia University and Mohit Tiwari of the University of Texas at Austin will be guest-editing this special issue. There is a lot of buzz around machine learning, and *IEEE Micro* cannot let this year pass through without

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an issue on the topic. As such, the September/October issue will be on machine learning acceleration. Hadi Esmaeilzadeh of the University of California, San Diego and Jongse Park of Georgia Tech will serve as guest editors on this issue.

We invite readers to submit to these special issues. Readers can find the calls for papers at <https://publications.computer.org/micro/category/calls-for-papers/>

The last issue of 2019 will likely be a regular issue with various submissions to the

magazine. *IEEE Micro* is interested in submissions on any aspect of chip/system design or architecture.

Hope you enjoy the papers presented in this issue. Happy reading!

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