Evaluating the Q-score of Quantum Annealers

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Abstract—We report the Atos Q-score for D-Wave's quantum devices, classical algorithms and hybrid quantum-classical solver. Computing the Q-score entails solving the Max-Cut problem for increasingly large graphs. This work presents the first computation of the Q-score on a quantum device and shows how these quantum devices compare to classical devices at solving optimisation problems. We use D-Wave's standard methods out of the box with a time limit of 60 seconds. The Q-score for D-Wave's 2000Q and Advantage devices are 70 and 140, respectively. The Q-score for two of D-Wave's classical algorithms, based on tabu search and simulated annealing respectively, are 2,300 and 5,800. Finally, we report the out-of-the-box hybrid approach to have a Q-score of 12,500.

Index Terms—Quantum computing, application-oriented benchmarks, quantum annealing, Q-score, D-Wave, Max-Cut.

I. Introduction

We live in interesting times. General purpose quantum computers, once a theoretical concept, are getting closer to outperforming classical computers on certain tasks. Many different parties are trying to build general-purpose quantum computers, such as IBM [1] and Atos [2]. At the same time quantum annealing devices promise utility in the shorter term in the domain of optimisation problems. Hybrid systems consisting of classical computing and quantum annealing are already being used to solve real world problems today [3], [4], [5], [6].

Interest in these devices from both investors and end users is growing rapidly. To help these parties compare the performance of different quantum devices and accelerate progress. we require performance benchmarks. Such metrics show the absolute and relative capabilities of a certain device with respect to some method chosen by the benchmark. A good benchmark is i) useful: it tells interested parties what they need to know, ii) hardware-agnostic: it does not favour a certain hardware implementation over another, iii) scalable: it can adapt to developments in quantum devices, and iv) comprehensive: it measures the quantum set-up as a whole, including the classical parts¹ [7]. In addition, it is useful for a benchmark to have an easy representation, to make it understandable and allow easy comparison of the performance of different devices. An example would be to have a benchmark given by a single number.

¹We note here that benchmarks that do not have properties (iii) and (iv) can still be useful in the short term.

Originally, the number of qubits was the leading benchmark for quantum computing. However, it turns out that this metric does not tell the whole story, as it is neither useful, nor hardware-agnostic, nor comprehensive. This can for example be seen by comparing the number of qubits of D-Wave's latest quantum annealer (5640) [8] and IBM's largest gate-based quantum computer (127) [9]. Since then, different parties have come up with new metrics, but these new metrics are not always adapted by other vendors. Different metrics focus on different aspects of quantum computers, which can favor one technology over another. Therefore, creating a universal metric is a challenging task. Examples of metrics that gained some traction are the Quantum Volume [10], Circuit Layer Operations Per Second (CLOPS) [11] and randomised benchmarking [12]. Although such component-level benchmarks are important, they lack some comprehensibility, which becomes more important once we get closer to the wide applicability of quantum computers.

Recently, application-oriented benchmarks were introduced [13], [14]. These metrics estimate the sizes of actual problems that can be solved with a quantum device. By focusing on applications, these metrics are naturally useful, hardware-agnostic and comprehensive. But, depending on the problem, they are not necessarily easily computable or scalable. By devising application-oriented benchmarks in a presentable and scalable way, such metrics promise to clearly measure the realistic performance of a quantum computer. The Q-score by Atos [14] and the metric by the QED consortium [13] are examples of application-oriented metrics. A more comprehensive discussion on recent metrics can be found in the table from the Boston Consultancy Group (BCG) white paper [7].

In this work, we will focus on the Q-score. The main advantage of the Q-score is that it predicts the efficacy of quantum devices in a real-world application. Furthermore, the Q-score can be computed for any computational device and hence allows a direct comparison between quantum and classical computers. The Q-score tests the entire computation stack of a computational setup, which is often not limited to the quantum device. On the one hand, this ensures that this metric measures the performance of a quantum device in practice, as quantum devices are always part of a larger computational workflow. However, this also makes it harder to compare the quantum

aspect of various devices. Although the Q-score is proposed for a useful and representative optimisation problem, it does not say much about the performance on the variety of other interesting problems that are relevant for quantum computers.

Atos proposes to approximately solve the Max-Cut problem for large graphs as a representative application for quantum devices [14]. They define the Q-score of a computational approach as the size of the largest graph for which the setup can approximately solve the Max-Cut problem with a solution that significantly outperforms a random guessing algorithm. For general purpose quantum computers, they propose to use the Quantum Approximate Optimisation Algorithm (QAOA) [15], as they view it as representative of practical needs of industry and challenging for current hardware. As the Max-Cut problem can be formulated as a Quadratic Unconstrained Binary Optimisation problem (QUBO), it can be solved on a quantum annealer as well. Due to the relatively small size of gate-based quantum computers, we expect a fairly small Qscore for these devices. Quantum annealers are already quite large, which raises the following questions:

- 1) What Q-score does a state-of-the-art quantum annealer achieve?
- 2) How do these Q-scores compare to classical algorithms for the Max-Cut problem?
- 3) What Q-score does a hybrid combination of a quantum annealer and a classical algorithm achieve?

In this paper, we give an answer to these three questions by reporting the Q-score of the various approaches by D-Wave. Since Atos, at the time of writing, has not yet published any Q-scores of their own, the results reported in this paper give a first indication of what kind of Q-scores we can expect for quantum devices. This will then give an indication of how quantum devices perform on optimisation problems compared to classical devices. In addition, it will give an indication on when quantum computers could outperform classical computers on solving optimisation problems, potentially even achieving quantum advantage. Recall that quantum advantage is defined as demonstrating a problem solvable by a quantum computer for which no classical device can find a solution in a feasible amount of time.

We investigate the D-Wave Advantage system, D-Wave's latest and largest device as well as their previous generation device, the D-Wave 2000Q [8]. In addition, we report the Q-score of their out-of-the-box hybrid approach [16] and of their implementations of the classical algorithms tabu search and simulated annealing. We start our work with a background on the Q-score and quantum annealing in Section II, after which we present our results of the Q-score in Section III. We finish our work with a discussion and directions for further research in Section IV.

II. BACKGROUND

A. Atos Q-score

In 2021, Atos introduced the Q-score [14]. They set out to define a metric that is simultaneously application-centric,

hardware-agnostic and scalable. Note that with these three characteristics, the Q-score inherently has the four desired properties for a quantum metric mentioned above. Furthermore, the Q-score is given by a single number, where a higher Q-score implies that a device has a higher performance. This makes the Q-score accessible to the public as well.

They propose approximately solving the Max-Cut problem for large graph sizes as a representative application of a quantum device. Given a graph G of size N, i.e. with N vertices, a cut P is a partitioning of the nodes in two sets. The cost of a cut is the total number of edges between these two sets. The Max-Cut problem entails finding a cut that maximises this cost. They consider $(N, \frac{1}{2})$ -Erdös-Rényi graphs, which are graphs of size N where each edge is added with probability $\frac{1}{2}$. By trying to find a maximal cut for multiple graphs of size N on a certain device, we can compute the average best cut C(N) for graphs of size N obtained by this device. We define the Q-score to be the highest N for which a setup can find a solution that is on average significantly better than a random cut. Specifically, the largest N for which

$$\beta(N) = \frac{C(N) - \frac{N^2}{8}}{C_{\text{max}}(N) - \frac{N^2}{8}},\tag{1}$$

is larger than $\beta^*=0.2$ is the Q-score². Here $\frac{N^2}{8}$ is the expected cost of a random cut, and $C_{\max}(N)$ is the cost of the optimal cut for a graph of size N. We can see that on average $\beta(N)=1$ for a setup that achieves the optimal cut and $\beta(N)=0$ for a setup that produces a random cut. It hence makes sense to find the maximum value of N for which $\beta(N)$ is relatively high. To make this computation scalable, Atos proposes the approximation $C_{\max}=\frac{N^2}{8}+0.178N^{\frac{3}{2}},$ where the 0.178 comes from a numerical fit of the optimal classical solution to Max-Cut in the range $N\in[5,40].$ Note that this might induce errors, especially at low graph sizes N.

Atos originally proposed to solve the Max-Cut problem for universal quantum devices with the Quantum Approximate Optimisation Algorithm (QAOA) [15], as they view it as representative of practical needs of industry and challenging for current hardware. However, due to the hardware agnosticism and the scalability, the Q-score can be computed for any computing device and by with other methods.

To compute the Q-score, one generates M Max-Cut problem instances of size N at random and then computes the average cost C(N) over these M problems for the algorithm in question. The constant M should be chosen large enough to mitigate the effect of stochasticity.

Q-score constraints: The Q-score leaves some practical degrees of freedom, such as the computing time, optimisation methods, number of layers p in QAOA, etc. Here, we explicitly state our constraints for computing the Q-score. Note that other choices for these constraints are possible and will likely lead

 $^{^2}$ Atos acknowledges that the constant $\beta^*=0.2$ is chosen somewhat arbitrarily but they have picked it so that a noiseless quantum simulator running the QAOA algorithm with depth 1 achieves an infinite Q-score for this cut-off.

to different Q-scores. Therefore, it is important to report the Q-score together with these constraints, most notably the time limit.

Firstly, we fix the maximum computation time at 60 seconds, which is chosen so that both quantum and classical devices are expected to solve the Max-Cut problem for at least some problem sizes N. This time includes the entire computation starting from either a QUBO or a graph representation of the problem (see Section II-B for a definition of QUBOs). Note that it thus does not include the time to generate the problem instances. It does however include the embedding time, i.e. the time to translate the given QUBO to a hardware specific formulation. This is in the spirit of the Q-score as it is meant to benchmark the entire computation stack.

Secondly, we set the number of random graphs instances M for each graph size to 100. In addition, the value of $\beta^* = 0.2$ was chosen by Atos. Therefore we do not just report the Q-score of the different devices, but also the β vs N graphs.

When the algorithm does not produce a meaningful result (i.e. a cut), we assign it $\beta=0$, the value that is obtained by randomly guessing a solution. This can happen for a variety ovf reasons, such as a quantum device having insufficient resources to solve problems of size N, or a quantum device not finding a way to translate the graph into the required problem formulation within the time limit. When an approach does not admit a time limit, its Q-score will be the largest N for which it can on average find an exact solution within 60 seconds.

We consider out-of-the-box solvers as a reproducible and first indication of the Q-score. In practice, solvers can benefit from a problem-specific optimisation to obtain higher Q-scores, e.g. optimal time distribution among algorithm steps. We chose to avoid such optimisations to allow the most neutral and fair comparison of the different approaches. This means that we restrict ourselves to supplying M=100 random graphs of size N, limiting the solving time to 60 seconds per graph and running the solvers with default parameters.

B. Quantum Annealing and D-Wave

There are multiple flavours of quantum computer of which two are currently most researched, namely gate-based devices and quantum annealers. In this work we will only consider quantum annealers. Quantum annealers are devices that can solve optimisation problems that are submitted in the form of a Quadratic Unconstrained Binary Optimisation problem (QUBO). A QUBO is an expression that is a sum of terms where each term is either constant, linear or quadratic in the binary variables x_i . When solving a OUBO, the aim is to find binary variable assignments that minimise the QUBO. Many optimisation problems can be formulated as a OUBO, so that the minimal value of the QUBO translates to the optimal solution of the optimisation problem [17]. The Max-Cut formulation is shown to be equivalent to the QUBO formulation in section 3.2 of [17]. Together, this implies that the Max-Cut problem can be seen as a representative problem instance for the class of optimisation problems.

Quantum annealers approximately solve QUBOs by using principles from quantum mechanics. A quantum annealer is a quantum device with some number of qubits, which are connected via a certain topology. Given a QUBO, the quantum annealer identifies each variable with a number of qubits, so that terms in the QUBO correspond to connections on the quantum annealer. This identification of QUBO with qubits is called an embedding. Quantum annealers prepare qubits in an initial uniform superposition and then let them evolve under a problem-specific Hamiltonian. If this evolution is slow enough, the qubits are likely to end up in the ground-state or other low energy-states of this Hamiltonian. When encoded correctly, the ground-state corresponds to the minimum of the QUBO and the other low energy-states correspond to values close to this minimum. By measuring the qubits after the evolution, we find values that approximately minimise the QUBO.

D-Wave Devices and Solvers: Currently, D-Wave offers three types of solvers: quantum annealers, classical solvers, and hybrid solvers [18], [19], [20]. These solvers are all constructed to solve QUBOs. At the time of writing, their biggest quantum devices are the Advantage device, which has 5640 qubits, where each non-boundary qubit is connected to 15 other qubits and the 2000Q device, which has 2048 qubits, where each non-boundary qubit is connected to 6 other qubits. Both devices have a certain topology on their qubits, that describes how the qubits are connected. The topologies of the D-Wave devices are designed to maximise the amount of different QUBOs that can be solved on these devices.

In addition to these quantum devices, we wish to compute the Q-score of some classical algorithms to compare them to the performance of the D-Wave. There is a wide variety of classical algorithms that solve the Max-Cut problem (see for example the overview in [21]), however most of these algorithms do not mention the time performance and even fewer have public source code for their implementation. As an exception, the tabu search implementation in [22] does mention its time performance. However, this implementation requires time in the magnitude of hours to solve problems of size larger than 5,000. There are some companies that offer classical solvers out of the box that can be used for the Max-Cut problem, such as D-Wave, LocalSolver, CPLEX and Gurobi. Two examples of classical solvers from D-Wave are Obsolv and simulated annealing. Obsolv starts by splitting the QUBO into smaller pieces and then solves the indivdual pieces with a tabu search algorithm. Simulated annealing is a local search algorithm that looks for minima by having a slow decrease in the probability of accepting worse solutions as the solution space is explored. Both of these methods appear in lists of best classical approaches to solve the Max-Cut problem [21]. Hence, we have chosen Obsolv and simulated annealing as classical algorithms to compare the D-Wave quantum devices to.

Last but not least, D-Wave offers a hybrid solver, which combines the strengths of both classical and quantum solvers to solve QUBOs [16]. The hybrid solver uses D-Wave's Advantage device for the quantum part, while it uses a

collection CPU and/or GPU platforms provided by Amazon Web Services (AWS) for the classical part. Because of this combination of strengths, the hybrid solver promises to have the best performance at solving QUBOs. As D-Wave's technical report shows, the hybrid solver outperforms many of the best classical algorithms at solving QUBOs, especially at larger problem sizes [16]. However, D-Wave does not provide information about the distribution of quantum and classical computing time and power. Because of this, this solver is more of a black box than the other solvers.

We have implemented this setup using python and used this code to obtain the results below. For transparency, we have made our code at https://github.com/TNO-Quantum/qscore_dwave. It consists of several scripts, allowing to solve random Max-Cut instances on any of the five solvers and compute the corresponding beta.

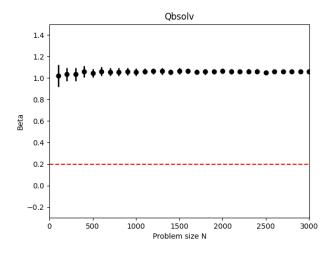
III. RESULTS

To compute the Q-score, we generate 100 random $(N, \frac{1}{2})$ -Erdös-Rényi graphs for increasing problem sizes N with the NetworkX python library. Next, we convert these graphs into QUBOs and (approximately) solve the Max-Cut problem for these QUBOs by finding binary variable assignments with low values for the QUBO. We do this by supplying these QUBOs to D-Wave's solvers provided by the D-Wave's open source library [23]. The time constraint of 60 seconds is implemented in different ways depending on the solver, each of which is explained separately below. To apply the solvers in an out-of-the-box fashion, we do not supply or change any other input variables. Having obtained results on all 100 graphs, we compute the average best cut found C(N) and compute $\beta(N)$ accordingly with the formula above. We increase N until we find a value of N for which $\beta(N) \leq 0.2$.

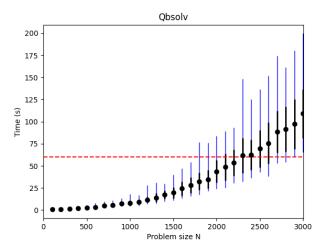
For the classical algorithms we use a modest server with an Intel Core i7-7600U CPU with a clock speed of 2.80GHz and 12 GBs of RAM.

A. Q-scores of D-Wave's classical solvers

Here we report the Q-score of two out-of-the-box classical solvers provided by D-Wave's open source library. We use tabu search as provided by the Qbsolv class [19] as well as simulated annealing as provided by D-Wave's neal package [20]. Fig. 1 and Fig. 2 show β and the running time plotted against the problem sizes N. The blue vertical lines indicate the minimum and maximum time recorded. The black lines represent the statistical standard deviation³. For both approaches we start at N=100 and increase N by 100 nodes each time. We terminate the computation when the average time for a graph size exceeds 100 seconds on our server. We see that these approaches yield good cuts with $\beta \approx 1$ in the entire range.



(a) Q-score's β vs problem size N for tabu search by Qbsolv.



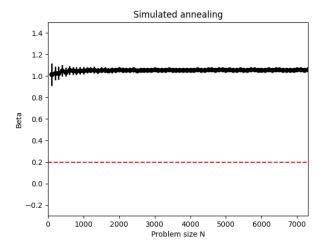
(b) Time vs problem size N for tabu search by Qbsolv.

Fig. 1: Tabu search by Qbsolv results.

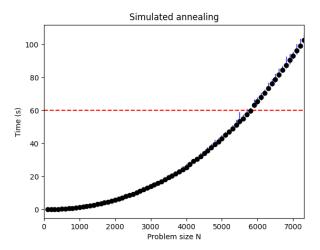
To impose the time constraint of 60 seconds, one should note that there is a degree of freedom is how much time is spent in inner and outer loops. This allows us to cut off the algorithm at 60 seconds in different ways. Although Qbsolv has a *timeout* parameter, it works by comparing the expired time with the *timeout* at the end of every outer loop. This results in problem instances where the total time taken exceeds 60 seconds, even for *timeout* parameters less than a second. Similarly, to terminate simulated annealing at 60 seconds inherently requires an optimisation. Rather than finding the ideal way to set this cut-off, we report the average time taken per graph instance and report the Q-scores to be the respective largest graph sizes for which the algorithms take less than 60 seconds on average. This allows us to read off the Q-score without having to do specific optimisations.

We see that simulated annealing outperforms Qbsolv, as it achieves a Q-score of 5,800 versus the 2,300 achieved by Qbsolv search within the 60 second time limit.

 $^{^3}$ Since random graph instances can have a different optimal cost, we expect systematic and statistical error. This effect will inflate the error bars for low N. However, it does not have a big effect on the estimation of the Q-score so we simply treat all error as statistical standard deviations.



(a) Q-score's β vs problem size N for simulated annealing.



(b) Time vs problem size N for simulated annealing.

Fig. 2: Simulated annealing results.

The results yield a β slightly higher than 1. This is due to the fact that the approximation of $C_{\rm max}$ in Eq. (1) is not exact. Comparing the approximation $C_{\rm max} \approx \frac{N^2}{8} + 0.178 N^{3/2}$ to the average best cut found by simulated annealing, we see that especially for low N the approximation is not perfect. See Fig. 3 for the ratio between the simulated annealing result and the approximation as a function of N at low N. In later results we will compute β in two ways, namely using for $C_{\rm max}$ both the approximation as well as the average best cut found by simulated annealing.

B. Q-scores of D-Wave hardware

Here, we report the Q-score of the D-Wave Advantage device, as well as the previous generation device, the 2000Q. The D-Wave interface allows us to set a maximum time of 60 seconds per problem instance. For both devices we start at N=5 and increment N in steps of five. We stop this process

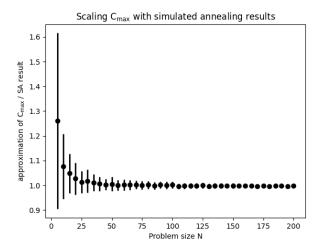


Fig. 3: Ratio of the approximation for $C_{\rm max} \approx \frac{N^2}{8} + 0.178 N^{3/2}$ and the mean cost of simulated annealing.

when β is clearly below 0.2 or no solution is found within the allowed time.

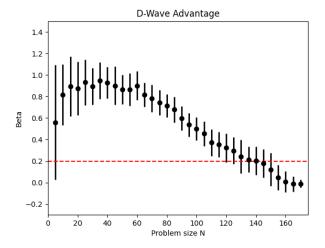
In Fig. 4 the β vs problem size N graph is plotted for the D-Wave Advantage and the D-Wave 2000Q in Fig.. For the D-Wave Advantage, $\beta=0.2$ is crossed at N=140, which means that we find a Q-score of 140.

The β vs N graph of the D-Wave 2000Q device only produces an approximate solution up to N=70. For larger graphs, the system is unable to find an embedding of the resulting QUBO onto the hardware. This inability to find an embedding is time independent, so our 60 second limit is not a factor. Following our definition we set $\beta=0$ for N>70 and hence report a Q-score of 70.

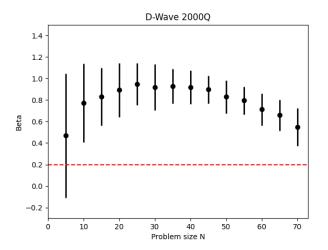
For both devices, we find $\beta < 1$ for low N. This is due to the approximation of the optimal cut, $\frac{N^2}{8} + 0.178N^{3/2}$, not being very accurate for low N as seen in Fig. 3. Replacing the approximation of $C_{\rm max}$ by the simulated annealing result, we find the expected result of $\beta \approx 1$ at low N, see Fig. 5.

C. Hybrid solver

D-Wave's hybrid binary quadratic model solver is used out of the box for Max-Cut problems. Just as on the D-Wave hardware, the D-Wave interface allows us to set a maximum time of 60 seconds per problem instance. We ran the hybrid solver from N=100 until N=5,000 in steps of 100 and found $\beta\approx 1$ in the entire range. In addition we ran the algorithm for N=12,500 and still found $\beta\approx 1$. At this point we were confronted with a hard-coded time limitation of the hybrid solver [24]. It requires a minimum time per problem instance given by a linear interpolation between some fixed points, see Fig. 6. Based on this plot we expect the effective Q-score to be about 12,500. However, due to this hard-coded time limitation we cannot see the result for larger graphs with our chosen time limit. It might very well be that we run into the same problem as with our classical algorithms, namely that



(a) Q-score's β vs problem size N for the D-Wave Advantage QPU.



(b) Q-score's β vs problem size N for the D-Wave 2000Q QPU.

Fig. 4: Q-score's β vs problem size N for the D-Wave quantum annealers with an approximated C_{\max} in Eq. (1).

TABLE I: Q-scores of D-Wave's QUBO solvers with a 60 seconds time limit.

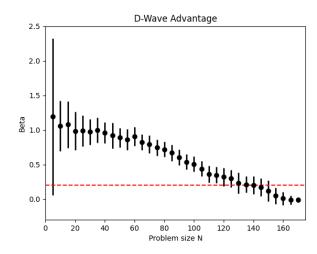
Approach	Q-score
Tabu search	2,300
Simulated annealing	5,800
D-Wave Advantage	140
D-Wave 2000Q	70
Hybrid solver	12,500

it will not complete a loop within the given time for larger graphs.

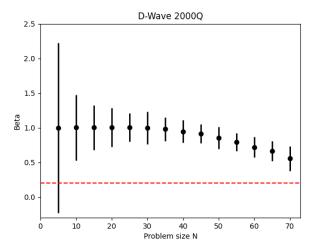
IV. DISCUSSION

We show an overview of the Q-scores of the various approaches in Table I.

If we compare the classical, fully quantum, and hybrid approaches, we see that the classical solvers significantly outperform the approaches with only quantum computations.



(a) Q-score's β vs problem size N for the D-Wave Advantage QPU with the simulated annealing result as C_{\max} .



(b) Q-score's β vs problem size N for the D-Wave 2000Q QPU with the simulated annealing result as $C_{\rm max}.$

Fig. 5: Q-score's β vs problem size N for the D-Wave quantum annealers with the simulated annealing result as $C_{\rm max}$ in Eq. (1).

In turn, the hybrid approach outperforms both given the fixed maximum time. From a business perspective it makes sense that the hybrid solver by D-Wave outperforms their free classical approaches. It would therefore be interesting to compare these Q-scores to classical approaches by other companies, such as LocalSolver, CPLEX and Gurobi. We tried to compute the Q-score using other classical algorithms, however found no improvement.

We should mention that this comparison between hybrid and classical solvers is somewhat skewed. We ran the classical solvers on a modest personal server. The hybrid solver however uses a collection of CPU and/or GPU platforms provided by Amazon Web Services for its classical computations. We expect these CPU and GPU platforms to outperform our

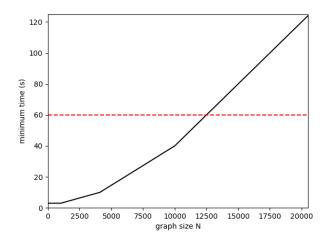


Fig. 6: Minimum time required to pass to the hybrid binary quadratic model solver.

modest server. For a better comparison, one should run both the hybrid solver and the classical solvers on the same classical hardware backend.

Another interesting direction for future research is lifting the hard-coded time limits for the hybrid solver, which seems to lead to higher Q-scores for this solver. This requires opening the black-box implementation, which we cannot do.

With these results we can to conclude that hybrid devices already outperform classical ones at solving a representative optimisation problem. The current difference is however not yet significant enough to speak of real quantum advantage on optimisation problems. Considering the focus of the Q-score on optimisation problems, it should be noted that this metric need not give any indication of the usefulness of a quantum device on other applications. Nonetheless, it will be exciting to see whether quantum developers can potentially extend this small advantage into a full quantum advantage in the future, especially considering that quantum computing is still in its infant years.

Reporting the Q-score together with a time limit worked well for D-Wave's Advantage device up to N=180 and the 2000Q device up to N=70. However, for the classical and hybrid approaches we found no relation between β and N in which β degraded over time. We expected this for exact solvers and it turns out to also be the case for out-of-the-box use of approximate algorithms. Although we can still report a Q-score based on the time spent, we believe the classical and hybrid results are less in line with the spirit of the Q-score since they do not find approximate solutions but only exact optimal solutions. To address this issue, one could instead consider classical solvers that do allow approximate solutions when time is limited. This would allow a comparison of quantum and classical devices which is more in line with the spirit of the O-score.

It is clear that the approximation used to compute β is not exact. We consistently find $\beta \approx 1.06$ instead of $\beta = 1$ for

large N. For low N the approximation is not needed, since optimal classical solutions can be found easily. We believe it is better not to use the approximation for N < 100, as at low N this introduces large uncertainties in the β parameter. If one uses a standardised set of graphs, rather than randomly generated graphs this error could be significantly reduced since the exact optimal cut would be known. However, since even with the approximation the β 's are still sufficiently higher than the boundary of $\beta*=0.2$, this does not affect the eventual Q-scores.

We reported $\beta=0$ in cases where no embedding on the device can be found. By setting $\beta=0$ we effectively benchmark the device with an additional step of generating a random cut when no embedding is found. One could argue this is not as out-of-the-box as other algorithms. However, since the Q-score aims to benchmark the entire setup, not just the quantum hardware, this step would be a trivial addition. A much harsher alternative would be to set the cost to zero when no embedding is found, though as these occurences are rare, the effect on the Q-score is limited.

As mentioned in the introduction, the Q-score can also be computed for gate-based devices. However, at the moment of writing, these devices are much smaller in size than the quantum annealers provided by D-Wave. That is why we expect a state-of-the-art gate-based device to have a signficantly lower Q-score than the 140 we found for the quantum annealers. It is interesting however to compute the Q-score for a gate-based device as well so that we can explicitly compare these two paradigms of quantum computing. Looking at the currently available topologies, gate-based quantum computers will not yet beat the Q-scores of the quantum annealers, let alone the Q-scores of classical and hybrid algorithms. A moment to look forward to is when gate-based devices reach similar performances as quantum annealers or classical algorithms for optimisation tasks.

In conclusion, classical devices currently outperform stateof-the-art quantum annealers on optimisation problems, while a hybrid approach combining the two seems to outperform classical approaches. These differences are however not yet significant enough to give strong hints towards quantum advantage.

REFERENCES

- [1] Y. Wang, Y. Li, Z.-q. Yin, and B. Zeng, "16-qubit IBM universal quantum computer can be fully entangled," *npj Quantum Information*, vol. 4, 2018.
- [2] P. Duluc, P.-A. Harraud, E. Munro, and J. Keller, "How quantum technology will revolutionize healthcare," Atos Ascent Magazine, 2019.
- [3] F. Neukart, G. Compostella, C. Seidel, D. von Dollen, S. Yarkoni, and B. Parney, "Traffic flow optimization using a quantum annealer," Frontiers in ICT, vol. 4, 2017. [Online]. Available: https://www.frontiersin.org/article/10.3389/fict.2017.00029
- [4] S. Feld, C. Roch, T. Gabor, C. Seidel, F. Neukart, I. Galter, W. Mauerer, and C. Linnhoff-Popien, "A hybrid solution method for the capacitated vehicle routing problem using a quantum annealer," *Frontiers in ICT*, vol. 6, p. 13, 2019.
- [5] F. Phillipson and H. S. Bhatia, "Portfolio optimisation using the D-Wave quantum annealer," in *International Conference on Computational Science*. Springer, 2021, pp. 45–59.

- [6] S. van der Linde, "Quantum annealing for seismic imaging: Exploring quantum annealing possibilities for residual statics estimation using the D-Wave advantage system and hybrid solver," Master's thesis, Delft University of Technology, Nov 2021.
- [7] M. Langione, J.-F. Bobier, L. Krayer, H. Park, and A. Kumar, "The race to quantum advantage depends on benchmarking," Boston Consulting Group, Tech. Rep., Feb 2022.
- [8] C. McGeoch and P. Farré, "The advantage system: Performance update," D-Wave: The Quantum Computing Company, Tech. Rep., October 2021.
- [9] "IBM quantum breaks the 100-qubit processor barrier," https://research.ibm.com/blog/127-qubit-quantum-processor-eagle, 2021.
- [10] A. W. Cross, L. S. Bishop, S. Sheldon, P. D. Nation, and J. M. Gambetta, "Validating quantum computers using randomized model circuits," *Physical Review A*, vol. 100, 2019.
- [11] A. Wack, H. Paik, A. Javadi-Abhari, P. Jurcevic, I. Faro, J. M. Gambetta, and B. R. Johnson, "Quality, speed, and scale: three key attributes to measure the performance of near-term quantum computers," arXiv preprint arXiv:2110.14108v2, 2021.
- [12] T. Proctor, S. Seritan, K. Rudinger, E. Nielsen, R. Blume-Kohout, and K. Young, "Scalable randomized benchmarking of quantum computers using mirror circuits," arXiv preprint arXiv:2112.09853, 2021.
- [13] T. Lubinski, S. Johri, P. Varosy, J. Coleman, L. Zhao, J. Necaise, C. H. Baldwin, K. Mayer, and T. Proctor, "Application-oriented performance benchmarks for quantum computing," arXiv preprint arXiv:2110.03137, 2021.
- [14] S. Martiel, T. Ayral, and C. Allouche, "Benchmarking quantum coprocessors in an application-centric, hardware-agnostic, and scalable way," *IEEE Transactions on Quantum Engineering*, vol. 2, p. 1–11, 2021. [Online]. Available: http://dx.doi.org/10.1109/TQE.2021.3090207
- [15] E. Farhi, J. Goldstone, and S. Gutmann, "A quantum approximate optimization algorithm," arXiv preprint arXiv:1411.4028, 2014.
- [16] C. McGeoch, P. Farré, and W. Bernoudy, "D-Wave hybrid solver service + advantage: Technology update," D-Wave: The Quantum Computing Company, Tech. Rep., September 2020.
- [17] F. W. Glover and G. A. Kochenberger, "A tutorial on formulating QUBO models," *CoRR*, vol. abs/1811.11538, 2018. [Online]. Available: http://arxiv.org/abs/1811.11538
- [18] C. McGeoch and P. Farré, "The advantage quantum computer," D-Wave, Tech. Rep. 14-1058A-A, Jan 2022.
- [19] D-Wave, "qbsolv," https://docs.ocean.dwavesys.com/projects/qbsolv/en/latest/, 2021, accessed: 25-3-2022.
- [20] —, "dwave-neal," https://docs.ocean.dwavesys.com/projects/neal/en/latest/, 2021, accessed: 25-3-2022.
- [21] I. Dunning, S. Gupta, and J. Silberholz, "What works best when? a systematic evaluation of heuristics for max-cut and QUBO," *INFORMS Journal on Computing*, vol. 30, pp. 608–624, 2018. [Online]. Available: https://doi.org/10.1287/ijoc.2017.0798
- [22] G. A. Kochenberger, J.-K. Hao, Z. Lü, H. Wang, and F. Glover, "Solving large scale max cut problems via tabu search," *J. Heuristics*, vol. 19, pp. 565–571, 2011.
- [23] D-Wave, "D-wave ocean software documentation," https://docs.ocean.dwavesys.com/en/stable/, accessed:25-5-2022.
- [24] —, "Solver properties," https://docs.dwavesys.com/docs/latest/c_solver_properties.html#minimum-time-limit, accessed:25-5-2022.