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Tera-scalable Algorithms for Variable-Density Elliptic Hydrodynamics with Spectral Accuracy

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Abstract

A hybrid spectral/compact solver for variable-density viscous incompressible flow is described. Parallelization strategies for the FFTs and band-diagonal matrices are discussed and compared. Transpose methods are found to be highly competitive with direct block parallel methods when the problem is scaled to tens of thousands of processors. Various mapping strategies for the IBM BlueGene/L torus configuration of processors are explored. By optimizing the communication, we have achieved virtually perfect scaling to 32768 nodes. Furthermore, communication rates come very close to the theoretical peak speed of the BlueGene/L network with sustained computation in the TeraFLOPS range.

1 Introduction

It is well known that low-order accurate solutions to the compressible Euler equations on parallel computers require only nearest neighbor communication and thus are easily parallelized (Cohen et al., 2002). Solutions to the variable-density incompressible Navier-Stokes equations, however, are much more difficult to obtain on parallel computers due to their elliptic nature. For incompressible flow, the inclusion of variable-density and diffusion effects makes the equations much more complicated and difficult to solve on parallel computers, compared to the single fluid case (Yokokawa et al., 2002). If the flow is turbulent, i.e., possesses a wide range of length scales, then spectral and/or Padé (compact) methods are highly desirable, since they can accurately represent a broad range of wavenumbers (Lele, 1992). Spectral and compact methods involve implicit derivatives, thus further complicating the parallelization strategy. In this paper we demonstrate a scalable and efficient method for achieving high-order accurate solutions for variable-density viscous incompressible turbulence and compare our strategy with various alternatives. In particular, we consider incompressible Rayleigh-Taylor instability, since it exhibits all of the above-mentioned difficulties.

Rayleigh-Taylor instability (RTI) is the baroclinic generation of vorticity at a perturbed interface subject to acceleration in a direction opposite the mean density gradient (Rayleigh, 1883; Taylor, 1950). The resulting interpenetration and mixing of materials has far-reaching consequences in many natural and man-made flows, ranging from supernovae to Inertial Confinement Fusion (ICF). In supernovae, the rate of growth of the mixing region is thought to be a controlling factor in the rate of formation of heavy elements. In ICF, accurate prediction of the depth of interpenetration of the fluids is crucial in designing capsules to maintain shell integrity.

We have investigated various parallelization strategies for computing RTI flows on the IBM BlueGene/L (BGL) system. In particular, we have ported the *Miranda* code (Cook *et al.*, 2004) to this machine and have performed a series of scaling studies to determine the optimal approach to simulating RTI on tens of thousands of processors. In Section 2, the governing equations and solution techniques employed by *Miranda*

are laid out and in Section 3 a few simulation results are presented. Section 4 contains a description of the IBM BGL hardware and introduces key features that impact the scalability and performance of *Miranda's* algorithms. In Section 5, two parallelization strategies are compared, a direct block parallel matrix solution method and a transpose method. In Section 6, we attempt to optimize the transpose method on the BGL torus network. Section 7 contains scaling and performance data for *Miranda* using the transpose method. Finally, conclusions are presented in Section 8.

2 Miranda Code Description

Miranda solves the governing equations for flows comprised of two incompressible miscible fluids in an accelerated Cartesian frame of reference, i.e.,

$$\frac{\partial \rho}{\partial t} + u_j \frac{\partial \rho}{\partial x_j} = \rho \frac{\partial}{\partial x_j} \left(\frac{D}{\rho} \frac{\partial \rho}{\partial x_j} \right) , \qquad (1)$$

$$\frac{\partial \rho u_i}{\partial t} + \frac{\partial \rho u_i u_j}{\partial x_j} = -\frac{\partial p}{\partial x_i} + \frac{\partial \tau_{ij}}{\partial x_j} + \rho g_i \; , \quad \tau_{ij} = \mu \left[\frac{\partial u_i}{\partial x_j} + \frac{\partial u_j}{\partial x_i} - \frac{2}{3} \delta_{ij} \frac{\partial u_k}{\partial x_k} \right] \; ; \tag{2}$$

where ρ is density, $u_i = (u, v, w)$ is velocity, p is pressure, D is diffusivity, μ is viscosity and $g_i = (0, 0, -g)$ is acceleration. Spatial derivatives are computed in the code with the following 10th-order compact scheme

$$\beta f'_{j-2} + \alpha f'_{j-1} + f'_j + \alpha f'_{j+1} + \beta f'_{j+2} = c \frac{f_{j+3} - f_{j-3}}{6\Delta_i} + b \frac{f_{j+2} - f_{j-2}}{4\Delta_i} + a \frac{f_{j+1} - f_{j-1}}{2\Delta_i} , \qquad (3)$$

$$\alpha = \frac{1}{2}, \ \beta = \frac{1}{20}, \ a = \frac{17}{12}, \ b = \frac{101}{150}, \ c = \frac{1}{100},$$

where j is a grid index along a line with N points in the i direction, and Δ_i is the grid spacing in that direction.

The solution is marched forward in time via the following 3rd-order, variable-timestep, predictor-corrector method. For equations of the form $\dot{\phi} = F(\phi)$, the predictor step is

$$\phi^* = \phi^n + \Delta t_{\text{new}} \left[(1 + \mathcal{R}) F(\phi^n) - \mathcal{R} F(\phi^{n-1}) \right]$$
(4)

and the corrector step is

$$\phi^{n+1} = \phi^* + \Delta t_{\text{new}} \left[\mathcal{A} F(\phi^*) + (\mathcal{B} - \mathcal{R} - 1) F(\phi^n) + (\mathcal{C} + \mathcal{R}) F(\phi^{n-1}) \right] , \qquad (5)$$

where

$$\mathcal{R} = \Delta t_{\rm new}/(2\Delta t_{\rm old}) \;, \quad \mathcal{A} = (2\Delta t_{\rm new} + 3\Delta t_{\rm old})/[6(\Delta t_{\rm new} + \Delta t_{\rm old})]$$

$$\mathcal{B} = (\Delta t_{\rm new} + 3\Delta t_{\rm old})/(6\Delta t_{\rm old}) \;, \quad \mathcal{C} = -\Delta t_{\rm new}^2/[6\Delta t_{\rm old}(\Delta t_{\rm new} + \Delta t_{\rm old})] \;,$$

with $\Delta t_{\rm old}$ denoting the time increment between the n-1 and n timesteps, and $\Delta t_{\rm new}$ being the time increment between the n and n+1 times.

The density equation (1) is integrated by straightforward application of the predictor-corrector scheme. However, it must be advanced in conjunction with the momentum equation, which follows a pressure-projection algorithm. The pressure-projection scheme requires the solution of a Poisson equation. With periodic boundary conditions in x and y, the Poisson equation can be Fourier transformed to obtain

$$\mathcal{F}_{xy}\left\{\frac{\partial^2 p}{\partial x^2} + \frac{\partial^2 p}{\partial y^2} + \frac{\partial^2 p}{\partial z^2} = \Omega(x, y, z)\right\} \Rightarrow -k_x^2 \hat{\hat{p}} - k_y^2 \hat{\hat{p}} + \hat{\hat{p}}'' = \hat{\hat{\Omega}}(k_x, k_y, z) ,$$

where $\hat{\hat{p}}'' = \partial^2 \hat{\hat{p}}/\partial z^2$ and Ω contains various space and time derivatives. Thus, with j being the z-index of the grid, $\hat{\hat{p}}_j'' = \hat{\Omega}_j + k^2 \hat{p}_j$, with $k^2 = k_x^2 + k_y^2$. An 8th-order compact approximation for \hat{p}_j'' can be written as (Lele, 1992)

$$\beta \hat{\hat{p}}_{j-2}^{"} + \alpha \hat{\hat{p}}_{j-1}^{"} + \hat{\hat{p}}_{j}^{"} + \alpha \hat{\hat{p}}_{j+1}^{"} + \beta \hat{\hat{p}}_{j+2}^{"} = b \frac{\hat{\hat{p}}_{j+2} - 2\hat{\hat{p}}_{j} + \hat{\hat{p}}_{j-2}}{4\Delta z^{2}} + a \frac{\hat{\hat{p}}_{j+1} - 2\hat{\hat{p}}_{j} + \hat{\hat{p}}_{j-1}}{\Delta z^{2}} , \qquad (6)$$

where $\alpha=344/1179,\,\beta=23/2358,\,a=320/393$ and b=310/393. The linear system for \hat{p}_j thus becomes

$$\left[\beta k^{2} - \frac{b}{4\Delta z^{2}}\right]\hat{p}_{j-2} + \left[\alpha k^{2} - \frac{a}{\Delta z^{2}}\right]\hat{p}_{j-1} + \left[k^{2} + \frac{b}{2\Delta z^{2}} + \frac{2a}{\Delta z^{2}}\right]\hat{p}_{j} + \left[\alpha k^{2} - \frac{a}{\Delta z^{2}}\right]\hat{p}_{j+1} + \left[\beta k^{2} - \frac{b}{4\Delta z^{2}}\right]\hat{p}_{j+2} = -\left[\beta\hat{\hat{\Omega}}_{j-2} + \alpha\hat{\hat{\Omega}}_{j-1} + \hat{\Omega}_{j} + \alpha\hat{\hat{\Omega}}_{j+1} + \beta\hat{\hat{\Omega}}_{j+2}\right], \tag{7}$$

which is combined with Neumann boundary conditions and solved during both the predictor and corrector steps. Further details concerning the numerical scheme are described in Cook et al. (2004).

3 Simulation Results

Results of large-scale RTI simulations with *Miranda* on 1728 CPUs of an Intel Linux cluster are reported in Cook *et al.* (2004). The evolution of the instability is such that, at early times, the perturbations grow in a fairly independent fashion. Then the modes begin to couple to one another and secondary Kelvin-Helmholtz instabilities appear. At this point, the range of scales in the mixing layer rapidly increases, generating more mixed fluid within the layer. The large structures in the flow continue to increase until the mixing region becomes fully turbulent. Figure 1 illustrates the flow once it has evolved to a fully turbulent state.

4 BlueGene/L Architecture

BlueGene/L was developed by IBM, in partnership with ASC, as a massively-parallel computing system designed for research and development in computational sciences. Its goal is to deliver TeraFLOP-scale computing on a routine basis to selected applications of interest to the Advanced Simulation and Computing program (ASC) of the U.S. Department of Energy's National Nuclear Security Agency. It's extremely high compute-density design results in a very high cost-performance system with comparatively modest power and cooling requirements. At this writing, BGL is the fastest computer in the world, based on the 135 TFLOPS achieved on the LINPACK benchmark. The 32,768-node BGL system installed at LLNL will double in size to 65,536 nodes in late 2005. Here we provide only a high-level description of the system architecture, since BGL has been extensively described elsewhere (Adiga, 2003; BGL, 2005).

A compute node of BGL is composed of 10 chips, a 700 MHz compute ASIC plus nine DRAM main memory chips. This highly integrated design drastically lowers power consumption and space requirements, while favoring communication and memory performance. The BGL chip is comprised of two independent PowerPC 440 cores, each capable of two floating point operations (FLOPs) per cycle (including fused multiply-adds, yielding a theoretical peak of 4 FLOPs per cycle), several independent network controllers, three levels of cache (including a 4 MiB L3), and memory controllers. Though each floating point unit is capable of two operations per cycle, they are not independent: the second floating point pipe is usable only by 2-way SIMD instructions, or by 2-way "SIMOMD" (i.e., "single instruction, multiple operation, multiple data") instructions (Bachega et al., 2004). The theoretical peak of a single node is 2.8 GFLOPs, hence 184 TFLOPs for the current 32,768-node system. The two processors on each chip are identical, with symmetric access to resources (but L1 cache coherence is not provided by the 440 core).

The system software supports two modes for applications to use the cores. In communication coprocessor mode, there is a single MPI task per node, with one processor running the application and offloading much of the work of message passing to the second processor. In virtual node mode, two MPI tasks run on each node, one on each processor. MPI communications are handled by three independent special-purpose networks in BGL. Point-to-point and all-to-all communications are handled by a 16x32x64-node three-dimensional torus, with each node connected to its nearest neighbors via six independent bidirectional links. In addition to the torus, BGL also has two tree-topology networks to perform global operations like broadcasts, reductions, and barriers, with very low latency and high bandwidth. For example, the current 32,768-node machine can complete an MPI_Barrier across the entire machine in under 2 microseconds. Miranda makes extensive use of all-to-all communications on various collections of nodes, as discussed later. Thus, mapping the tasks onto

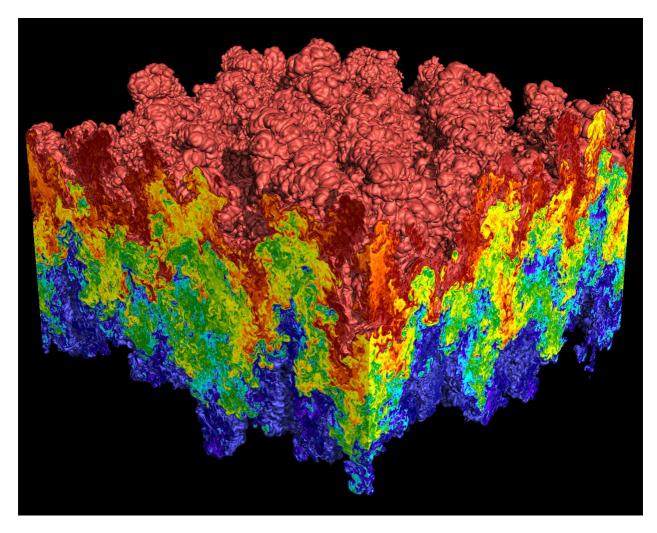


Figure 1: Turbulent state of Rayleigh-Taylor instability. Light fluid (density=1) is blue and heavy fluid (density=3) is red. The flow was computed with *Miranda* using 1152³ grid points on 1728 CPUs of an Intel Linux cluster.

the torus to reduce the overall communication time is one of the major challenges in running Miranda well on BGL.

5 Parallelization Strategies

5.1 Band-diagonal Matrices

A core task of the Miranda code is computing implicit derivatives, f', from functions, f, using the compact scheme. Additionally, FFTs in the horizontal directions are required for the Poisson solve. The communications required for the matrix solvers and FFTs are very similar; hence, for brevity, the discussion will focus mainly parallelization of the compact derivatives. Compact derivatives require solving the linear matrix problem

$$Af' = Bf. (8)$$

For a 10th-order first-derivative, A is a pentadiagonal matrix and B is a heptadiagonal matrix (see Eq. 3). If all data for f in the given direction is contained on a process, the solution for f' is determined directly by

a band-diagonal matrix solver based on LU decomposition. If f is distributed across some or all processors, then some communication overhead must be paid.

There are basically three strategies for solving band-diagonal linear systems (or computing Fourier transforms) in parallel: direct methods, whereby data are exchanged at processor boundaries then local solutions are obtained and joined back together; transpose methods, whereby the data are rearranged to give each processor all of the data it needs to compute a complete solution in serial; and iterative methods, whereby boundary data are exchanged and an initial guess is then iterated to convergence. Since we are interested in turbulent flows with significant high wavenumber content (for which iterative schemes are typically slow to converge) our studies thus far have focused on direct and transpose methods. However, we are beginning to investigate iterative schemes as well.

5.2 Domain Decomposition

For any of the above-mentioned strategies, a three dimensional computational domain can be broken up in any or all directions; e.g., a 3D mesh of size (n_x, n_y, n_z) can be distributed across a process grid of size (p_x, p_y, p_z) , such that each MPI process contains a block of size $(a_x, a_y, a_z) = (n_x/p_x, n_y/p_y, n_z/p_z)$. The left-hand side of Figure 2 portrays a typical *Miranda* decomposition in which $p_z = 1$ and the process grid is decomposed into p_y X-communicator groups and p_x Y-communicator groups. These communicators are

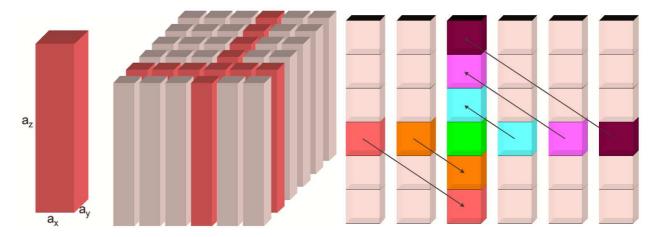


Figure 2: Left side: A sample two dimensional domain decomposition in *Miranda*. Right side: Block transfer in MPI_Alltoall operation for data transposes in *Miranda*.

used to martial data across processors, primarily for the purpose of computing high-order spatial derivatives. The highlighted regions in Figure 2 show portions of the computational domain existing on particular X and Y communicators.

5.3 Direct Approach to Solution

The direct option for solving pentadiagonal matrices in a direction of distributed data is to perform a block parallel pentadiagonal solve (see e.g., Ivanov & Walshaw (2004)). In this method, local (incomplete) pentadiagonal solutions are performed on each process, boundary data is gathered across the communicator to form a global overlap solution, and the overlap solution is used by each process to complete the global solution. Computing the right-hand side of (8) requires sharing planes of boundary data with nearest neighbors; e.g., if B is heptadiagonal, 3 planes of data must be exchanged at each boundary with paired MPI_Sendrecv calls. The pentadiagonal A on the left-hand side of (8) generates 4 planes of overlap data from the local solution. The global solution requires all overlap data, so an MPI_Allgather call is used to collect all overlap data on all processes. Each process then computes the global overlap solution independently (for

load balancing) and complete the exact global solution. Note that the global overlap problem requires the solution of a 4×4 block-tridiagonal linear matrix problem with a dimension equal to the number of processes on the communicator (and hence is not strictly scalable). If the direction is globally periodic, the global overlap solution involves a periodic block-tridiagonal matrix.

5.4 Transpose Approach to Solution

In the transpose method, all the data along a pencil of cells in the desired coordinate direction must be collected onto a single process before the linear system is solved. For an X-Y domain decomposition, Z-derivatives require no communication, since all the required data is local to each process. For the computation of X and Y derivatives, MPI_Alltoall operations are used to reorganize the data, in a load-balanced manner, across all the processes within a communicator group. Consider, for example, the computation of an X-derivative. The data on each process can be logically decomposed into blocks of size $a_x a_y a_z/p_x$. The MPI_Alltoall operation acting on this data over the X-communicator group behaves like a data transpose operation. The jth data block from the ith process in the group is transported to the ith data block of the jth process in the group; this is illustrated by the graphic on the right-hand side of Figure 2. Following the MPI_Alltoall, local data reorganization is performed to align the data pencils at constant stride. The derivatives are computed in parallel, with each processor operating on its own block of data pencils. Another MPI_Alltoall operation sends the computed derivatives back to their home processes. An incentive for breaking up the data in X and Y, rather than Y and Z, is to minimize the cost of the "descrambling" operation required after the MPI_Alltoall calls. With careful ordering of the data, the descrambling operations can performed as Fortran-intrinsic transpose and reshape calls on the first two (most contiguous) dimensions of the 3D arrays.

5.5 Comparisons

Various timings have been obtained on BlueGene/L for computing the gradient of a function,

$$\nabla f = \left(\frac{\partial f}{\partial x}, \frac{\partial f}{\partial y}, \frac{\partial f}{\partial z}\right) , \tag{9}$$

using the 10th-order compact scheme (3). This is a common operation in *Miranda*, performed many times per cycle. Derivatives were computed with both the direct block parallel pentadiagonal ("BPP") scheme and with the serial pentadiagonal solves of globally transposed data ("XDP" scheme). The two methods were compared for both 2D $(p_z = 1)$ and 3D domain decompositions. The runs were performed on the 32K-node BGLb hardware at LLNL (driver 100) in 512, 2K, 4K, 8K, 16K and 32K processor configurations with a fixed problem size $(16 \times 16 \times 2048 \text{ grid points})$ per process. All computations were performed in double (8-byte) precision with -03 optimization in communication coprocessor mode (one task per node). A sample of the timing results are presented in Table 1. From Table 1, it is clear that the XDP scheme performs best for two-dimensional processor arrangements. This is because no communication is required for directions containing complete columns of data. The BPP method performs best when the data and processor volumes are nearly cubic. A cubic data decomposition minimizes the surface-to-volume ratio of the grid blocks and a cubic processor distribution reduces the cost of the global overlap solution by minimizing the number of processes on each communicator. It is somewhat surprising that, despite the very different communication patterns, the XDP and BPP times are comparable to one another and have similar weak scaling properties. The anomalously high XDP value at 8096 CPUs is due to a suboptimal (machine default) processor arrangement. This is discussed in detail in Section 6, where we explore the possibility of reducing XDP times via custom torus mappings.

6 Communication Performance

For the transpose method, *Miranda* spends roughly half of its runtime in MPI_Alltoall communication operations. Consequently, we have investigated methods for reducing this time by employing special map-

2D processor layouts		XDP times [s]				BPP times [s]			
processes	data per process	dx	dy	dz	grad	dx	dy	dz	grad
$16 \times 32 \times 1$	$16 \times 16 \times 2048$	0.17	0.17	0.03	0.37	0.57	0.65	0.03	1.25
$32 \times 32 \times 1$	$16 \times 16 \times 2048$	0.23	0.17	0.03	0.43	1.05	0.65	0.03	1.73
$32 \times 64 \times 1$	$16 \times 16 \times 2048$	0.34	0.16	0.03	0.53	0.60	1.06	0.03	1.69
$64 \times 64 \times 1$	$16 \times 16 \times 2048$	0.33	0.22	0.03	0.58	3.21	1.26	0.03	4.50
$128 \times 64 \times 1$	$16 \times 16 \times 2048$	0.32	0.22	0.03	0.57	7.76	4.48	0.03	12.27
$128 \times 128 \times 1$	$16 \times 16 \times 2048$	0.32	0.22	0.03	0.57	7.72	5.69	0.03	13.44
$256 \times 128 \times 1$	$16 \times 16 \times 2048$	0.31	0.35	0.03	0.69	9.76	4.55	0.03	14.34
3D processor layouts		XDP times [s]				BPP times [s]			
processes	data per process	dx	dy	dz	grad	dx	dy	dz	grad
$8 \times 4 \times 16$	$64 \times 64 \times 128$	0.17	0.18	0.18	0.54	0.17	0.08	0.10	0.35
$8 \times 8 \times 16$	$64 \times 64 \times 128$	0.23	0.18	0.18	0.59	0.20	0.11	0.10	0.41
$8 \times 8 \times 32$	$128 \times 64 \times 64$	0.35	0.17	0.18	0.70	0.17	0.11	0.17	0.45
$16 \times 8 \times 32$	$64 \times 128 \times 64$	0.32	0.22	0.18	0.72	0.25	0.09	0.17	0.51
$32 \times 8 \times 32$	$64 \times 128 \times 64$	0.32	0.34	0.18	0.84	0.27	0.09	0.17	0.53
$32 \times 16 \times 32$	$64 \times 128 \times 64$	0.32	0.32	0.23	0.87	0.27	0.10	0.17	0.55
$32 \times 32 \times 32$	$128 \times 64 \times 64$	0.33	0.34	0.34	1.01	0.20	0.18	0.15	0.53

Table 1: Mean time per gradient operation using the transpose pentadiagonal (XDP) scheme and the direct block parallel pentadiagonal (BPP) scheme on the BGLc hardware with driver 100 and -03 optimization.

pings of the Miranda processes to the processors in the BGL Torus. We have also investigated the efficiency of the current IBM MPI_Alltoall implmentation by comparing it to an estimate of the minimum time required to complete such an operation.

6.1 The Mapping Problem

MPI_Alltoal1 communication is performed over the BGL point-to-point or "torus" network. With this network topology, communication performance is improved by packing the processes that communicate most frequently onto neighboring processors so as to minimize hop distance and maximize the number of torus links available for communication. Gven that MPI_Alltoal1 operations perform an equal amount of communication between every pair of processes in the communicator group it is desirable that these processes map to nearby processors. For two-dimensional domain decompositions, finding a good mapping is complicated by the fact that each process p belongs to two distinct communicator groups X(p) and Y(p) such that $X(p) \cap Y(p) = \{p\}$. It is difficult to construct a mapping that will closely pack the processes of X(p) and Y(p) for every process p. We have experimented with various mapping that optimize the placement of the processes in the X communicator group, leaving the processes of the Y communicator groups to fall where they may. We are in the process of investigating other mapping techniques, including the use of space filling curves and simulated anealing. One advantage to the maps we currently generate is that they are regular in shape and tile the space of processors, resulting in uniform communication times across all communicator groups, preventing communication load imbalance.

By default, processes are mapped by MPI_COMM_WORLD rank order to processors in the torus network in XYZ order. That is, if (q_x, q_y, q_z) is the size of the BGL partition, then the first q_x processes are mapped to locations $(0:q_x-1,0,0)$, the second q_x processes are mapped to $(0:q_x-1,1,0)$, etc. Alternatively, we can specify a mapping file that explicitly states the torus location for each process. *Miranda* constructs an X-Y process grid of size $p_x \times p_y$ via the MPI Cartesian communicator constructors. The result is that the first p_x processes in MPI_COMM_WORLD constitute the first X-communicator group. The second p_x processes form the second X-communicator group, and so on. By default then, each X-communicator group is mapped to contiguous X-direction rows of the BGL torus network. Consider the example of the 16K BGL configuration at LLNL which has shape (16, 32, 32). By default, *Miranda* will construct a (128, 128) process grid that maps the first X-communicator group onto the first 8 X-direction rows of the torus at locations (0:16, 0:7, 0),

the second group to (0:15,8:15,0), etc. This results in the first Y-communicator group being mapped to the locations $\{(0,\alpha,0:31),\alpha=0,8,16,24\}$ Consequently, the X-communicator groups are packed into subregions of shape $16\times8\times1$ whereas the Y-communicator groups are mapped to 4 Z-direction pencils of processors, each of length 32, distributed a distance of 7 hops apart in the Y-direction. Alternatively, we could produce a mapping that packs the X-communicator groups into a $16\times4\times2$ block, or $8\times4\times4$. Tight packing the X-communicator groups can improve X-direction communication, but at the expense of dispersing the processes in the Y-communicator groups.

6.2 Estimating Optimal MPI_Alltoall Communication Performance

We can construct a lower bound on the time required to complete an MPI_Alltoall operation on a mesh or torus network provided we make the assumption that the communication time is network bandwidth limited. The BGL torus network has six links on each node $(\pm X, \pm Y, \pm Z)$, each operating at a peak bandwidth b of 175 MB/sec. If we can compute the number of messages m of size s that traverse the most heavily loaded link in the torus, then the minimum time required to complete the communication operation is $t_{\min} = sm/b$. In general, it is difficult to determine the most heavily loaded link and the amount of data that it transports because messages in the BGL torus are broken into 256 byte packets that are adaptively routed; however, we can estimate this value.

Consider the two-dimensional mesh or torus as shown in Figure 3. The circles represent nodes and the line represent communication links. The dashed lines represent links in a torus that are not present in mesh (the Y-direction dashed links are not shown). Let $\lambda_x = 1$ if the network is a mesh in the X direction and

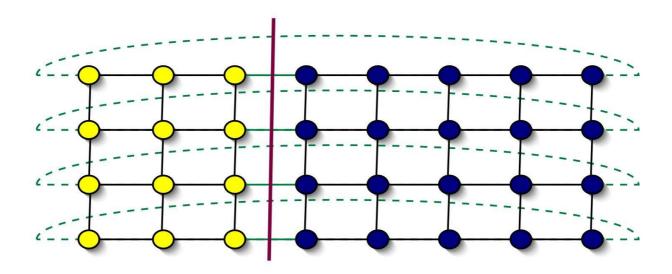


Figure 3: A two-dimensional processor mesh or torus network.

 $\lambda_x=2$ if it is a torus and similarly for λ_y . There are q_x processors in the X direction and q_y in the Y direction. We consider a cut through the $q_y\lambda_x$ X-links as shown, dividing the processors into left and right sets L and R. Let α be the number of nodes in L. The number of nodes in R is $q_xq_y-\alpha$ thus, the number of messages sent from L to R is $m(\alpha)=\alpha(q_xq_y-\alpha)$. This function has a maximum when $\alpha=q_xq_y/2$ stating that the set of links that carry the maximal message traffic are those that, if cut, would divide the network into two equal parts. The number of messages across this set of links is: $m_{\max}=q_x^2q_y^2/4$. We estimate the traffic across the maximally loaded link by taking the average of this message count across all cut links, or $m_{x,\max}=q_x^2q_y/(4\lambda_x)$. A similar argument for cuts in the X-direction yield an estimate of the maximal message count for any Y-direction link as $m_{y,\max}=q_y^2q_x/(4\lambda_y)$. So, an approximation of the minimum time required to complete an MPI_Alltoall operation on this 2D torus would be determined by the larger of these

two values, or

$$t_{\min} = \operatorname{Max}\left\{\frac{q_x}{\lambda_x}, \frac{q_y}{\lambda_y}\right\} \frac{sq_xq_y}{4b} \ . \tag{10}$$

The extension to three dimensions is straightforward and yields the estimate

$$t_{\min} = \operatorname{Max}\left\{\frac{q_x}{\lambda_x}, \frac{q_y}{\lambda_y}, \frac{q_z}{\lambda_z}\right\} \frac{sq_xq_yq_z}{4b} \ . \tag{11}$$

Furthermore, we can extend this argument to MPI_Alltoall operations on subcommunicators within a rectangular region of shape $R = (r_x, r_y, r_z)$ provided the processes of the subcommunicators are uniformly distributed within this region. Furthermore, we require the region be minimal, in that it is the smallest rectangle that contains all processes in the communicator groups and that if $q \in R$ then all processes in the communicator group containing q are also in R. Let κ be the number of communicator groups in R, then we estimate the minimum communication time for AlltoAll operations that execute simultaneously on all communicator groups within R to be

$$t_{\min} = \operatorname{Max}\left\{\frac{r_x}{\lambda_x}, \frac{r_y}{\lambda_y}, \frac{r_z}{\lambda_z}\right\} \frac{sr_x r_y r_z}{4b\kappa} \tag{12}$$

 $\lambda_x = 1$ if the BGL partition is a mesh in the X-direction or if $\lambda_x \leq q_x/2$. $\lambda_x = 2$ if the BGL partition is a torus in the X-direction and $r_x = q_x$.

6.3 MPI_Alltoall Communication Timings

In order to gain a complete picture of how torus mappings affect communication efficiency, a host of timing data has been collected for Miranda runs on processor partitions up to 32K of BlueGene/L. For these runs, we constructed a collection of process maps that pack the X-communicator groups into compact regions and timed the X and Y direction MPI_Alltoall operations. We then compared these timings with the estimated minimum time as computed using (12); the results are shown in Figure 4. The bars in the figure are organized into sets, with 512 node runs at the bottom and 32K node runs near the top. The bars in each set show the timings for different mappings of the X-communicator groups. For example, the $4 \times 4 \times 4$ bar of the 4K set says that the 64 processes of each X-communicator were mapped into a $4 \times 4 \times 4$ block of processors. Furthermore, the mapping that corresponds to the default process layout is always the bottom bar in each set. Each bar has four segments which are (from left to right): the estimated minimum time for an X-communicator MPI_Alltoall (red), the measured X-communicator MPI_Alltoall time minus the minimum estimate (blue), the estimated minimum time for the Y communicator (yellow) and the measured Y-communicator MPI_Alltoall time minus the estimated minimum (green). That is, the actual measured X communicator time is the sum of the red and blue bars and the actual Y communicator time is the sum of the yellow and green bars. Note that the 64K node entries include only estimated times since the full system is not yet available. Finally, when computing the estimated minimum MPI_Alltoall time via 12, we used a peak per link bandwidth of 170MB/sec rather than 175. This is to compensate for the overhead of MPI message and packet headers that are sent in addition to the actual data payload.

The conclusions to be gathered from Figure 4 are that, for reasonable mappings, communication times can vary by as much as a factor of 2 depending on how the MPI tasks are mapped to the BGL torus. Also, once the maximum number of hops in Y is reached, communication times can be significantly reduced by packing as tightly as possible in X (or vice versa). Finally, it is seen that a good mapping results in communication times close to the theoretical peak speed of the network.

7 Overall Performance

7.1 Scaling

We now turn to the overall performance (communication plus computation) of *Miranda* using the XDP scheme and simulating Rayleigh-Taylor instability, on up to 32768 nodes of the 700 MHz IBM BGL cluster. For scaling studies, *Miranda* was compiled with IBM's **xlf90** Fortran compiler (version 9.1 for BGL), with

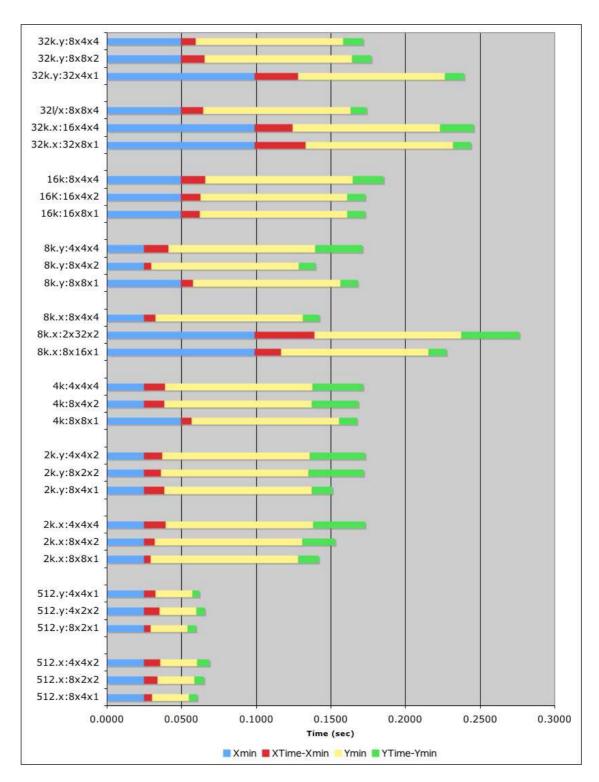


Figure 4: MPI_Alltoall communication timings for Miranda using various torus mappings on BlueGene/L.

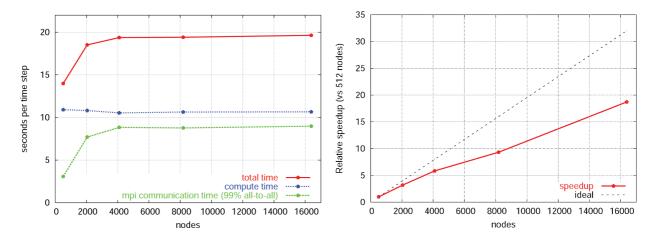


Figure 5: Left plot: Weak scaling for Miranda simulating Rayleigh-Taylor instability with $16 \times 16 \times 2048$ grid points per node. Right plot: Strong scaling for Miranda simulating Rayleigh-Taylor instability on a 512^3 grid. All simulations were performed on a 700 MHz BGL cluster in coprocessor mode.

optimization flags -03 -qalias=noaryovrlp -qmaxmem=-1 -qalign=4k -qhot=novector -qarch=440 -qtune=440 -qess1, and executed with system software driver DRV100_2004, using communication coprocessor mode. Weak scaling results (fixed workload per node) are shown on the left in Figure 5 and strong scaling results (fixed problem size) on the right. For the weak scaling runs, each node contained a $16 \times 16 \times 2048$ point grid. For the strong scaling runs, 512^3 total grid points were used. From Figure 5, it can be seen that the transpose approach to computing implicit derivatives yields near perfect scaling on the BGL architecture. Furthermore, Figure 5 shows linear speedup for a fixed problem size as more processors are added.

7.2 Tuning and FLOPS

For Raleigh-Taylor instability simulations using compact derivatives, *Miranda* spends a significant amount of time in the backsubstitution portion of the pentadiagonal matrix solver. We've therefore focused our optimization efforts on this portion of the algorithm in an attempt to improve its floating point performance. We have implemented loop unrolling, hardware prefetch controls, and cache blocking, with empirical testing to determine the optimal blocking parameters for these functions. Manual tuning in this fashion has generated a 33% speedup of this portion of the algorithm. Using performance utilities provided by IBM, we have measured the sustained computing rate of *Miranda* on 32768 BGL nodes to be 1.8 TFLOPS, with the core of the matrix solver being just over 6 TFLOPS.

8 Conclusions

The combined effects of variable density and diffusion on incompressible turbulent flows, coupled with the need for high fidelity numerical methods, presents a challenging problem for parallel computing on tens of thousands of processors. We have demonstrated that, contrary to popular opinion, transpose techniques for implicit derivatives can provide excellent scaling to such large numbers of processors and are highly competitive with direct block parallel matrix decomposition schemes, provided an optimal torus mapping is employed. After porting the *Miranda* code to the BGL architecture and removing various memory and CPU bottlenecks, we have obtained near perfect weak scaling and linear speedup of the code to 32768 processors. By packing the data tightly in one direction, we have reduced the computational overhead by roughly 12% over the default mapping and have achieve data transfer rates exceeding 80% of the theoretical peak of the network. In addition to optimizing the torus mapping for MPLAlltoall communications,

efforts are currently underway to tune the packet injection rate of the messages. Despite the substantial communication and data rearrangement requirements of the implicit 10th-order compact scheme, we have achieved a sustained computational rate of 1.8 TFLOPS for *Miranda*, simulating Rayleigh-Taylor instability on 32768 nodes of the IBM BGL machine.

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