Finite Approximations of Switched Homogeneous Systems for Controller Synthesis

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Abstract

We demonstrate the use of a new, control-oriented notion of finite state approximation for a particular class of hybrid systems. Specifically, we consider the problem of designing a stabilizing binary output feedback switching controller for a pair of unstable homogeneous second order systems. The constructive approach presented in this note, in addition to yielding an explicit construction of a deterministic finite state approximate model of the hybrid plant, allows us to efficiently establish a useable upper bound on the quality of approximation, and leads to a discrete optimization problem whose solution immediately provides a certifiably correct-by-design controller for the original system. The resulting controller consists of a finite state observer for the plant and a corresponding full state feedback switching control law.

I. INTRODUCTION

A. Motivation and Overview

Finite state approximations and abstractions of hybrid plants have been explored as a means for addressing instances in which the interaction between analog and discrete dynamics¹ leads to complex analysis and synthesis problems that cannot be adequately handled by traditional methods. Some of the early work in this area explored the use of 'qualitative' models, namely non-deterministic automata whose output behavior contains that of the original hybrid system [19]. In [21], an approach for deriving non-deterministic finite state approximations of systems with quantized outputs was proposed and used in conjunction with the supervisory control theory developed by Ramadge and Wonham [22] to design controllers meeting the desired specifications. Another line of research, inspired by formal verification techniques in computer science, explored the construction of deterministic discrete abstractions of hybrid systems [2]. Early work in this area focused on identifying classes of systems that admit finite bisimulation abstractions [11], [15]. Having recognized that these classes are fairly limited, more recent work has focused on finding discrete abstractions that are related to the original hybrid system by an approximate simulation or bisimulation relation [10], [25], [26]. The resulting controller design problem is then a two step procedure in which a finite state supervisory controller is first designed, and subsequently refined to yield a certified hybrid controller for the original plant [27].

In this note, we demonstrate the use of a new², alternative notion of approximation that, in addition to yielding a finite state deterministic approximate model of the original hybrid

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¹"Analog" and "discrete" here refer to whether the dynamics evolve in a continuous or a discrete state-space; the independent variable, time, may be continuous or discrete in either case.

²Preliminary versions of this work were presented in [29], [30], [32].

system, allows one to efficiently establish a useable upper bound on the quality of approximation, and leads to a discrete optimization problem whose solution immediately provides a certifiably correct-by-design (finite state) controller for the original system. The notion of finite input/output approximation employed in this note is thus potentially better suited to the purpose of *control design* than the existing methods since it allows for a streamlined and efficient *synthesis procedure*. Another promising development is that, in contrast to many of the existing methods and our early work on this problem, we have succeeded in approximating *unstable* systems by finite state automata for the purpose of control design.

We demonstrate this approach in a particular setting, namely for the problem of designing a stabilizing, binary output feedback, switching controller for a pair of unstable, homogeneous, second order systems. This problem was specifically chosen because several of its simpler formulations have been extensively studied and are well understood [7], [17]. Necessary and sufficient conditions for stability of switched second order homogeneous systems were derived in [9]. A Lyapunov based approach for designing stabilizing full state feedback switching controllers for second order homogenous systems was proposed in [12]. While the results available for general homogeneous systems remain limited, the special case of switched linear systems has been more extensively studied. For instance, in the linear full state feedback analog case, the existence of a Hurwitz convex combination $(A_{eq} = \alpha A_1 + (1 - \alpha)A_2, \alpha \in (0, 1))$ of a given pair of unstable state matrices $(A_1 \text{ and } A_2)$ is known to be a necessary [8] and sufficient [34] condition for the existence of a quadratically stabilizing switching controller. The switching controller can take on a variety of forms in this case (time-switched controller, hybrid controller, etc...). In particular, when A_{eq} has a real eigenvalue, a quadratic switching surface and corresponding state dependent variable structure control law [13] can be designed to stabilize the system [34]. In this case, once designed assuming full state feedback, the stabilizing controller can be exactly *implemented* by transmitting *appropriately chosen* binary state information from the plant to the controller, namely information that the state is currently in one or the other region of the state-space. Lyapunov based approaches have also been extended to time-sampled versions of the problem [6], [18], and to the output feedback case [24]: Typically the output is assumed to be a linear function of the state, and the resulting controller is observer-based [8]. In contrast, the setting considered in this note where fixed, binary sensors are used leads to a more difficult state estimation (and thus controller synthesis) problem³. This case has been much less well studied, and has only begun to receive attention in the recent past [4], [20], [23]. Specifically, the use of finite state approximations in this context remains minimally explored.

The constructive controller synthesis approach can be summarized as follows: First, the plant is approximated by a deterministic finite state machine and a usable bound on the quality of approximation is efficiently established. Next, a switching control law is designed to robustly stabilize the nominal finite state machine model in the presence of admissible approximation uncertainty. The resulting controller, which is thus certifiably correct-by-design, is then also a finite state system. Additionally, the procedure by which it is synthesized is computationally tractable in the sense that it is based on a *finite* state nominal model.

Practically, this design approach may be utilized in applications where very coarse sensors are used to keep operating cost, weight or power consumption low. Looking ahead, having a systematic procedure for designing control systems specifically for the case where the plant

³This setup differs significantly from the variable structure controller setup described above, as the binary sensors are assumed to be fixed a priori with no regard to the system dynamics. As such, there generally is a mismatch between the information provided by the sensors and the binary information needed to implement a variable structure switching law. The resulting state estimation problem is at the core of the design challenge here.

and controller interact through finite alphabets points to new paradigms for control over networks in which the amount of information to be encoded and transmitted over communication channels is significantly reduced. Finally, this design procedure may be employed by artificially imposing coarse measurements on a switched system for which there is no other available or tractable approach for synthesizing controllers, though the relevant question of how to best impose a finite measurement quantization for the control objective of interest is not addressed in this paper.

B. Organization of the Paper

The paper is organized as follows: The statement of the control design problem and an overview of the controller design procedure are described in Section II. The algorithms proposed for constructing a finite state approximation of the plant, and for computing an a-posteriori bound on the resulting approximation error are presented in Section III. Design of the robust stabilizing switching law and the structure of the resulting switching controller are addressed in Section IV. Illustrative examples are presented in Section V. The paper concludes in Section VI, where recommendations for future work are given.

C. Notation

The notation used in the paper is fairly standard: \mathbb{Z}_+ , \mathbb{R} and \mathbb{R}_+ denote the sets of nonnegative integers, reals and non-negative reals, respectively. For real interval I = [a, b), |I| = |b-a| denotes the length of I. Given a vector $v \in \mathbb{R}^n$, v' denotes its transpose and $||v|| = \sqrt{v'v}$ denotes its Euclidean norm. Given sets A and B, card(A) denotes the cardinality of A, $A \times B$ denotes their Cartesian product, $A^{\mathbb{Z}_+}$ denotes the set of all infinite sequences taking their values in set A and (boldface) a denotes an element of $A^{\mathbb{Z}_+}$. Given a function $f : A \to B$ and a proper subset $A' \subset A$, $f|_{A'}$ denotes the restriction of f to A', that is the function $f|_{A'} : A' \to B$ defined by $f|_{A'}(a) = f(a)$ for $a \in A'$. Given two functions $f : A \to B$ and $g : B \to C$, their composition, denoted by $g \circ f$, is a function $g \circ f : A \to C$ defined by $g \circ f(a) = g(f(a))$. Given two real-valued functions $f : A \to \mathbb{R}$ and $g : A \to \mathbb{R}$, the notation $f \leq g$ is understood to mean that $f(a) \leq g(a)$ for all $a \in A$.

II. PROBLEM STATEMENT & OVERVIEW OF THE DESIGN PROCEDURE

A. Problem Statement

Consider a discrete-time plant P described by

$$x(t+1) = f_{u(t)}(x(t))$$
(1)

$$y(t) = sign(c'x(t))$$
⁽²⁾

$$v(t) = log\left(\frac{\|x(t+1)\|}{\|x(t)\|}\right)$$
(3)

where the time index $t \in \mathbb{Z}_+$, state $x(t) \in \mathbb{R}^2$, performance output $v(t) \in \mathbb{R}$, and control input u is binary with $u(t) \in \mathcal{U} = \{0, 1\}$. Sensor output y is also taken to be binary, $y(t) \in \mathcal{Y} = \{-1, 1\}$, with the understanding that when c'x = 0, y is taken to be +1 in one quadrant and -1 in the other. Functions $f_u : \mathbb{R}^2 \to \mathbb{R}^2$ and vector $c \in \mathbb{R}^2$, $c \neq 0$, are given. The following conditions are assumed to hold for each $u \in \{0, 1\}$:

- 1) f_u is continuous.
- 2) f_u is homogeneous with degree 1: That is, $f_u(\alpha x) = \alpha f_u(x)$ for all $\alpha \in \mathbb{R}$, $x \in \mathbb{R}^2$.

In this setup, the effect of the control action is to pick a choice of system from a given pair and to hold that choice until the next time step, at which point a new measurement is available and a new choice of system is made and implemented.

The objective is to design a controller $K \subset \mathcal{Y}^{\mathbb{Z}_+} \times \mathcal{U}^{\mathbb{Z}_+}$ such that the closed loop system (P, K) with output v (Figure 1) satisfies the following performance objective for some R > 0, for any initial condition of plant P:

$$\sup_{T \ge 0} \sum_{t=0}^{T} \left(v(t) + R \right) < \infty \tag{4}$$

Satisfaction of this performance objective guarantees that the state of the closed loop system (globally) exponentially converges to the origin, on average, at a rate not less than R.

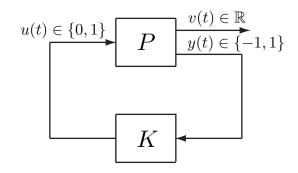


Fig. 1. Closed loop system.

B. Overview of the Controller Design Procedure

Design of the stabilizing controller is an iterative procedure, with each iteration consisting of a sequence of steps. First, the plant P is approximated by the interconnection of a finite state machine M and a complex system Δ representing the approximation error (Figure 2). Next, a meaningful and useable "gain bound" is established for the approximation error, system Δ . Finally, an attempt is made to synthesize a feedback switching law for the nominal finite state model M, that is robust to the approximation error Δ . If synthesis is successful, the resulting controller is guaranteed to globally exponentially stabilize the plant at some verified rate R. Otherwise if synthesis is unsuccessful, or if the verified rate is unsatisfactory, a more refined approximation (meaning a finite state machine with a larger number of states) is sought for the plant and the above process is repeated.

$$u(t) \in \mathcal{U} \qquad P \qquad \begin{array}{c} v(t) \in \mathbb{R} \\ y(t) \in \mathcal{Y} \end{array} \approx \begin{array}{c} w(t) \in \mathcal{W} \\ u(t) \in \mathcal{U} \\ u(t) \in \mathcal{U} \end{array} \begin{array}{c} \Delta \\ u(t) \in \mathcal{U} \\ y(t) \in \mathcal{V} \\ \overline{y(t)} \in \mathcal{Y} \end{array}$$

Fig. 2. Plant P, its finite state machine approximation M, and the corresponding error system Δ .

III. A FINITE STATE APPROXIMATION OF THE PLANT

A deterministic finite state machine (DFM for short) is understood to be a discrete-time dynamical system (independent variable $t \in \mathbb{Z}_+$) described by a state transition equation (5) and an output equation (6)

$$q(t+1) = f(q(t), u(t))$$
 (5)

$$y(t) = g(q(t, u(t))) \tag{6}$$

State $q(t) \in Q$, input $u(t) \in U$, and output $y(t) \in Y$, where Q, U and Y are *finite* state, input alphabet and output alphabet sets, respectively.

A. A Notion of Approximation

Given a plant P with binary control input $u(t) \in \mathcal{U} = \{0, 1\}$, binary sensor output $y(t) \in \mathcal{Y} = \{-1, 1\}$ and upper bounded⁴ performance output $v(t) \in \mathbb{R}$. Consider a system M with inputs $u(t) \in \mathcal{U}$ and $w(t) \in \mathcal{W} = \{0, 1\}$, and with outputs $u(t) \in \mathcal{U}$, $\tilde{y}(t) \in \mathcal{Y}$ and $\hat{v}(t) \in \mathcal{V}$, where \mathcal{V} is a finite discrete subset of \mathbb{R} . Suppose that M has the internal structure shown in Figure 3, where \hat{M} is a deterministic finite state machine. To ensure well-posedness, we require that there be no direct feedthrough from input \tilde{y} to output \hat{y} in \hat{M} : In other words, $\hat{y}(t)$ is only allowed to be a function of the input u(t) and the state of \hat{M} at time t. Memoryless system ϕ is defined by

$$\tilde{y} = \begin{cases} \hat{y} & \text{if } w = 0\\ -\hat{y} & \text{if } w = 1 \end{cases}$$

System M is thus a deterministic finite state machine.

Consider also the corresponding system Δ shown in Figure 4. Memoryless system Ψ is defined by

$$w = \begin{cases} 0 & \text{if } y = \hat{y} \\ 1 & \text{if } y \neq \hat{y} \end{cases}$$

System Δ is not a deterministic finite state machine in general, unless plant P is one. Moreover, the existing requirement that there be no direct feedthrough from y to \hat{y} in \hat{M} ensures that the outputs y of P and \hat{y} of \hat{M} cannot be trivially matched.

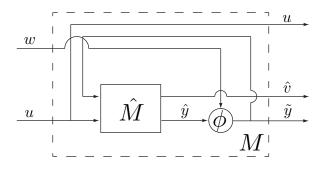


Fig. 3. Internal structure of M, the deterministic finite state machine approximation of P.

Now consider the interconnection, as in Figure 2, of M and Δ with these particular structures, and suppose that the two copies of \hat{M} are identically initialized. Regardless of the choice of \hat{M} , it can be seen by direct inspection that for arbitrary initial conditions of P

⁴It will become clear in Section III-B that v is indeed upper bounded for the class of systems under consideration.

and for any input $\mathbf{u} \in \mathcal{U}^{\mathbb{Z}_+}$, the corresponding outputs $\mathbf{y} \in \mathcal{Y}^{\mathbb{Z}_+}$ of P and $\tilde{\mathbf{y}} \in \mathcal{Y}^{\mathbb{Z}_+}$ of (M, Δ) are identical.

Suppose that we can construct \hat{M} such that the following two conditions are satisfied for any input $\mathbf{u} \in \mathcal{U}^{\mathbb{Z}_+}$ and any initial condition of P:

1) Outputs $\hat{\mathbf{v}} \in \mathcal{V}^{\mathbb{Z}_+}$ of the interconnection (M, Δ) and $\mathbf{v} \in \mathbb{R}^{\mathbb{Z}_+}$ of P satisfy

$$\hat{v}(t) \ge v(t), \text{ for all } t \in \mathbb{Z}_+.$$
 (7)

2) For ρ : U → {1} and μ : W → ℝ₊ defined by μ(w) = w, there exists a constant γ ∈ (0,1) such that every feasible input/output pair of signals (u, w) ∈ U^{ℤ+} × W^{ℤ+} of the error system Δ satisfies the gain condition:

$$\inf_{T \ge 0} \sum_{t=0}^{T} \gamma \rho(u(t)) - \mu(w(t)) > -\infty.$$
(8)

The resulting deterministic finite state machine M is then said to be a *finite state approximation* of P, and the corresponding system Δ is said to be the *approximation error*. The following remarks aim to clarify the reasoning behind this approach to plant approximation.

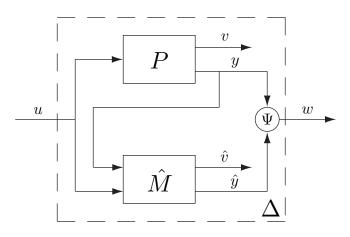


Fig. 4. The error system Δ of \hat{M} and P.

Remark 1: The structure of the approximation error Δ proposed in this setting is significantly different from that of the approximation error in the traditional stable LTI model reduction setting. In that setting, a stable lower order LTI model is considered to be a good approximation of the original stable LTI system if the outputs of the two systems are not too different, in a worst case sense, when driven side by side by the same input. The structure proposed here, where the output of the plant P is fed back to \hat{M} , is needed because the original system is not stable: Two copies of P initialized differently and driven side by side may end up with different outputs at every step. Thus, there is a need to explicitly 'estimate' the initial condition of P, otherwise there is no hope for satisfying gain condition (8) for any choice of \hat{M} .

Remark 2: The first condition characterizing a valid finite state approximation, namely the inequality in (7), is motivated by the control objective (4) at hand. In particular, if (7) holds and a controller $\varphi \subset \mathcal{Y}^{\mathbb{Z}_+} \times \mathcal{U}^{\mathbb{Z}_+}$ is designed to ensure that the closed loop system (M, Δ, φ) satisfies the auxiliary performance objective

$$\sup_{T \ge 0} \sum_{t=0}^{T} \hat{v}(t) + R < \infty,$$
(9)

then a corresponding controller implemented in feedback with plant P is guaranteed to satisfy performance objective (4): We will return to address this statement in more detail in Section IV. Upper boundedness of signal v ensures that (7) can always be satisfied for some appropriate finite choice of \mathcal{V} . Intuitively, a better approximation in which the instantaneous difference between v and \hat{v} is smaller is desirable and leads to a less conservative controller design.

Remark 3: The second condition characterizing a valid finite state approximation, namely the 'gain condition' describing the approximation error Δ , is compatible with the framework and the tools for robustness analysis of systems over finite alphabets developed in [31]. The choice of functions ρ and μ proposed here are specific to the control objective at hand. In particular, the smallest value of γ for which (8) holds (the 'gain' of Δ) represents the fraction of time (computed over an infinite window) that the outputs of P and \hat{M} disagree in the worst-case scenario. A smaller value of γ is thus desirable and indicative of a better approximation.

In the following sections, a constructive procedure for generating a viable \hat{M} and a computationally efficient algorithm for computing an a-posteriori upper bound on the gain of the resulting error system Δ are proposed.

B. Construction of the Nominal Model

The approach proposed for constructing a nominal finite state model to approximate plant P takes advantage of the dynamical properties specific to homogeneous systems, evident after a coordinate transformation; interested readers are referred to [28] for an overview of other potential approaches. Let

$$x = \begin{bmatrix} x_1 \\ x_2 \end{bmatrix}, \ c = \begin{bmatrix} c_1 \\ c_2 \end{bmatrix}, \ f_u(.) = \begin{bmatrix} f_{u1}(.) \\ f_{u2}(.) \end{bmatrix}$$

In a polar coordinate system where $r = \sqrt{x_1^2 + x_2^2}$ and $\theta = \tan^{-1}\left(\frac{x_2}{x_1}\right)$, the dynamics of system *P* described in (1), (2) and (3) are given by:

$$r(t+1) = r(t) \left\| f_{u(t)} \left(\beta(\theta(t)) \right) \right\|$$

$$\theta(t+1) = \tan^{-1} \left(\frac{f_{u(t)2} \left(\beta(\theta(t)) \right)}{f_{u(t)1} \left(\beta(\theta(t)) \right)} \right)$$
(10)

$$y(t) = sign\left(c'\beta(\theta(t))\right)$$
(11)

$$v(t) = \log\left(\left\|f_{u(t)}\left(\beta(\theta(t))\right)\right\|\right)$$
(12)

where

$$\beta(\theta) = \left[\begin{array}{c} \cos \theta \\ \sin \theta \end{array} \right].$$

This coordinate transformation highlights two important properties of the class of systems under consideration:

1) The evolutions of the angular coordinate θ and of both outputs of P are independent of the radial coordinate r: The state of the system relevant to the stabilization problem at hand effectively evolves on the unit circle.

2) For brevity of notation in the following argument, denote the composition β ∘ θ simply by β. For each t ∈ Z₊, we have β(t) ∈ [-1,1]×[-1,1] = R₁, f_{u(t)}(β(t)) ∈ f_{u(t)}(R₁) = R₂^{u(t)}, where R₂⁰ and R₂¹ are again compact in ℝ² since each f_u is continuous. Thus we have

$$||f_{u(t)}(\beta(t))|| \le \max_{x \in R_2^u, u \in \{0,1\}} ||x|| = M$$

and

$$v(t) = log \Big(\|f_{u(t)}(\beta(t))\| \Big) \le log M$$
, for all $t \in \mathbb{Z}_+$.

It thus follows that v is upper bounded.

Compactness of the effective state set and upper boundedness of the performance output v are instrumental in ensuring that this class of plants is amenable to a finite state approximation.

 \hat{M} is constructed by partitioning the unit circle into a collection of intervals and defining the potential states of \hat{M} to correspond to the intervals and the unions of adjacent intervals. This partition generally need not consist of equal length intervals; however, it should be chosen such that the outputs associated with all the (analog) states of P whose angular coordinates lie in any given interval are identical, in order to avoid introducing unnecessary uncertainty.

In particular, consider a partition of the unit circle consisting of intervals I_1, \ldots, I_{2n} where $I_i = [\alpha_i, \alpha_{i+1})$ for some sequence of angles $\alpha_1 < \ldots < \alpha_{2n+1}$ satisfying:

$$\alpha_1 = \tan^{-1} \left(-\frac{c_1}{c_2} \right), \ \alpha_1 \in [0,\pi)$$
$$\alpha_{n+1} = \alpha_1 + \pi$$
$$\alpha_{2n+1} = \alpha_1 + 2\pi$$

The number and choice of angles is a design parameter here.

Construct a set Q of intervals on the unit circle:

$$\hat{\mathcal{Q}} = S_1 \cup S_2 \cup \ldots \cup S_{2n}$$

where S_k is the set of all "k adjacent intervals". That is:

$$S_{1} = \{I_{1}, I_{2}, \dots, I_{2n}\}$$

$$S_{2} = \{I_{1} \cup I_{2}, I_{2} \cup I_{3}, \dots, I_{2n} \cup I_{1}\}$$

$$S_{3} = \{I_{1} \cup I_{2} \cup I_{3}, I_{2} \cup I_{3} \cup I_{4}, \dots, I_{2n} \cup I_{1} \cup I_{2}\}$$

$$\vdots$$

$$S_{2n} = \{I_{1} \cup I_{2} \cup \dots \cup I_{2n}\}$$

This set \hat{Q} , consisting of 2n(2n-1)+1 distinct elements, is the set of all *potential states* of \hat{M} . It will become clear shortly that continuity of f_u rules out the union of two non-adjacent quantization intervals from being a potential state of \hat{M} in this setup.

For $q \in \mathcal{Q}$, let

$$\mathcal{P}_{1}(q) = \begin{cases} q \cap [\alpha_{1}, \alpha_{n+1}) & \text{if } sign(c'\beta(\alpha_{2})) = 1\\ q \cap [\alpha_{n+1}, \alpha_{2n+1}) & \text{otherwise} \end{cases}$$

and

$$\mathcal{P}_{-1}(q) = \begin{cases} q \cap [\alpha_1, \alpha_{n+1}) & \text{if } sign(c'\beta(\alpha_2)) = -1 \\ q \cap [\alpha_{n+1}, \alpha_{2n+1}) & \text{otherwise} \end{cases}$$

Note that $\mathcal{P}_1(q) \in \hat{Q}$ and $\mathcal{P}_{-1}(q) \in \hat{Q}$, by construction. For $u \in \mathcal{U}, q \in \hat{\mathcal{Q}}$, let

$$q_{+}^{u} = tan^{-1} \left(\frac{f_{u2}(\beta(q))}{f_{u1}(\beta(q))} \right)$$

and

$$\mathcal{I}_q^u = \Big\{ i \in \{1, \dots, 2n\} \Big| q_+^u \cap [\alpha_i, \alpha_{i+1}) \neq \emptyset \Big\}.$$

Remark 4: It follows from the continuity of f_u that for any choice of $u \in \mathcal{U}$ and $q \in \hat{\mathcal{Q}}$, q^u_+ is a single connected interval. Thus

$$\bigcup_{i \in I_q^u} [\alpha_i, \alpha_{i+1})$$

is an interval of the form $[\alpha_i, \alpha_k)$, in other words, an element of \hat{Q} .

The dynamics of \hat{M} are then given by

$$q(t+1) = \hat{f}(q(t), u(t), \tilde{y}(t))
\hat{y}(t) = \hat{g}(q(t))
\hat{v}(t) = \hat{h}(q(t), u(t))$$
(13)

with $\hat{f}: \hat{\mathcal{Q}} \times \mathcal{U} \times \mathcal{Y} \to \hat{\mathcal{Q}}, \, \hat{g}: \hat{\mathcal{Q}} \to \mathcal{Y} \text{ and } \hat{h}: \hat{\mathcal{Q}} \times \mathcal{U} \to \mathcal{V} \text{ defined by}$

$$\begin{split} \hat{f}(q, u, \tilde{y}) &= \bigcup_{i \in \mathcal{I}_{\mathcal{P}_{\tilde{y}}(q)}} [\alpha_i, \alpha_{i+1}) \\ \hat{g}(q) &= \begin{cases} 1 & \text{if } q = \mathcal{P}_1(q) \text{ or } |\mathcal{P}_1(q)| \ge |\mathcal{P}_{-1}(q)| \\ -1 & \text{if } q = \mathcal{P}_{-1}(q) \text{ or } |\mathcal{P}_1(q)| < |\mathcal{P}_{-1}(q)| \\ \hat{h}(q, u) &= \sup_{\theta \in q} \log \left(\|f_u(\beta(\theta))\| \right) \end{split}$$

In particular, let q_o denote the potential state corresponding to the whole unit circle, that is $q_o = [\alpha_1, \alpha_{2n+1})$. It is shown in the following Lemma that condition (7) is satisfied provided \hat{M} is initialized to this state.

Lemma 1: Consider a plant P and let M be the corresponding finite state machine defined by (13) for some choice of integer n > 0 and (admissible) angles $\alpha_1, \ldots, \alpha_{2n+1}$. If $q(0) = q_o$, then for any input $\mathbf{u} \in \mathcal{U}^{\mathbb{Z}_+}$ and for any initial condition of P, the outputs $\hat{\mathbf{v}} \in \mathcal{V}^{\mathbb{Z}_+}$ of (M, Δ) and $\mathbf{v} \in \mathbb{R}^{\mathbb{Z}_+}$ of P satisfy (7).

Proof: Let $\theta(t)$ and q(t) denote the states of P and \hat{M} , respectively, at time t. It follows from the construction of \hat{M} that:

- 1) $\theta(t) \in q(t) \Rightarrow \hat{v}(t) \ge v(t)$
- 2) $\theta(t) \in q(t) \Rightarrow \theta(t+1) \in q(t+1)$

We have $\theta(0) \in [0, 2\pi) = [\alpha_1, \alpha_1 + 2\pi) = [\alpha_1, \alpha_{2n+1})$. When \hat{M} is initialized to $q(0) = q_o$, $\theta(0) \in q(0)$, hence $\hat{v}(0) \ge v(0)$ and $\theta(1) \in q(1)$. The statement of the Lemma thus follows by induction on t.

Since the initial state of \hat{M} will be fixed to q_o , the *actual* states of \hat{M} consist of those states in \hat{Q} that are reachable from q_o . The problem of computing the reachable subset Q can be recast as either one of two well-studied problems: (i) A one-to-all network shortest path problem, which can be efficiently solved (polynomial time in n) using any of the available shortest path algorithms (Dijkstra's, Bellman-Ford, ...) [1], or (ii) the problem of computing the accessible states of an automaton, which can be efficiently solved by constructing the

transition tree of the automaton [16]. Thus any of a standard collection of algorithms can be used to compute Q; interested readers are referred to the above two references for details of the various algorithms.

The dynamics of M are thus given by:

$$\begin{array}{rcl}
q(t+1) &=& f(q(t), u(t), \tilde{y}(t)) \\
\hat{y}(t) &=& g(q(t)) \\
\hat{v}(t) &=& h(q(t), u(t))
\end{array} \tag{14}$$

where the state transition function and output functions are given by $f = \hat{f}|_{\mathcal{Q} \times \mathcal{U} \times \mathcal{Y}}$ and $g = \hat{g}|_{\mathcal{Q}}, h = \hat{h}|_{\mathcal{Q} \times \mathcal{U}}$, respectively.

C. Description of the Approximation Error

T

In this section, a procedure for computing an upper bound on the gain of the error system Δ associated with a given plant P and a corresponding nominal finite state model M, constructed as described in the previous section, is presented. As before, Q refers to the actual states of \hat{M} .

Lemma 2: If there exists a function $V : \mathcal{Q} \to \mathbb{R}$ and a $\gamma \in (0, 1)$ such that

$$V(f(q, u, y)) - V(q) \le \gamma \rho(u) - d(q)$$
(15)

holds for all $q \in \mathcal{Q}$, $u \in \mathcal{U}$ and $y \in \mathcal{Y}$, where $d : \mathcal{Q} \to \{0, 1\}$ defined by:

$$d(q) = \begin{cases} 0 & \text{if } q = \mathcal{P}_1(q) \text{ or } q = \mathcal{P}_{-1}(q) \\ 1 & \text{otherwise} \end{cases}$$

then the error system Δ with \hat{M} is initialized to q_o satisfies (8) for that choice of γ .

Proof: By summing up (15) along any state trajectory of M from t = 0 to t = T, we get:

$$\sum_{t=0}^{T} \gamma \rho(u(t)) - d(q(t)) \geq V(q(T)) - V(q(0))$$

$$\geq \min_{q_1, q_2} \left(V(q_1) - V(q_2) \right)$$

Hence, we have:

$$\inf_{T \ge 0} \sum_{t=0}^{T} \gamma \rho(u(t)) - d(q(t)) \ge \min_{q_1, q_2} \left(V(q_1) - V(q_2) \right) > -\infty$$

It follows from Lemma 1 that when \hat{M} is initialized to $q(0) = q_o$, we have $\theta(t) \in q(t)$ for all t, where θ and q are the states of P and \hat{M} , respectively. Thus, when $q(t) = \mathcal{P}_1(q(t))$ or $q(t) = \mathcal{P}_{-1}(q(t)), y(t) = \hat{y}(t)$ and w(t) = 0. Otherwise $w(t) \in \{0, 1\}$. Hence $w(t) \leq d(q(t))$ for all t and all feasible input/output signal pairs of Δ satisfy (8).

An upper bound for the gain of Δ can thus be computed by solving a linear program in which we minimize γ such that (15) holds for all $q \in Q$, $u \in U$ and $y \in Y$. This linear program has N + 1 decision variables and 4N inequality constraints, where N = card(Q).

Remark 5: Recall that the approximation error Δ is a *complex* system with both continuous and discrete states. Thus the appeal of this approach to computing a gain bound for Δ is its simplicity and its computational efficiency. The downside of this approach is that it results in conservative gain bounds, for two reasons:

- 1) It inherently assumes that an error occurs every single time it can.
- 2) It assumes that all signal pairs $(\mathbf{u}, \mathbf{y}) \in \mathcal{U}^{\mathbb{Z}_+} \times \mathcal{Y}^{\mathbb{Z}_+}$ are valid input sequences for \hat{M} , which is not the case since \mathbf{y} is an output of P corresponding to \mathbf{u} .

Thus, in using this approach, we are in effect trading off simplicity and efficiency versus conservatism.

IV. CONTROLLER DESIGN

A. Two Related Synthesis Problems

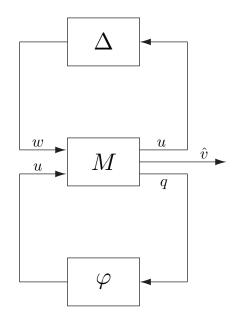


Fig. 5. Robust full state feedback control design setup

Consider the following two controller synthesis problems:

Problem 1: Given a plant P as in (1), (2) and (3), design a controller $K \subset \mathcal{Y}^{\mathbb{Z}_+} \times \mathcal{U}^{\mathbb{Z}_+}$ such that the feedback interconnection (P, K) shown in Figure 1 satisfies the performance objective

$$\sup_{T \ge 0} \sum_{t=0}^{T} \left(v(t) + R \right) < \infty \tag{4}$$

for some R > 0, for any initial condition of P.

Problem 2: Given a plant P as in (1), (2) and (3) and a finite state approximation M of P constructed as described in Section III-B with the corresponding error system satisfying gain condition (8) for some $\gamma \in (0, 1)$. Design a full state feedback control law $\varphi : \mathcal{Q} \to \mathcal{U}$ such that the interconnection (M, Δ, φ) shown in Figure 5 satisfies the auxiliary robust performance objective

$$\sup_{T \ge 0} \sum_{t=0}^{T} \left(\hat{v}(t) + R \right) < \infty \tag{9}$$

for some R > 0, for all systems Δ satisfying gain condition (8).

Problem 1 is the original problem of interest. Satisfaction of performance objective (4) guarantees that the state of the closed loop system globally exponentially converges to the

origin, on average, at a rate not less than R. To see that, note that this performance objective can be equivalently re-written as

$$\|x(t)\| \le k(x(0))10^{-Rt} \|x(0)\|, \ \forall t \ge 0, x(0) \in \mathbb{R}^2$$

where $k(x(0)) = 10^{S(x(0))-R}$ with

$$S(x(0) = \sup_{T>0} \sum_{t=0}^{T} (v(t) + R)$$

when the plant is initialized to x(0). The largest value of R for which (9) holds is then the guaranteed rate of exponential⁵ convergence; the actual rate of convergence may be (significantly) better.

Problem 2 is of interest as its solution provides a solution for Problem 1. In particular, let φ be a full state feedback controller such that (M, Δ, φ) satisfies (9) for some R > 0, for all admissible Δ . Recall that by construction, the outputs \hat{v} of (M, Δ) and v of P satisfy (7) whenever the same input $\mathbf{u} \in \mathcal{U}^{\mathbb{Z}_+}$ drives the plant P and the interconnection (M, Δ) . Thus, to ensure that (4) holds for interconnection (P, K) whenever (9) holds, it is sufficient to ensure that the controller K connected in feedback with plant P is identical to the subsystem with input y and output u in the interconnection (M, Δ, φ) . The structure of the resulting controller K is shown in Figure 6: K thus consists of \hat{M} , a deterministic finite state machine "observer" for the plant and φ , a corresponding full state feedback control law.

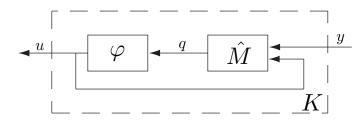


Fig. 6. The finite state stabilizing controller K

B. Design of a Full State Feedback Robust Switching Law

The following problem is addressed in this section: Given a deterministic finite state machine $M \subset (\mathcal{W}^{\mathbb{Z}_+} \times \mathcal{U}^{\mathbb{Z}_+}) \times (\mathcal{U}^{\mathbb{Z}_+} \times \mathcal{V}^{\mathbb{Z}_+} \times \mathcal{Y}^{\mathbb{Z}_+})$ with state set \mathcal{Q} , a scalar $\gamma_o \in (0, 1)$, and an uncertainty class

$$\boldsymbol{\Delta}_{\gamma_o} = \Big\{ \Delta \subset \mathcal{U}^{\mathbb{Z}_+} \times \mathcal{W}^{\mathbb{Z}_+} \Big| \Delta \text{ satisfies (8) for } \gamma = \gamma_o \Big\}.$$

Design a robust switching law $\varphi : \mathcal{Q} \to \mathcal{U}$ such that the closed loop system (M, Δ, φ) shown in Figure 5 satisfies the auxiliary robust performance objective

$$\sup_{T \ge 0} \sum_{t=0}^{T} \left(\hat{v}(t) + R \right) < \infty \tag{9}$$

for some R > 0, for all admissible uncertainty $\Delta \in \Delta_{\gamma_o}$.

⁵The notion of exponential convergence considered here is slightly different than that of global exponential convergence defined in standard textbooks such as [14], [33]; in particular, we are not requiring the term k(x(0)) to be uniformly bounded.

The robust switching law φ will be designed using dynamic programming techniques and a "small gain" argument. Consider the feedback interconnection of two systems S and Δ as in Figure 7. The 'Small Gain Theorem' (Theorem 1) and Corollary 1, presented here without proof, are directly adapted from Theorem 1 and Remark 4, respectively, in [31].

Theorem 1: (A 'Small Gain' Theorem) Suppose that S satisfies

$$\inf_{T \ge 0} \sum_{t=0}^{T} \rho_S(r(t), w(t)) - \mu_S(\hat{v}(t), u(t)) > -\infty$$
(16)

for some $\rho_S : \mathcal{R} \times \mathcal{W} \to \mathbb{R}$ and $\mu_S : \hat{\mathcal{V}} \times \mathcal{U} \to \mathbb{R}$, where $\mathcal{R}, \mathcal{W}, \hat{\mathcal{V}}$ and \mathcal{U} are finite sets, and that Δ satisfies

$$\inf_{T \ge 0} \sum_{t=0}^{T} \rho_{\Delta}(u(t)) - \mu_{\Delta}(w(t)) > -\infty$$
(17)

for some $\rho_{\Delta} : \mathcal{U} \to \mathbb{R}$ and $\mu_{\Delta} : \mathcal{W} \to \mathbb{R}$. Then the interconnected system (S, Δ) with input r and output \hat{v} satisfies

$$\inf_{T \ge 0} \sum_{t=0}^{T} \rho(r(t)) - \mu(\hat{v}(t)) > -\infty$$
(18)

for $\rho : \mathcal{R} \to \mathbb{R}$ and $\mu : \hat{\mathcal{V}} \to \mathbb{R}$ defined by

$$\rho(r) = \max_{w \in \mathcal{W}} \{\rho_S(r, w) - \mu_\Delta(w)\}$$

$$\mu(\hat{v}) = \min_{u \in \mathcal{U}} \{\mu_S(\hat{v}, u) - \rho_\Delta(u)\}$$

$$w(t) \in \mathcal{W}$$

$$u(t) \in \mathcal{U}$$

$$r(t) \in \mathcal{R}$$

$$S$$

$$\hat{v}(t) \in \mathcal{V}$$

Fig. 7. Setup for the 'Small Gain' Theorem.

Corollary 1: The interconnected system satisfies (18) for $\rho : \mathcal{R} \to \mathbb{R}$ and $\mu : \hat{\mathcal{V}} \to \mathbb{R}$ defined by

$$\rho(r) = \max_{w \in \mathcal{W}} \{ \rho_S(r, w) - \tau \mu_\Delta(w) \}$$
$$\mu(\hat{v}) = \min_{u \in \mathcal{U}} \{ \mu_S(\hat{v}, u) - \tau \rho_\Delta(u) \}$$

for any scalar parameter $\tau > 0$.

It follows from Theorem 1 and Corollary 1 that the objective in (9) can be achieved by designing a switching law $\varphi : \mathcal{Q} \to \mathcal{U}$ such that the feedback interconnection $S = (M, \varphi)$ satisfies (16) with

$$\rho_S(r,w) = \tau \mu(w) - R,$$

$$\mu_S(\hat{v},u) = \hat{v} + \tau \gamma_o \rho(u)$$
(19)

With the auxiliary robust performance objective reformulated as a design objective for system $S = (M, \varphi)$, the design of the full state feedback switching law can be carried out by solving a min-max optimization problem using standard dynamic programming techniques [3], [5]. The following theorem precisely formulates this approach: In this setting, function J and operator \mathbb{T} play the role of the "cost-to-go" function and the "dynamic programming" operator, respectively. Value iteration is used to solve for the cost-to-go function, and the sought full state feedback switching law is then simply the optimizing argument. For the sake of completeness, a proof is outlined in the Appendix.

Theorem 2: Consider a deterministic finite state machine M with state transition equation

$$q(t+1) = f(q(t), u(t), w(t))$$

where Q is the state set and U and W are the input sets. Let $\sigma : Q \times U \times W \to \mathbb{R}$ be a given function. The following three statements are equivalent:

(a) There exists a $\varphi : \mathcal{Q} \to \mathcal{U}$ such that the closed loop system (M, φ) satisfies

$$\inf_{T \ge 0} \sum_{t=0}^{T} \sigma(q(t), \varphi(q(t)), w(t)) > -\infty.$$

$$(20)$$

(b) There exists a function $J: \mathcal{Q} \to \mathbb{R}_+$ such that the inequality

$$J(q) \ge \mathbb{T}(J(q)) \tag{21}$$

holds for any $q \in \mathcal{Q}$, for $\mathbb{T} : \mathbb{R}^{\mathcal{Q}} \to \mathbb{R}^{\mathcal{Q}}$ defined by

$$\mathbb{I}(J(q)) = \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{-\sigma(q, u, w) + J(f(q, u, w))\}.$$
(22)

(c) The sequence of functions $J_k : \mathcal{Q} \to \mathbb{R}, k \in \mathbb{Z}_+$, defined recursively by

$$J_{0} = 0$$

$$J_{k+1} = \max\{0, \mathbb{T}(J_{k})\}$$
(23)

converges.

For the particular control problem of interest, the cost function

$$\sigma(q, u, w) = \tau \mu(w) - R - \hat{v}(q, u) - \tau \gamma_o \rho(u)$$

involves two parameters, the convergence rate R and the small gain scaling parameter τ . Ultimately, the goal is to maximize R > 0 for which there exists a $J : \mathcal{Q} \to \mathbb{R}$ and a $\tau > 0$ such that (21) holds. However, since it is not possible to directly compute the optimal value of R, a numerical search is carried out yielding a suboptimal value of R: First, a range of values of τ for which the design objective can be met when R = 0 is computed. Different values of τ are then sampled in this range to compute the largest feasible corresponding value of R, with the largest of those being the (suboptimal) guaranteed rate of convergence.

V. ILLUSTRATIVE EXAMPLES

An academic example is presented in this section to illustrate the proposed design procedure. The example was chosen for its simplicity and amenability to analysis in the ideal (full state feedback) case, thus allowing us to numerically compare the performance of our finite state controllers in the binary sensing setting to the best achievable performance in the ideal setting for a given pair of plants.

Consider a harmonic oscillator

$$\begin{array}{rcl} \dot{x}_1 &=& x_2 \\ \dot{x}_2 &=& k x_1 \end{array}$$

described in polar coordinates by

$$\dot{r} = (1+k)r\sin(\theta)\cos(\theta)$$

$$\dot{\theta} = (1+k)\cos^2(\theta) - 1$$

When k = -1, $\dot{r} = 0$ and the state trajectories are concentric circles centered at the origin. For any $k \neq -1$, $\dot{r} < 0$ in exactly two sectors of the state space given by

$$\theta \in \left\{ \begin{array}{ll} (0, \pi/2) \cup (\pi, 3\pi/2) & \text{ when } k < -1 \\ (\pi/2, \pi) \cup (3\pi/2, 2\pi) & \text{ when } k > -1 \end{array} \right.$$

Thus in the ideal case where the switching controller has full access to the state and switching can occur at any time, it is always possible to stabilize a pair of harmonic oscillators with k = -1 and $k \in \mathbb{R} \setminus \{-1\}$ by appropriately switching between them.

Now suppose that the system is sampled and the only sensor information available for use by the controller is the sign of the position measurement. The stabilization problem becomes much more difficult because of the non-trivial state estimation problem that arises due to binary sensing and the time delays introduced by sampling. The approach described in this paper will be used to design a finite state stabilizing controller.

The dynamics of the system, sampled at times tT_S , $t \in \mathbb{Z}_+$, for a given sampling period T_S , are

$$\begin{aligned} x(t+1) &= A_S(u(t))x(t) \\ y(t) &= sgn(x_1(t)) \\ v(t) &= log\Big(\frac{\|x(t+1)\|_2}{\|x(t)\|_2}\Big) \end{aligned}$$

where $A_S(0)$, $A_S(1)$ are given by $A_S(u) = e^{A(u)T_S}$ with

$$A(0) = \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix},$$
$$A(1) = \begin{bmatrix} 0 & 1 \\ k_o & 0 \end{bmatrix}, \ k_o \in \mathbb{R} \setminus \{-1\}.$$

In this setting, applying inputs u = 0 and u = 1 effectively corresponds to switching between "passive" and "aggressive" control modes, respectively. The sensor measurement y is assumed to be available at the beginning of each sampling interval $[tT_S, (t+1)T_S)$ at which point a plant (or equivalently, a value of k) is chosen by the controller K and held until the end of the sampling interval. The sampling interval T_S is assumed to be a design parameter in this case. In order to counteract some of the conservatism introduced in the computation of an

	EXPLANATION OF REPORTED PARAMETERS.
n	2n is the total number of intervals partitioning the unit circle
N	Actual number of states of the deterministic finite state machine approximation $(N \le 2n(2n-1)+1)$
T_S	Sampling interval
γ	Tightest verifiable gain bound for the approximation error
R	Guaranteed rate of exponential convergence of the sampled system
p	Number of iterations required for convergence of the Value Iteration algorithm

TABLE I

n	N	T_S	γ	R	p
5	39	0.6283	0.75	0.0160	10
10	107	0.3142	0.625	0.0267	20
15	207	0.2094	0.5833	0.0195	28
20	331	0.1571	0.5625	0.0152	39

TABLE II

DATA FOR EXAMPLE 1.

upper bound for the gain of Δ , the unit circle is uniformly partitioned into 2n intervals and the sampling rate is matched to this partition, that is $T_S = \frac{\pi}{n}$. This ensures that a partition interval maps to another partition interval in the "passive" control mode.

Example 1: In particular, consider the case where $k_o = -3$. The coarsest partition of the unit circle that gives rise to a good enough approximate model allowing for successful control design corresponds to n = 5. The relevant data for several choices of n are shown in Table II, and the implementation of the resulting controllers is plotted in Figure 8, together with the corresponding plot in the "ideal" (full state feedback, unsampled) setting for comparison. Table I explains the significance of the various parameters reported for the examples. In this case, refining the partition by increasing n improves both the provable gain bound for Δ and the rate of convergence up to a certain point. The improvement seems to taper off beyond n = 15, likely due to numerical errors. Note that while R decreases for the cases where n = 15 and n = 20, the actual rate of convergence still improves as seen in the simulations, thus reinforcing the fact that the actual rate of convergence may be significantly better than the provable rate.

Example 2: Now consider the case where $k_o = 2$. In this case, the coarsest workable partition of the unit circle giving rise to a good enough approximate model allowing for successful control design corresponds to n = 6. The relevant data for several choices of n are shown in Table III, and the implementation of the resulting controllers is plotted in Figure 9, together with the corresponding plot in the "ideal" setting for comparison. Note in this case that even though the best provable gain bound for the error system Δ remains unchanged between n = 6 and n = 7, the difference between \hat{v} and v decreases as the unit circle partition is refined, hence the quality of approximation effectively improves as does the guaranteed rate of convergence. Also note that in this case the improvement stalls much sooner, around n = 8. This is likely due to the fact that the "aggressive" control law is much more aggressive than in the previous example, and hence numerical errors affect the computation sooner.

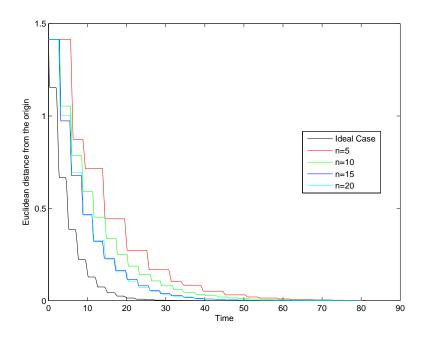


Fig. 8. Implementation of the DFM controller in Example 1

TABLE IIIDATA FOR EXAMPLE 2.

n	N	T_S	γ	R	p
6	39	0.5236	1	0.0141	11
7	55	0.4488	1	0.0387	13
8	69	0.3927	1	0.0279	17

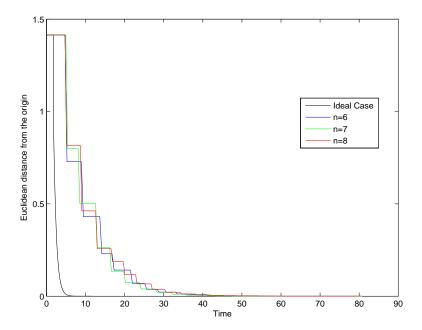


Fig. 9. Implementation of the DFM controller in Example 2.

VI. CONCLUSIONS AND FUTURE WORK

A new constructive approach for designing switching controllers to stabilize pairs of discretetime homogenous second order systems under limited information (due to binary sensors) was presented. The hybrid plant is first approximated by a deterministic finite state machine, and a useable description of the resulting approximation error, in the form of a "gain" bound, is established. Using a "small gain" argument and dynamic programming techniques, a controller is then synthesized to robustly stabilize the nominal (approximate) model in the presence of admissible (approximation) uncertainty. The resulting stabilizing controller then consists of a deterministic finite state machine observer for the hybrid plant and a corresponding full state feedback switching law. While the proposed approach is inspired from classical robust control, the use of deterministic finite state machines as an alternative class of nominal models necessitates the development of a new set of complementary approximation, analysis and control tools.

Future work will focus on three main directions:

- 1) Reducing the conservatism of the approach: As mentioned in the paper, the proposed approach for computing gain bounds for the approximation uncertainty Δ is efficient (polynomial in the size of the nominal models) but conservative. One direction of future effort will thus focus on developing alternative tractable approaches for verifying tighter gain bounds, allowing for the design of stabilizing controllers with better performance.
- 2) Extending the approach to broader problems: Another direction of future work will look into extending the proposed analysis, approximation and synthesis tools to broader classes of plants beyond homogeneous systems, in addition to identifying alternative performance objectives (beyond stability) that can be captured by "gain conditions".
- 3) Addressing the issue of scalability: While the design approach presented here can, in principle, be applied to systems of arbitrary order, it scales poorly (exponentially) with plant order, as do most existing hybrid design techniques. Thus one important direction of future work is finding ways of improving the scalability of the approach, potentially by identifying and exploiting system structure.

VII. ACKNOWLEDGMENTS

The first author was supported by AFOSR Grant FA9550-04-1-0052 while she was at MIT and by startup funds from the Whiting School of Engineering while at The Johns Hopkins University.

APPENDIX I

PROOF OF THEOREM 2

Several statements will be useful in proving Theorem 2. The first one, Theorem 3, is adapted from Theorem 3 in [31] and is presented here without proof.

Theorem 3: Consider a deterministic finite state machine M with state transition equation

$$q(t+1) = f(q(t), u(t), w(t))$$

where Q is the state set and U and W are the input sets. Let $\sigma : Q \times U \times W \to \mathbb{R}$ be a given function. The following two statements are equivalent:

(a) The inequality

$$\inf_{T\geq 0}\sum_{t=0}^T\sigma(q(t),u(t),w(t))>-\infty$$

is satisfied for any $(\mathbf{u}, \mathbf{w}) \in \mathcal{U}^{\mathbb{Z}_+} \times \mathcal{W}^{\mathbb{Z}_+}$ and $q(0) \in \mathcal{Q}$.

(b) There exists a non-negative function $J: \mathcal{Q} \to \mathbb{R}_+$ such that the inequality

$$J(f(q, u, w)) - J(q) \le \sigma(q, u, w)$$

holds for all $q \in \mathcal{Q}$, $u \in \mathcal{U}$ and $w \in \mathcal{W}$.

In the terminology of Willem's theory of dissipative systems [35], J in Theorem 3 is the storage function of the dissipative system with supply rate σ .

Lemma 3: Function \mathbb{T} defined in (22) is monotonic, that is

$$J_1 \leq J_2 \Rightarrow \mathbb{T}(J_1) \leq \mathbb{T}(J_2).$$

Proof: When $J_1 \leq J_2$, we have

$$J_{1}(q) \leq J_{2}(q), \ \forall q \in \mathcal{Q}$$

$$\Rightarrow \ \sigma(q, u, w) + J_{1}(f(q, u, w)) \leq \sigma(q, u, w) + J_{2}(f(q, u, w)), \forall q \in \mathcal{Q}, u \in \mathcal{U}, w \in \mathcal{W}$$

$$\Rightarrow \ \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + J_{1}(f(q, u, w))\} \leq \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + J_{2}(f(q, u, w))\}, \forall q \in \mathcal{Q}, u \in \mathcal{U}$$

$$\Rightarrow \ \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + J_{1}(f(q, u, w))\} \leq \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + J_{2}(f(q, u, w))\}, \forall q \in \mathcal{Q}$$

$$\Rightarrow \ \mathbb{T}(J_{1})(q) \leq \mathbb{T}(J_{2})(q), \forall q \in \mathcal{Q}$$

Thus $\mathbb{T}(J_1) \leq \mathbb{T}(J_2)$.

Lemma 4: The sequence $\{J_k\}$ defined in (23) is monotonically increasing.

Proof: The proof is by induction on k. We have

$$J_1 = \max\{0, \mathbb{T}(J_0)\} \ge 0 = J_0.$$

Now suppose $J_k \ge J_{k-1}$. Then

$$J_{k+1} = \max\{0, \mathbb{T}(J_k)\}$$

$$\geq \max\{0, \mathbb{T}(J_{k-1})\}$$

$$\geq J_k$$

which completes the proof.

Given a scalar $c \in \mathbb{R}$ and a function $J : \mathcal{Q} \to \mathbb{R}$, denote by J + c the function from \mathcal{Q} to \mathbb{R} defined by (J + c)(q) = J(q) + c.

Lemma 5: For any $J : \mathcal{Q} \to \mathbb{R}$ and $c \in \mathbb{R}$, $\mathbb{T}(J + c) = \mathbb{T}(J) + c$. *Proof:* For any $q \in \mathcal{Q}$ we have

$$\begin{aligned} \mathbb{T}((J+c)(q)) &= \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + (J+c)(f(q, u, w))\} \\ &= \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + J(f(q, u, w)) + c\} \\ &= \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{\sigma(q, u, w) + J(f(q, u, w))\} + c \\ &= \mathbb{T}(J(q)) + c \end{aligned}$$

We are now ready to prove Theorem 2.

Proof: (*Theorem 2*)

(b) \Rightarrow (a): Suppose there exists a function $J : \mathcal{Q} \to \mathbb{R}$ satisfying (21), and let

$$\varphi(q) = \arg\min_{u \in \mathcal{U}} \left\{ \max_{w \in \mathcal{W}} \left(-\sigma(q, u, w) + J(f(q, u, w)) \right) \right\}.$$

For any $q \in \mathcal{Q}$, we have

$$J(q) \geq \mathbb{T}(J(q))$$

= $\min_{u} \max_{w \in \mathcal{W}} \{-\sigma(q, u, w) + J(f(q, u, w))\}$
= $\max_{w \in \mathcal{W}} \{-\sigma(q, \varphi(q), w) + J(f(q, \varphi(q), w))\}$
 $\geq -\sigma(q, \varphi(q), w) + J(f(q, \varphi(q), w)), \forall w \in \mathcal{W}$

It follows from Theorem 3 that the (deterministic finite state machine) closed loop system (M, φ) satisfies (20).

(a) \Rightarrow (c): Suppose there exists a φ such that the closed loop system satisfies (20). By Theorem 3, there exists a $J : \mathcal{Q} \to \mathbb{R}_+$ such that

$$J(f(q,\varphi(q),w)) - J(q) \le \sigma(q,\varphi(q),w), \forall q \in \mathcal{Q}, w \in \mathcal{W}$$

We then have

$$\begin{split} J(q) &\geq -\sigma(q,\varphi(q),w) + J(f(q,\varphi(q),w)), \forall q \in \mathcal{Q}, w \in \mathcal{W} \\ &\geq \max_{w \in \mathcal{W}} \{-\sigma(q,\varphi(q),w) + J(f(q,\varphi(q),w))\}, \forall q \in \mathcal{Q} \\ &= \min_{u \in \mathcal{U}} \max_{w \in \mathcal{W}} \{-\sigma(q,u,w) + J(f(q,u,w))\}, \forall q \in \mathcal{Q} \\ &= \mathbb{T}(J(q)), \forall q \in \mathcal{Q} \end{split}$$

It follows that function J + c also satisfies

$$(J+c) \ge \mathbb{T}((J+c))$$

for any choice of $c \in \mathbb{R}$. Since the set Q is finite, we can assume, without loss of generality, that $J \ge 0$ with $\min_{q} J(q) = 0$ and $\max_{q} J(q) = m \ge 0$. Moreover, the sequence $\{J_k\}$ defined in (23) is bounded above by J. The proof is by

induction on k. We have

$$J_0 = 0 \le J$$

Suppose that $J_k \leq J$. Then

 $\mathbb{T}(J_k) < \mathbb{T}(J) < J$

where the first inequality follows from Lemma 3 and

$$J_{k+1} = \max\{0, \mathbb{T}(J_k)\} \le J.$$

Sequence $\{J_k\}$ is thus monotonically increasing (Lemma 4) and bounded above by J. Hence it converges to $J^* = \lim_{k \to \infty} J_k \le J$. (c) \Rightarrow (b): Suppose that the sequence $\{J_k\}$ converges to $J^* = \lim_{k \to \infty} J_k$ and let

$$\epsilon_k = \max_{q \in \mathcal{Q}} \{ J^*(q) - J_k(q) \}$$

Note that $\epsilon_k \ge 0$, the sequence $\{\epsilon_k\}$ is monotonically decreasing (follows from Lemma 4) and $\lim \epsilon_k = 0$. Moreover $k{
ightarrow}\infty$

$$J_k(q) \ge J^*(q) - \epsilon_k, \forall q \in \mathcal{Q}.$$

It follows from Lemmas 3 and 5 that

$$\mathbb{T}(J_k) \ge \mathbb{T}(J^* - \epsilon_k) = \mathbb{T}(J^*) - \epsilon_k.$$

But we have

$$J_{k+1} = \max \{0, \mathbb{T}(J_k)\}$$

$$\geq \mathbb{T}(J_k)$$

$$\geq \mathbb{T}(J^*) - \epsilon_k.$$

Thus

$$J^* = \lim_{k \to \infty} J_{k+1} \ge \mathbb{T}(J^*) - \lim_{k \to \infty} \epsilon_k = \mathbb{T}(J^*).$$

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