

Editorial: A Message from the Incoming Editor-in-Chief

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I am greatly honored to be given the opportunity to serve for the next two years as the Editor-in-Chief (EiC) of the *IEEE Transactions on Cloud Computing (TCC)*, the premier journal in this field. I thank the *TCC* EiC Search Committee and *TCC*'s Steering Committee for their confidence in me and for giving me this opportunity. As a brief introduction, I am a Distinguished Professor of Electrical and Computer Engineering at Stony Brook University, and have a rich service record to the IEEE and the leading conferences and journals. More information about me can be found at <http://www.ece.sunysb.edu/~yang/>. As the incoming EiC, I would like to recognize and thank Hui Lei for his tireless work and great leadership for elevating the stature of *TCC* and broadening its dissemination, and the devoted efforts of the *TCC* editorial board. I am inheriting a very healthy and impactful publication. I hope to follow my predecessors' footsteps and learn from their experiences to continue the great success of the journal. My agenda as EiC includes, among other things, improving the quality of accepted papers, reducing the turnaround time for reviews without sacrificing review quality, further increasing the impact of the journal, identifying area focus of the journal and increasing the scope of readership and authorship to ensure that *TCC* is the premier Transactions in the field.

Next, I would like to take this opportunity to introduce the following new members of our editorial board:

Douglas M Blough, Georgia Institute of Technology
 Wei Cai, The Chinese University of Hong Kong, Shenzhen
 Vipin Chaudhary, University at Buffalo
 Ahmed Louri, George Washington University
 Jianping Wang, City University of Hong Kong
 Xin Yuan, Florida State University

These outstanding researchers (their bios and photos are shown below) agreed to serve as Associate Editors, filling gaps left by several AEs whose terms recently expired. I thank them for taking time from their busy schedules to volunteer and serve our research community. On this occasion, I also would like to express my gratitude to several retiring members of the editorial board: Xiaoyang Sean Wang, Fred Douglass, Beng Chin Ooi, Leandro Navarro, Volislav Mistic, Mohan Gurusamy, Rajiv Ranjan, Bruno Schulze and Carlos Varela. Their service and dedication to our research community are truly remarkable and highly appreciated.

I look forward to working with our research community, including authors, reviewers, editors, and publication staff, to take the journal to a new level of success! I hope the authors will continue to submit their best papers to *TCC*!

YuanYuan Yang
Editor-in-Chief



Douglas M. Blough received the BS degree in electrical engineering and the MS, and PhD degrees in computer science from the Johns Hopkins University, Baltimore, MD, in 1984, 1986, and 1988, respectively. Since Fall 1999, he has been the professor of electrical and computer engineering at the Georgia Institute of Technology, where he also holds a joint appointment in the School of Computer Science. From 1988 to 1999, he was on the faculty of Electrical and Computer Engineering at the University of California, Irvine. He has been general chair for the International Conference on Dependable Systems and Networks (DSN, in 2014), area chair for INFOCOM (2019 and 2020), and program chair for DSN (2000). He is a past chair of the IEEE Technical Committee on Dependable Computing and Fault Tolerance (TCFT) and serves on the Steering Committee of DSN, which is the flagship conference of TCFT. He served as associate editor for *IEEE Transactions on Computers* (1995 through 2000), *IEEE Transactions on Parallel and Distributed Systems* (2001 through 2005), *IEEE Transactions on Mobile Computing* (2008 through 2013), and *IEEE Transactions on Dependable and Secure Computing* (2015 through 2018). His research interests include wireless networks and dependable distributed systems.



Wei Cai (Member, IEEE) received the BEng degree in software engineering from Xiamen University, China, in 2008, the MS degree in electrical engineering and computer science from Seoul National University, South Korea, in 2011, and the PhD degree in electrical and computer engineering from the University of British Columbia (UBC), Vancouver, Canada, in 2016. From 2016 to 2018, he was a post-doctoral research fellow with UBC. He joined the School of Science and Engineering, the Chinese University of Hong Kong, Shenzhen, in 2018, where he is currently an assistant professor. He has completed visiting research at the National Institute of Informatics, Japan, the Hong Kong Polytechnic University, and Academia Sinica, Taiwan. His recent research interests include cloud computing, edge computing, blockchain systems, and interactive multimedia. He was a recipient of the 2015 Chinese Government Award for the Outstanding Self-Financed Students Abroad, the UBC Doctoral Four-Year-Fellowship from 2011 to 2015, and the Brain Korea 21 Scholarship. He has also received the Best Student Paper Award from ACM BSCI 2019, and Best Paper Awards from CCF CBC 2018, IEEE CloudCom2014, IEEE SmartComp2014, and IEEE CloudComp2013.



Vipin Chaudhary received the BTech (hons.) degree in computer science and engineering from the Indian Institute of Technology, Kharagpur, in 1986, and the PhD degree from the University of Texas at Austin, in 1992. He is a veteran of High Performance Computing (HPC), he has been actively participating in the science, business, government, and technology innovation frontiers of HPC for almost three decades. His contributions range from heading research laboratories and holding executive management positions, to starting new technology ventures. He is currently a program director in the Office of Advance Cyberinfrastructure at National Science Foundation which gives him a unique opportunity to work across all disciplines of science and engineering. He co-leads (or has co-lead) the National Strategic Computing Initiative from NSF for the United States and is in the working group of the Quantum Leap Initiative, National Quantum Initiative, National Artificial Intelligence Research Institutes, Cyber, and the I-Corps Program (where he was also a Program Director). I-Corps program is now part of "The American Innovation and Competitiveness Act" that enables commercialization of research and venture startups.

He co-chairs the Networking and Information Technology Research and Technology Program's Middleware and Grid Inter-agency Coordination (MAGIC) Team for United States. He is also in the working group of the US Interagency Modeling and Analysis Group and a member of the Advanced Computing Roundtable of the Council on Competitiveness. He is the empire innovation professor of computer science and engineering at the Center for Computational Research at the New York State Center of Excellence in Bioinformatics and Life Sciences at SUNY Buffalo, and the director of the university's Data Intensive Computing Initiative. He is also the co-founder of the Center for Computational and Data-Enabled Science and Engineering. He cofounded Scalable Informatics, a leading provider of pragmatic, high performance software-defined storage and compute solutions to a wide range of markets, from financial and scientific computing to research and big data analytics. From 2010 to 2013, he was the chief executive officer of Computational Research Laboratories (CRL) where he grew the company globally to be an HPC cloud and solutions leader before selling it to Tata Consulting Services. Prior to this, as senior director of Advanced Development at Cradle Technologies, Inc., he was responsible for advanced programming tools for multi-processor chips. He was also the chief architect at Corio Inc., a company that was the leader in Software-as-a-Service Cloud and had a successful IPO in July, 2000. He was awarded the Prestigious President of India Gold Medal, in 1986 for securing the first rank amongst graduating students at the Indian Institute of Technology (IIT).



Ahmed Louri (Fellow, IEEE) received the PhD degree in computer engineering from the University of Southern California, Los Angeles, California, in 1988. He is the David and Marilyn Karlgaard Endowed chair professor of electrical and computer engineering at the George Washington University. He is also the director of the High Performance Computing Architectures and Technologies Laboratory. From 1988 to 2015, he was a professor of electrical and computer engineering at the University of Arizona, and during that time, he has served six years (2000 to 2006) as the chair of the Computer Engineering Program. From 2010–2013, he served as a program director in the National Science Foundation's (NSF) Directorate for Computer and Information Science and Engineering. He conducts research in the broad area of computer architecture and parallel computing, with emphasis on interconnection networks, optical interconnects for scalable parallel computing systems, reconfigurable computing systems, and power-efficient and reliable Network-on-Chips (NoCs) for multicore architectures. Recently he has been concentrating on: energy-efficient, reliable, and high-performance many-core architectures; accelerator-rich reconfigurable heterogeneous architectures; machine learning techniques for efficient computing, memory, and interconnect systems;

emerging interconnect technologies (photonic, wireless, RF, hybrid) for NoCs; future parallel computing models and architectures (including convolutional neural networks, deep neural networks, and approximate computing); and cloud-computing and data centers. He has published more than 160 refereed journal articles and peer-reviewed conference papers, and is the co-inventor on several US and international patents. He is the editor-in-chief of the *IEEE Transactions on Computers* (2019–present). He is also currently serving as associate editor for the *IEEE Transaction on Sustainable Computing* (2016–present). He previously served on the editorial boards for *IEEE Transactions on Computers* (2011–2016), and *Cluster Computing*, the *Journal of Networks*, *Software Tools and Applications* (2000–2010). Since January 2016, he has served on the steering committee for *IEEE Transactions on Sustainable Computing*. He also served as guest editor for special issues in the *Journal of Parallel and Distributed Computing* (2010) and *IEEE Transactions on Emerging Technologies for Computing* (2015). His recent IEEE CS committee service includes being the 2019 chair for the IEEE CS Fellow Evaluation Committee, vice-chair for the IEEE CS Fellow Evaluation Committee (2013, 2017, and 2018), member of IEEE CS Computer Entrepreneur Award Committee (2017), and an evaluator on the IEEE CS Fellow Evaluation Committee (2012).



Jianping Wang is a professor in the department of Computer Science at the City University of Hong Kong, with concurrent appointment as assistant provost (Strategic Planning). Her research interests include cloud computing, edge computing, autonomous driving, network coding, and networking. She has published more than 200 papers where most published in IEEE transactions and IEEE flagship conferences. She served as an associate editor of *IEEE Transactions on Mobile Computing* (2012–2018), *Optical Switching and Networking* (2013–present), *Wiley's Security and Communication Networks Journal* (2012–2016), and *IEEE Communication Letters* (2012–2013). She was the TPC co-chair of IWQOS 2017 and is currently on the steering committee of IWQOS. She was the TPC Symposium co-chair of IEEE Globecom 2014 Optical Networks and Systems Symposium.



Xin Yuan (Senior Member, IEEE) received the BS and MS degrees in computer science from Shanghai Jiaotong University, in 1989 and 1992, respectively, and the PhD degree in computer science from the University of Pittsburgh, in 1998. He is a full professor and department chair of the Department of Computer Science at Florida State University. His research interests include high performance computing, cloud computing, parallel and distributed systems, and interconnection networks. He has published more than 100 papers in leading journals and conferences. The STAR-MPI software package that he and his students developed has been incorporated in the MPI stack of the IBM Blue Gene/P system. He has served as the program chairs and vice-chairs for several international conferences and workshops such as IEEE IPDPS, the International Conference on Parallel Processing (ICPP) and the IEEE International Conference on High Performance Computing (HiPC), and as program committee members for many international conferences and workshops. He is a senior member of ACM.