

Guest Editorial

Special Issue on the 2018 IEEE International Symposium on Circuits and Systems

THIS special issue of IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—PART II: EXPRESS BRIEFS (TCAS-II) represents a new co-publication initiative of the IEEE Circuits and Systems Society (CASS) to publish a selection of the best papers accepted for presentation at the IEEE International Symposium on Circuits and Systems (ISCAS), held in Florence, Italy, on May 27–30, 2018. As TCAS-II only publishes 5-page briefs, and hence both conference and journal (revised) versions of these works would largely overlap, the papers included in this issue that you are holding represent the only record published in IEEE Xplore and they will not appear in *Proceedings of IEEE International Symposium on Circuits and Systems (ISCAS)*, which will however contain DOI links to the corresponding TCAS-II papers. With this initiative, CASS joins other forward-looking IEEE Societies, namely, the IEEE Robotics and Automation Society and IEEE Control Systems Society, which have developed a similar process for presenting papers submitted to the IEEE Robotics and Automation Letters or to the IEEE Control Systems Letters at ICRA, CASE, or IROS and to CDC, respectively. In a world where the role of a conference is shifting more and more to a networking event and a scientific discussion opportunity, and in which the importance of conference publications is vanishing (except for some specific fields), we consider this opportunity an important step forward for the CASS community.

The selection process has been carried out as follows. First, a preliminary selection of 51 papers was made among the 1715 newly¹ submitted papers to ISCAS, and those selected papers were invited to submit revised versions to TCAS-II. This first selection was made on the basis of the timeliness and quality of technical results presented, the outcome of the review process and the recommendations of the Review Committee Members (RCMs) and Track Chairs (TCs) who were involved in the review process of ISCAS. Among those papers selected, a total of 46 papers accepted the invitation to submit revised versions of their manuscripts to TCAS-II, by taking into account all suggestions and recommendations given by ISCAS reviewers.

In a second step, revised manuscripts were submitted to TCAS-II for further review, involving both ISCAS reviewers and other highly qualified reviewers in

order to guarantee the highest quality of the papers finally accepted for publication at TCAS-II. This second review process was handled by the following Guest Editors (alphabetically sorted): Mrityunjoy Chakraborty, Meng-Fan Chang, Masud H. Chowdhury, Abdelali El Aroudi, Gabriele Manganaro, Mitra Mirhassani, Fabio Pareschi, and George Jie Yuan, who are leading experts in diverse research areas covering the whole spectrum of topics included in ISCAS.

The deadline for submitting revised papers was on March 2 and final decisions were taken on March 23. This means a really tight schedule, justified by the fact that these papers could be practically considered as manuscripts subject to minor revision, and prompted by the need to have all papers available during the conference, published either in TCAS-II or in *Proceedings of ISCAS*. As a result of the review process, 34 papers were finally accepted for publication in TCAS-II. They cover a wide variety of research areas and topics, including among others: analog and mixed-signal circuits, digital signal processing and integrated circuits, nonlinear circuits and systems, power management and energy harvesting, wireless communications, sensory systems, imagers and vision sensors, neural networks, and neuromorphic engineering and emerging technologies beyond CMOS. We hope readers find this selection of papers interesting and those who attend ISCAS can take the opportunity of this co-publication initiative to discuss with authors their works included in this special issue.

To conclude this guest editorial, we would like to express our deepest gratitude to all authors contributing to this special issue, for their excellent research and hard work to write their papers, and to address their revisions in a tight and stressful time window. We are also indebted to the hundreds of reviewers involved in both the conference and the journal review rounds, as well as the RCMs and TCs of ISCAS who helped to select high-quality papers. Last, but not least, we wish to thank the Editor-in-Chief of TCAS-II, Prof. C.K. Michael Tse, and our excellent Guest Editorial team for their enormous dedication, professionalism, and huge efforts to make this special issue a great success in a record time!

JOSÉ M. DE LA ROSA, *Deputy Editor-in-Chief*
Institute of Microelectronics of Seville
Seville, Spain

GIANLUCA SETTI, *General Co-Chair*
Politecnico di Torino
Turin, Italy
e-mail: gianluca.setti@polito.it

Digital Object Identifier 10.1109/TCSII.2018.2826298

¹Excluding submissions to Demo sessions and to CAS Transactions tracks aiming to allow authors of already published papers in CASS journals to present their work at ISCAS.