Soft-Bit Assisted Iterative AMR-WB Source-Decoding and Turbo-Detection of Channel-Coded Differential Space-Time Spreading Using Sphere Packing Modulation

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Abstract- Jointly optimised iterative source- and channel-decoding is used for enhancing the error resilience of the Adaptive Multi Rate WideBand (AMR-WB) speech codec. The resultant AMR-WB coded speech signal is protected by Recursive Systematic Convolutional (RSC) codes and transmitted using a Sphere Packing (SP) aided Differential Space-Time Spreading (DSTS) assisted transceiver. The performance of the proposed scheme is evaluated when communicating over correlated non-dispersive Rayleigh fading channels. The proposed system exhibits an E_b/N_0 gain of about 1 dB in comparison to the benchmark scheme carrying out joint channel decoding and DSTS aided SP-demodulation, but separate AMR-WB decoding, when using I=4 external iterations.

1. MOTIVATION AND BACKGROUND

The classic Shannonian source and channel coding separation theorem [1] has limited applicability in the context of finite-complexity, finite-delay lossy speech [2] and video [3] codecs, where the different encoded bits exhibit different error sensitivity. These arguments are particularly valid, when the limited-complexity, limited-delay source encoders fail to remove all the redundancy from the correlated speech or video source signal. Fortunately, this residual redundancy may be beneficially exploited for error protection by intelligently exchanging soft information amongst the various receiver components. More explicitly, it was explicitly demonstrated in [4] that the video performance of a twin-class protected MPEG4 video transceiver substantially benefitted from a multi-stage turbo detection process, which exchanged soft-information across three Soft-In-Soft-Out (SISO) blocks.

These powerful turbo principles may be further enchanced by exploiting the innovative concept of soft speech bits, which was developed by Vary and his team [5, 6], culminating in the formulation of iterative source and channel decoding (ISCD) [7]. More explicitly, in ISCD the source- and channel-decoder iteratively exchange *a priori* and *extrinsic* information for the sake of improving the overall system performance. As a further development, in [8] the employment of the turbo principle [9] for iterative soft demapping in multilevel modulation [10] schemes combined with channel coding demonstrated an enhanced bit error rate performance. Thus, ISCD combined with iterative soft demapping in the context of multilevel modulation and amalgamated with a number of other sophisticated wireless transceiver components may beneficially exploited. In the resultant

multi-stage scheme *extrinsic* information is exchanged amongst three receiver components, namely the demodulator, the channel decoder and the soft-input source decoder in the spirit of [11].

Explicitly, we proposed and investigate a jointly optimised ISCD scheme invoking the Adaptive Multirate-Wideband (AMR-WB) speech codec [12], which is protected by a Recursive Systematic Convolutional (RSC) code. The resultant bitstream is transmitted using Differential Space-Time Spreading (DSTS) and Sphere Packing (SP) modulation [13] over a correlated, non-dispersive Rayleigh fading channel. An efficient iterative turbo-detection scheme is utilised for exchanging *extrinsic* and *a priori* information between the constituent codes. In an effort to mitigate the effects of the hostile Rayleigh fading channel, DSTS employing two transmit and one receive antennas was invoked for sake of providing a spatial diversity gain. This powerful wireless transceiver is advocated, since it was demonstrated that the employment of SP modulation combined with the orthogonal transmit diversity designs of [14], outperformed the conventional orthogonal design based Space-Time Block Coding (STBC) schemes of [15, 16].

The paper is structured as follows. In Section 2, the AMR-WB speech codec is described briefly and the residual redundancy inherent in its parameters is quantified. In Section 3, the overall system model is described, while our simulation results are presented in Section 4. Our conclusions are offered in Section 5.

2. RESIDUAL REDUNDANCY IN THE AMR-WB SPEECH CODEC

The AMR-WB speech codec is capable of supporting nine different bit rates [17], each of which may be activated in conjunction with different-rate channel codecs and different-throughput adaptive modem modes [18]. Similar near-instantaneously adaptive speech and video systems were designed in [2, 3]. In our prototype system investigated here the AMR-WB codec operated at 15.85 kbit/s extracts a set of speech parameters per 20 ms frame for representing the speech signal. Each AMR-WB frame consists of 317 bits per 20 ms of speech frame. Similar to any other code excited linear prediction (CELP) based codec [2], it performs short-term linear prediction (LP), longterm prediction (LTP) and generates the excitation codebook parameters [12]. The resultant bit-allocation scheme is summarized in Table 1. The LP coefficients produced for the 20 ms speech frame are converted to the so-called immittance spectrum pair (ISP) representation [19] and they are vector-quantised using split-multistage vector quantisation (S-MSVQ) producing 7 ISP parameters. The LTP analysis [2] is performed for each new 5 ms subframe, which produces the LTP lag and gain parameters. The LTP lag is encoded using 9 bits in the first and third subframes, and differentially encoded with respect to the first and third subframes, using 6 bits in the second and fourth subframes, respectively. The LTP and fixed code-

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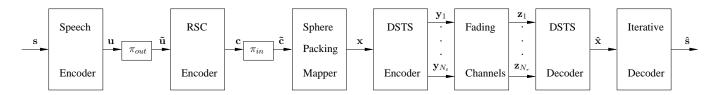


Figure 1: Block diagram of the DSTS-SP-RSC-AMRWB scheme. The notations \mathbf{s} , $\hat{\mathbf{s}}$, \mathbf{u} , \mathbf{c} , \mathbf{x} , \mathbf{y}_j , \mathbf{z}_k , π_{out} and π_{in} represent the frame of the speech source samples, the estimate of the speech source samples, the bit-representation of the encoded speech parameters, the encoded bits of the RSC encoder, the SP-coded symbols, the DSTS coded symbols of transmitter j, the received symbols at receiver k, the outer bit interleaver, and the inner bit interleaver, respectively. Furthermore, N_t and N_r denote the number of transmitters and receivers, respectively. The iterative decoder seen at the right is detailed in Figure 2.

Parameter	1st	2nd	3rd	4th	Total
	subfr	subfr	subfr	subfr	per frame
VAD-flag					1
ISPs					46
LTP Lag	9	6	9	6	30
LTP-filtflag	1	1	1	1	4
Fixed Index	52	52	52	52	208
CB Gains	7	7	7	7	28
Total		•			317

Table 1: Bit allocation of the AMR-WB speech codec at 15.85 kbit/s [17].

book gains are jointly vector quantized using 7 bits per subframe, whilst the fixed excitation codebook parameters of each 5 ms subframe are encoded using 13 bits. [17]. A large training sequence of 2,133,035 20 ms frames was applied to the AMR-WB encoder in order to estimate the residual redundancy of each individual quantised AMR-WB parameter. In general, the residual redundancy inherent in the encoded bit-stream representing the speech signal at the output of the speech encoder reflects the residual redundancy manifesting itself in terms of the residual correlation (or memory). Another sign of substantial residual redundancy is the non-uniform probability of occurance for the various encoded speech parameters. Let us assume that each of the encoded codec parameters can be sufficiently accurately modelled by a stationary stochastic process and each of the encoded parameters is modelled by a zero-order- or a first-order Markov chain, when quantifying the residual redundancy manifesting itself in terms of the nonuniform relative frequency of the legitimate encoded speech parameters. Let us now consider the estimation of the residual redundancy inherent in the AMR-WB encoded parameters which is quantified by estimating the entropy $H(\mathcal{U})$ of the encoded parameters, where \mathcal{U} represents the random process modelling the AMR-WB encoded parameters. The entropy $H(\mathcal{U})$ of the encoded parameters, represents the minimum number of bits required for describing the parameter. Thus, if each parameter is encoded by \mathcal{B} bits/symbol, the total residual redundancy inherent in the parameter is given by $\mathcal{R}_T = \mathcal{B} - H(\mathcal{U})$. The residual redundancy reflects the residual correlation (or memory), \mathcal{R}_M and nonuniformity, \mathcal{R}_D . Specifically, we can write $\mathcal{R}_T = \mathcal{R}_D + \mathcal{R}_M$ where for variable U_1 , the redundancy due to the nonuniformity $\mathcal{R}_D = \mathcal{B} - H(U_1)$, whilst, $\mathcal{R}_M = H(U_1) - H(\mathcal{U})$ denotes the redundancy due to the memory [20]. Let us provide an example for quantifying the entropy $H(\mathcal{U})$ of the ISP parameters. Assuming the ISP parameters can be approximated by a stationary stochastic process, let the random process, $U_i: i = 1, 2, ..., 7$, represent the i^{th} vector quantized ISP in each AMR-WB frame, which are encoded using 8, 8, 6, 7,

7, 5 and 5 bits, respectively. Additionally, we assume that the ISPs corresponding to the same ISP index i=1,...,7 in the other AMR-WB encoded frames are independent and each of the ISPs within a frame exhibits *first-order* Markov chain, so,

$$P(U_n = u_n | U_1 = u_1, ..., U_{n-1} = u_{n-1})$$

$$= P(U_n = u_n | U_{n-1} = u_{n-1})$$
(1)

Following the derivation in [21], $H(\mathcal{U})$ can be expressed as:

$$H(\mathcal{U}) = \lim_{n \to \infty} H(U_n | U_1, U_2, ..., U_{n-1})$$

$$= \lim_{n \to \infty} H(U_n | U_{n-1})$$

$$= H(U_n | U_{n-1})$$

$$= \mathcal{E}_{p(u_n, u_{n-1})}$$

$$\times log_2[P(U_n = u_n | U_{n-1} = u_{n-1})]$$
 (2)

Thus, the total residual redundancy, \mathcal{R}_T of U_2 is given by:

$$\mathcal{R}_T = 8 - H(\mathcal{U}) \tag{3}$$

since, $\mathcal{R}_T = \mathcal{R}_D + \mathcal{R}_M$, thus, giving:

$$\mathcal{R}_D = 8 - H(U_2) \tag{4}$$

$$\mathcal{R}_M = H(U_2) - H(U_2|U_1) \tag{5}$$

where $H(U_2)$ is the entropy of parameter U_2 , given by:

$$H(U_2) = -\sum_{u_1} P(U_2 = u_2) \dot{l}og_2 [P(U_2 = u_2)]$$
 (6)

The total redundancy of each AMR-WB codec parameter modelled with the aid of a first-order as a Markov Chain is shown in Table 2, although, the total redundancy of the fixed codebook index was modelled by a zero-order Markov chain, as shown in Table 3. communicating the second and fifth columns of Table 2 as well as the second and fourth columns of Table 3, it becomes clear that the AMR-WB codec is highly efficient in removing the redundancy. Hence, we may conclude from Table 2 and 3, that the residual correlation of all parameters within a given frame is insignificant and hence its exploitation is unlikely to be justifiable, considering the significant complexity imposed in terms of their trellis decoding invoked for exploiting our related first-order a priori knowledge, since the attainable performance would only be marginally increased. Hence, only our zero-order a priori knowledge is exploited in the decoding process. It will be shown in Section 4 that the exploitation of this residual redundancy at the decoder has the potential of providing substantial performance gains when compared to the receiver dispensing with this knowledge.

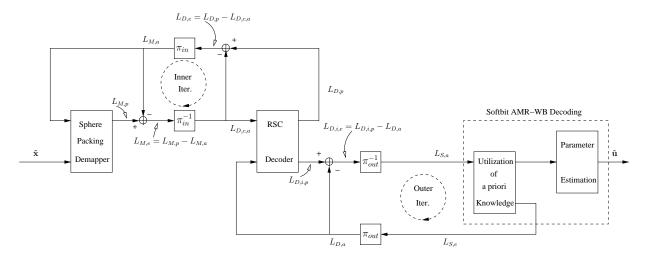


Figure 2: Block diagram of the jointly optimised DSTS-SP-RSC-AMRWB turbo detection scheme seen at the right of Figure 1. The notations π_{in} and π_{in}^{-1} represent the *inner* interleaver and deinterleaver, π_{out} and π_{out}^{-1} denote the outer interleaver and deinterleaver.

Parameter		Encoded Bits	\mathcal{R}_D	\mathcal{R}_M	\mathcal{R}_T
ISP No.	1	8	0.57	0.00	0.57
	2	8	0.45	0.96	1.41
	3	6	0.10	0.10	0.20
	4	7	0.06	0.06	0.12
	5	7	0.14	0.05	0.19
	6	5	0.03	0.03	0.06
	7	5	0.07	0.04	0.11
LTP Lag	subfr 1	9	0.23	0.00	0.23
	subfr 2	6	0.40	0.21	0.61
	subfr 3	9	0.20	0.14	0.34
	subfr 4	6	0.39	0.21	0.60
CB Gain	subfr 1	7	0.10	0.00	0.10
	subfr 2	7	0.10	1.10	1.20
	subfr 3	7	0.10	1.12	1.22
	subfr 4	7	0.12	1.12	1.24

Table 2: Residual Redundancy in the AMR-WB codec's Parameters Modelled by a First-Order Markov Chain

Parameter		Encoded Bits	\mathcal{R}_D	$ \mathcal{R}_T $
Fixed Ind. subfr1	1	13	0.33	0.33
	2	13	0.31	0.31
	3	13	0.31	0.31
	4	13	0.31	0.31

Table 3: Residual Redundancy in the Fixed Codebook Index Modelled by a Zero-Order Markov Chain

3. SYSTEM OVERVIEW

Figure 1 shows the schematic of the proposed scheme. The iterative decoder structure of the DSTS-SP-RSC-AMRWB scheme is illustrated in Figure 2, where the *extrinsic* information is exchanged amongst all three constituent decoders. The *inner* iterative loop corresponds to the iterative soft SP demapping and RSC channel decoding [13], while the *outer* iterative loop corresponds to the *extrinsic/a*

priori information exchange between the AMR-WB speech decoder and the RSC decoder.

3.1. Transmitter

The AMR-WB speech encoder produces a frame of speech codec parameters, namely $\{\mathbf v_{1,\tau},\mathbf v_{2,\tau},\dots,\mathbf v_{\kappa,\tau},\dots,\mathbf v_{36,\tau}\}$, where $\mathbf v_{\kappa,\tau}$ denotes an encoded parameter, with $\kappa=1,\dots,K_\kappa$ denoting the position of each parameter in the encoded speech frame, whilst, τ denotes the time index referring to the current encoded frame index. Then, $\mathbf v_{\kappa,\tau}$ is quantised and mapped to the bit sequence, $\mathbf u_{\kappa,\tau}=[u(1)_{\kappa,\tau}\ u(2)_{\kappa,\tau}\ \dots\ u(M)_{\kappa,\tau}]$, where M is the total number of bits assigned to the κ th parameter. Then, the outer interleaver, π_{out} permutes the bits of the sequence $\mathbf u$, yielding $\tilde{\mathbf u}$ of Figure 1.

The interleaved RSC encoded source bit sequence $\tilde{\mathbf{u}}$ of Figure 1 is generated by a rate $R = \frac{1}{2}$ code having a constraint length of K=7 and octally represented generator polynomials of $(G_r,G)=$ (217, 110). The DSTS-SP modulator of Figure 1 first maps B number of channel-coded bits $\tilde{\mathbf{c}} = [\tilde{c}_0 \ \tilde{c}_1 \ \dots \ \tilde{c}_{B-1}] \in 0,1$ to a sphere packing symbol $x \in X$, so that we have $x = map_{sp}(\tilde{\mathbf{c}})$, where the function of $map_{sn}(\tilde{\mathbf{c}})$ represents the mapping of B bits to a SP symbol, x. Furthermore, we have $B = log_2L = log_216 = 4$, where L represents the set of legitimate SP constellation points, as outlined in [14]. Subsequently, we have a set of SP symbols that can be transmitted using DSTS and two transmit antennas as detailed in [14]. Space-Time Spreading (STS) was first proposed by Marzetta et. al [22] for the sake of providing space-time-coding-style spatial transmit diversity gain for CDMA systems, as detailed in Section 8.2 of [23]. However, the channel impulse response (CIR) of all transmit-receive antenna links has to be estimated in coherently detected STS-CDMA systems which is a challenging task. Hence, in [13] the philosophy of differentially detected DSTS was developed, which dispenses with the need of estimating the CIR.

In this study, we considered transmissions over a correlated narrowband Rayleigh fading channel, associated with a normalised Doppler frequency of $f_D=0.01$.

3.2. Receiver

Inner Iterations: the complex-valued received symbols, **z** are de-

mapped to their Log-likelihood Ratio (LLR) [9] representation for each of the B RSC-encoded bits per DSTS-SP symbol. As seen in Figure 2, the *a priori* LLR values, $L_{M,a}$ provided by the RSC decoder are subtracted from the *a posteriori* LLR values, $L_{M,p}$ at the output of the SP-demapper for the sake of generating the extrinsic LLR values $L_{M,e}$ and then the LLRs $L_{M,e}$ are deinterleaved by a soft-bit deinterleaver. Next, the deinterleaved soft-bits $L_{D,c,a}$ of Figure 2 are passed to the RSC decoder in order to compute the a posteriori LLR values $L_{D,p}$ provided by the Max-log MAP algorithm [24] not only for the systematic bits, but for all the RSC-encoded bits. The extrinsic information $L_{D,e}$ seen in Figure 2 is generated by subtracting the *a priori* information, $L_{D,c,a}$ from the *a posteriori* information $L_{D,p}$ according to $L_{D,p} - L_{D,c,a}$, which is then fed back to the SP demapper as the *a priori* information $L_{M,a}$ after appropriately reordering them using the inner soft-value interleaver. The SP-demapper of Figure 2 exploits the a priori information $L_{M,a}$ for the sake of providing improved a posteriori LLR values, $L_{M,p}$ which are then passed to the RSC decoder and in task, back to the SP-demapper for further iterations.

Outer Iterations: As seen in Figure 2, the *extrinsic* LLR values $L_{D,i,e}$ of the original uncoded systematic information bits are generated by subtracting the *a priori* LLR values, $L_{D,a}$ of the RSC decoder from the LLR values $L_{D,i,p}$ of the original uncoded systematic information bits. Then, the LLRs $L_{D,i,e}$ are deinterleaved by the outer soft-bit deinterleaver. The resultant soft-bits $L_{S,a}$ are passed to the AMR-WB decoder that was further developed for handling soft input bits in order to compute the *extrinsic* LLR values, $L_{S,e}$ with the aid of soft-bit source decoding (SBSD). These *extrinsic* LLR values are then fed back to the RSC decoder after appropriately reordering them in the order required by the RSC decoder for the sake of completing an outer iteration. We define this as having I=1 iteration, namely, one external iteration. The residual redundancy quantified in Section 2 is exploited as *a priori* information in computing the *extrinsic* LLR values, and estimating the parameters.

The details of the algorithm used for computing the *extrinsic* LLR values, $L_{S,e}$ can be found in [7].

4. PERFORMANCE RESULTS

In this section we evaluate the attainable performance of the proposed scheme using both the Bit Error Ratio (BER) and the Segmental Signal to Noise Ratio (SegSNR) [2] evaluated at the speech decoder's output as a function of the channel SNR.

All simulation parameters are listed in Table 4. In our simulations each inner iteration was followed by an outer iteration.

Figure 3 depicts the BER versus Signal to Noise Ratio (SNR) per bit, namely E_b/N_0 performance of the non-coherent DSTS-SP-RSC-AMRWB scheme and that of its corresponding DSTS-SP-RSC benchmarker scheme, when communicating over uncorrelated Rayleigh fading channels. It can be seen from Figure 3 that the DSTS-SP-RSC-AMRWB scheme outperforms the DSTS-SP-RSC benchmarker scheme by about 1 dB at BER= 1.5×10^{-4} after I=4 iterations where, we define a complete decoding iteration cycle, as having an inner iteration followed by a single outer-iteration, as mentioned in Section 3, which is referred to as having I=1 iteration. The AMR-WB-decoded scheme has a low BER at its speech decoded output than its benchmarker dispensing with speech decoding, because the extrinsic information exchange between the AMR-WB decoder and the RSC decoder has the potential of improving the BER.

Let us now study the speech SegSNR performance of the pro-

System Parameters	DSTS-SP-RSC-AMR-WB		
Source Coding	AMR-WB		
Bit Rates (kbit/s)	15.85		
Speech Frame Length (ms)	20		
Sampling Rate (kHz)	16		
Channel Coding	Recursive Sys. Conv. Code		
Code Rate	R=1/2		
Code Memory	K=7		
Code Generator (G_r, G)	$(217, 110)_8$		
Modulation Scheme	Sphere Packing (L=16)		
MIMO Scheme	DSTS		
Number of Transmitters, N_t	2		
Number of Receivers, N_r	1		
Spreading Code	Walsh Code		
Spreading Factor	8		
Number of users	4		
Channel	Correlated Rayleigh Fading		
Normalised Doppler frequency	0.01		
System Throughput (bit/symbol)	1		

Table 4: System Parameters

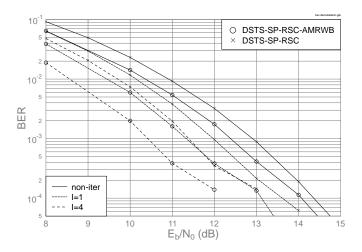


Figure 3: BER versus E_b/N_0 performance of the jointly optimised DSTS-SP-RSC-AMRWB scheme of Figure 1 and Table 4, when communicating over correlated non-dispersive Rayleigh fading channels.

posed scheme in Figure 4. In this context the residual redundancy inherent in the encoded source is exploited twice, firstly during computing the *extrinsic* information and secondly during the parameter Markov-model-based estimation [25]. It can be seen from Figure 4 that the exploitation of residual redundancy inherent in the encoded source during the decoding process benefitting from Markov-model-based parameter estimation performs approximately 0.5 dB better in terms of the required channel E_b/N_0 value, than its corresponding hard speech decoding based counterpart, when allowing a SegSNR degradation of 1 dB in comparison to the maximum attainable SegSNR maintained over perfectly error-free channels. Additionally, iteratively exchanging the soft-information amongst the three receiver components of the amalgamated DSTS-SP-RSC-AMRWB scheme has resulted in a further E_b/N_0 gain of about 2.5 dB after I=4 iterations, when tolerating a SegSNR degradation of 1 dB.

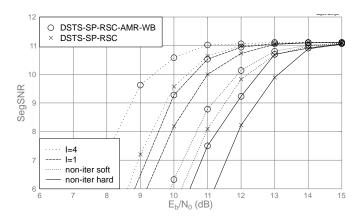


Figure 4: Average SegSNR versus E_b/N_0 performance of the jointly optimised DSTS-SP-RSC-AMRWB scheme of Figure 1 and Table 4 in comparison to the DSTS-SP-RSC benchmarker scheme, when communicating over correlated non-dispersive Rayleigh fading channels

5. CONCLUSIONS

In this contribution the three-stage turbo detection DSTS-SP-RSC-AMRWB scheme of Figure 1, 2 and Table 4 was proposed for transmission over a correlated non-dispersive Rayleigh fading channel. The employment of the soft-output AMR-WB speech codec, which exploits the residual redundancy inherent in the encoded bitstream demonstrates a significant improvement in terms of the average SegSNR versus channel E_b/N_0 performance compared to its corresponding hard decoding based benchmarker. The performance of the three-component turbo receiver is about 1 dB better in terms of the E_b/N_0 in comparison to the benchmarker scheme carrying out joint iterative channel decoding and DSTS aided SP-demodulation, but using separate AMR-WB decoding. The employment of the DSTS scheme that requires no channel state information, whilst providing a substantial diversity gain by invoking two transmit antennas.

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