

# Convergence Behaviour of Iteratively Decoded Short Block-Codes in H.264 Joint Source and Channel Decoding

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**Abstract**— We propose a novel class of Short Block Codes (SBCs) designed for guaranteed convergence to an infinitesimally low Bit Error Ratio (BER), which relies on the joint optimisation of soft-bit assisted iterative source and channel decoding. An iterative detection aided combination of SBC assisted Soft-Bit Source Decoding (SBSD) and a rate-1 precoder was used to evaluate the attainable performance of the proposed scheme for transmission of Data-Partitioned (DP) H.264 source coded video over correlated narrowband Rayleigh fading channels. Additionally, we demonstrated the effects of different SBCs having diverse minimum Hamming distances [ $d_{H,min}$ ] but identical coding rates on both the overall BER performance as well as on the objective video quality expressed in terms of the Peak Signal-to-Noise Ratio (PSNR). The convergence behaviour of the iterative decoding scheme using SBCs as a function of  $d_{H,min}$  is analysed by utilising extrinsic information transfer (EXIT) charts. Explicitly, our experimental results show that the proposed error protection scheme using SBCs associated with  $d_{H,min} = 6$  outperforms the identical-code-rate SBCs having  $d_{H,min} = 3$  by about 3 dB at the PSNR degradation point of 1 dB. Additionally, an  $E_b/N_0$  gain of 27 dB is attained using iterative soft-bit source and channel decoding with the aid of rate- $\frac{1}{3}$  SBCs relative to the identical-rate benchmarker.

## I. MOTIVATION AND BACKGROUND

There has been an increasing interest in multimedia transmission over wireless channels [1, 2]. However, in wireless systems the data rate is limited by both the available bandwidth and transmission power. Various data compression standards have been developed to transmit multimedia signals over band-limited channels [3, 4]. However, the removal of redundancy renders the compressed data stream more vulnerable to transmission errors. Therefore, simultaneously maintaining a high compression efficiency as well as a high integrity, when communicating over heterogeneous communication networks is of paramount importance [3]. As a result, the joint optimisation of traditionally separate functions leads to joint source and channel decoding (JSCD) in order to achieve the best possible performance in realistic practical scenarios, which has attracted considerable research interest [5–9]. The family of JSCD schemes involving the exploitation of the residual redundancy in the source-coded bit-stream as a form of error protection is of prime concern [10, 11]. More specifically, an attractive SBSD scheme was proposed by Fingscheidt and Vary [10], in order to exploit the residual redundancy of the source-coded bit-stream for the convergence improvement of Iterative Source-Channel Decoding (ISCD) [11]. However, when using sophisticated state-of-the-art coding schemes, only modest residual redundancy remains in the source coded bit-stream, therefore we propose to deliberately impose further

redundancy on the source coded bit-stream with the aid of the novel class of SBCs proposed. In our investigations we used the H.264/AVC [12] source codec, which employs predictive coding and Variable Length Coding (VLC) techniques to achieve a high compression efficiency, hence rendering the compressed bit-stream vulnerable to transmission errors [3]. A single bit error in the coded stream may lead to the corruption of numerous future codewords. Furthermore, due to predictive coding the effects of channel errors may affect the neighboring video blocks owing to error propagation. Therefore the transmission of compressed video over wireless systems presents a challenging task. Error resilient Data Partitioning (DP) [12] has been incorporated in the H.264/AVC codec in order to mitigate the effects of channel errors. In the H.264/AVC codec DP results in three different types of streams, each containing specific sets of coding parameters having different level of importance. Various error resilient schemes have been proposed in [3], in order to alleviate these problems, but the price paid is a potentially reduced compression efficiency and increased computational complexity. Similarly, an iterative joint source-channel decoding procedure inspired by the concept of serial concatenated codes was presented in [6]. Moreover, a symbol-based soft-input a posteriori probability (APP) decoder was presented in [8], where the residual redundancy inherent in the compressed bit stream was exploited for improved error protection. On the other hand, a novel irregular variable length coding (IrVLC) scheme designed for near-capacity joint source and channel coding was advocated in [5]. Likewise, [13] advocates state-of-the-art system design principles and the performance of burst-by-burst adaptive transceivers for interactive cellular and cordless video telephony. Furthermore, a joint source-channel decoding method based on the MAP algorithm was proposed in [9].

Against this background, in this paper we propose novel SBCs designed for guaranteed convergence of the iterative SBSD scheme advocated, leading to a 27 dB transmit-power gain at the Peak Signal-to-Noise Ratio (PSNR) degradation point of 1 dB.

The rest of the paper is organised as follows. An overview of our system model is provided in Section II. Section III portrays our SBC based ISCD scheme accompanied by a design example, while its EXIT chart analysis is provided in Section IV. The performance of the proposed system is characterised with the aid of our simulation results in Section V. Finally we provide our conclusions in Section VI.

## II. ITERATIVE SOURCE-CHANNEL DECODING

### A. System Overview

The structure of our proposed ISCD videophone arrangement used as our design example for quantifying the performance of various SBC schemes is shown in Figure 1. At the transmitter side the H.264 video codec is used to compress the source video sequence and the generated bit-stream  $x_k$  is mapped or encoded into the bit-string  $x'_m$  by employing the SBC scheme. Subsequently, the encoded bit-string  $x'_m$  is interleaved using the bit-interleaver  $\Pi$  of Figure 1, yielding the interleaved sequence  $\bar{x}_m$ , which is encoded by the rate-1 precoder of Table II before transmission.

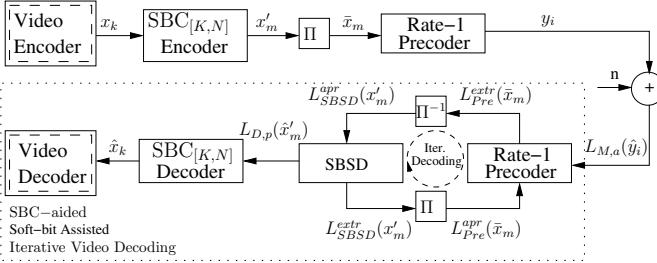


Fig. 1. The Proposed Iterative Source-Channel Decoding System Model.

In the iterative decoder of Figure 1, the extent of the statistical independence of the extrinsic information provided by an interleaver is always related to its length [7]. Therefore instead of performing the ISCD operation on the various frame slices independently, we concatenated all the bits generated by the MBs of the slices within a given frame, which results in a longer interleaver and hence improves the attainable performance of iterative decoding without extending the video delay. The precoded bit-stream  $y_i$  is QPSK modulated and transmitted over a temporally correlated narrowband Rayleigh fading channel, associated with the normalised Doppler frequency of  $f_d = f_D T_s = 0.01$ , where  $f_D$  is the Doppler frequency and  $T_s$  is the symbol duration. At the receiver the soft-information obtained after QPSK demodulation is forwarded to the inner decoder. The extracted extrinsic information is then exchanged between the inner and outer decoder of Figure 1, in order to attain the lowest possible BER [11].

### B. The Soft-bit Source Decoding

The details of computing the extrinsic Log-Likelihood-Ratios (LLRs) using SBSD for the zero-order Markov model can be obtained from [11]. More specifically, according to our coding procedure the source-encoded bit-stream is partitioned into  $M = 2^K$ -ary, or  $K$ -bit symbols, and will be termed as the information word to be encoded by the proposed SBC, each with a different probability of occurrence. The redundancy of the source bit-stream is then characterised with the aid of the non-uniform  $M = 2^K$ -ary symbol probability distribution  $P[S_K(\tau)]$ , where  $S_K(\tau) = [S_K(1), S_K(2), \dots, S_K(M)]$ , with  $K$  denoting the number of bits in each  $M = 2^K$ -ary symbol<sup>1</sup>. Provided that the bits of an  $M = 2^K$ -ary symbol may be considered independent of each other, the channels'

<sup>1</sup>The choice of how many bits are specifically grouped into an  $M = 2^K$ -ary symbol represents an important system parameter, but due to space limitations this issue is not detailed in this paper.

output information generated for the  $\tau$ -th  $K$ -bit symbol is provided by the product of each of the constituent single-bit probabilities given as:

$$P[\hat{y}_\tau | y_\tau] = \prod_{k=1}^K P[\hat{y}_\tau(k) | y_\tau(k)], \quad (1)$$

where  $\hat{y}_\tau$  and  $y_\tau$  are the corresponding transmitted and received  $K$ -bit source sequences respectively. For each desired bit  $[y_\tau(\lambda)]$ , the extrinsic channel output information  $P[\hat{y}_\tau^{[ext]} | y_\tau^{[ext]}]$  is expressed as:

$$P[\hat{y}_\tau^{[ext]} | y_\tau^{[ext]}] = \prod_{k=1, k \neq \lambda}^K P[\hat{y}_\tau(k) | y_\tau(k)]. \quad (2)$$

Finally, the resultant extrinsic LLR value can be acquired for each bit of the  $\tau$ -th symbol by combining its channel output information and the *a-priori* knowledge of the corresponding  $\tau$ -th symbol as [10, 11]:

$$LLR[y_\tau(\lambda)] = \log \left( \frac{\sum_{y_\tau^{[ext]}} P[y_\tau^{[ext]} | y_\tau(\lambda) = +1].P[\hat{y}_\tau^{[ext]} | y_\tau^{[ext]}]}{\sum_{y_\tau^{[ext]}} P[y_\tau^{[ext]} | y_\tau(\lambda) = -1].P[\hat{y}_\tau^{[ext]} | y_\tau^{[ext]}]} \right). \quad (3)$$

The trait of our proposed SBC coding technique is that its generic and hence it is applicable to arbitrary speech, audio and video source codecs. However in our design example we characterised the source coded bit-stream's redundancy with the aid of the non-uniform  $M$ -ary symbol probability distribution<sup>2</sup> using the H.264/AVC video encoded bit-stream of the 300-frame "Akio" video sequence, the 150-frame "MissAmerica" video clip and the 300-frame "Mother&Daughter" video sequence, which were used as training sequences.

## III. SHORT BLOCK CODE BASED ITERATIVE SOURCE CHANNEL DECODING

### A. The Criterion for Iterative Convergence

The purpose of ISCD is to allow the constituent inner and outer decoders in order to assist each other in an iterative fashion to glean the highest possible extrinsic information from each other. In this scenario the achievable performance of SBSD is limited by the factor that its achievable iteration gain is dependent on the residual redundancy or correlation that remains in the coded bit pattern  $x_i$ . Therefore, despite using limited-delay, limited-complexity, lossy compression, which typically results in residual redundancy in the compressed stream, the achievable performance improvements of SBSD may remain limited. To elaborate a little further, the lower the video bitrate, the less extrinsic information may be generated from the source-coded stream. It may be observed from the simulation results of [14] that using SBSD typically results in negligible system performance improvements beyond two decoding iterations. Hence, in order to improve the achievable ISCD performance gain, we *artificially* introduce redundancy

<sup>2</sup>The resultant  $M$ -ary source word probability distribution for the  $SBC_{[K,N]}$  coding schemes used in our design example is not provided in this paper owing to space limitations.

in the source coded bit-stream using a carefully designed technique, which we refer to as an SBC scheme. The novel philosophy of our SBC design is based on exploiting a specific property of EXIT charts. More explicitly, an iterative decoding aided receiver is capable of near-capacity operation at an infinitesimally low BER, if there is an open tunnel between the EXIT curves of the inner and outer constituent component decoders. The *sufficient and necessary* condition for this iterative detection convergence criterion to be met was shown by Kliewer et al [15] to be that the legitimate codewords should have  $d_{H,min} = 2$ . With  $d_{H,min} = 2$  the ISCD scheme becomes capable of achieving the highest possible source entropy denoted as  $H(X) = L_{SBSB}^{extr} = 1 \text{ bit}$ , provided that the input *a-priori* information of the SBSB is perfect, i.e. we have  $H(X) = L_{SBSB}^{apri} = 1 \text{ bit}$ . This motivates the design of our novel SBC coding scheme, because it is plausible that using our design procedure all legitimate SBC codewords having a specific mapping-rate equivalent to the reciprocal of the classic code-rate results in a code-table satisfying the condition of  $d_{H,min} = 2$ .

### B. Algorithm of Short Block Codes

The fundamental principle of our  $\text{SBC}_{[K, N]}$  encoding algorithm is based on the *necessary and sufficient* condition outlined in Section III-A for achieving perfect iterative convergence to an infinitesimally low BER. The proposed  $\text{SBC}_{[K, N]}$  encoding algorithm partitions the video-stream  $x_k$  into  $K$ -bit source symbols, which will be encoded into  $N = (K + P)$ -bits, where  $P$  represents the number of redundant bits per  $K$ -bit information word.

For  $P = (m \times K)$  with  $m \geq 1$ , we propose the corresponding  $\text{SBC}_{[K, N]}$ -encoding procedure, which results in a gradual increase of  $d_{H,min}$  for the coded symbols upon increasing both  $K$  and  $N$  of the  $\text{SBC}_{[K, N]}$ , while the code-rate is fixed. Using this  $K$  to  $N$ -bit encoding method, first  $I = [(m - 1) \times K]$  number of redundant bits  $r_\tau(i)$ , for  $i = 1, 2 \dots I$  are concatenated to the  $\tau$ -th  $K$ -bit source symbol by repeated concatenation of  $K$  additional source coded bits  $(m - 1)$  times, yielding a total of  $[(m - 1) \times K]$  bits. Finally, the last set of  $K$  redundant bits  $r_\tau(k)$ , for  $k = 1, 2 \dots K$  is generated by calculating the *exclusive OR* (*XOR*) function of the  $K$  source bits  $b_\tau(j)$ , while setting  $b_\tau[j = k]$  equal to 0, yielding:

$$r_\tau(k) = [b_\tau(1) \oplus b_\tau(2) \dots \oplus b_\tau(K)]; \text{ for } k = 1, 2 \dots K, \quad (4)$$

while setting  $b_\tau(k) = 0,$

where  $\oplus$  represents the *XOR* operation.

Using this method a carefully controlled redundancy is imposed by the specific rate  $r = \lceil \frac{K}{N} \rceil \text{ SBC}_{[K, N]}$  to ensure that the resultant  $N$ -bit codewords exhibit a  $d_{H,min} \geq 2$  between the  $M = 2^K$  number of legitimate  $K$ -bit source code words.

### C. Design Example

Let us now demonstrate the power of SBCs with the aid of a design example. As an example, the  $\text{SBC}_{[K, N]}$ -encoded symbols generated by applying this algorithm for the different rate- $\frac{1}{3}$  SBC schemes along with their corresponding  $d_{H,min}$  is summarised in Table I for the specific case of incorporating

the redundant bits  $r_\tau(k)$ , for  $k = 1, 2 \dots K$  at the right of the  $\tau$ -th  $K$ -bit source symbol. As evident from Table I, only  $2^K$  out of the  $2^N$  possible  $N$ -bit symbols are legitimate in the mapped source coded bit-stream, which exhibits a non-uniform probability of occurrence for the  $N$ -bit source symbols. The

TABLE I

DIFFERENT SBCs WITH CORRESPONDING SYMBOLS AND  $d_{H,min}$

SBC Type	Symbols in Decimal	$d_{H,min}$
Rate-1 SBC	{0,1}	1
Rate- $\frac{1}{3}$ SBC $_{[2, 6]}$	{0,22,41,63}	3
Rate- $\frac{1}{3}$ SBC $_{[3, 9]}$	{0,78,149,219,291,365,438,504}	4
Rate- $\frac{1}{3}$ SBC $_{[4, 12]}$	{0,286,557,819,1099,1365,1638,1912,2183, 2457,2730,2996,3276,3538,3809,4095}	5
Rate- $\frac{1}{3}$ SBC $_{[5, 15]}$	{0,1086,2141,3171,4251,5285,6342,7416, 8471,9513,10570,11636,12684,13746,14801, 15855,16911,17969,19026,20076,21140,22186, 23241,24311,25368,26406,27461,28539,29571, 30653,31710,32736}	6

coding parameters of the different SBC schemes used in our design example are shown in Table II. Observe from the table that an overall code-rate of  $R = \frac{1}{3}$  was maintained by using a concatenated rate-1 precoder in order to accommodate the different rate- $\frac{1}{3}$  SBCs of Table I. Moreover, for the rate-1 SBC benchmarker scheme the rate- $\frac{1}{3}$  concatenated RSC is used as inner code in order to achieve an iteration gain, while keeping the overall bit-rate budget constant.

TABLE II

CODE RATES FOR DIFFERENT ERROR PROTECTION SCHEMES

Error Protection Scheme	Code Rate		
	Outer Code (SBC)	Inner Code	Overall
SBC $_{[2, 6]}$	Rate- $\frac{1}{3}$ SBC	Rate-1 Precoder	1/3
SBC $_{[3, 9]}$	Rate- $\frac{1}{3}$ SBC	Rate-1 Precoder	1/3
SBC $_{[4, 12]}$	Rate- $\frac{1}{3}$ SBC	Rate-1 Precoder	1/3
SBC $_{[5, 15]}$	Rate- $\frac{1}{3}$ SBC	Rate-1 Precoder	1/3
SBC Rate-1	Rate-1 SBC	Rate- $\frac{1}{3}$ RSC	1/3

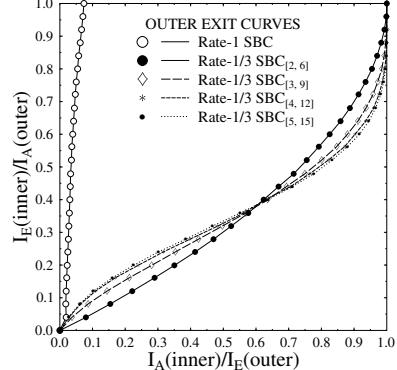


Fig. 2. EXIT characteristics of SBSB with and without the aid of SBCs.

### IV. EXIT CHART ANALYSIS

The EXIT characteristics of the SBSB scheme of Figure 1 using either the rate-1<sup>3</sup> or the rate- $\frac{1}{3}$  SBC $_{[K, N]}$  schemes shown in Table I are portrayed in Figure 2. More specifically, the EXIT curve of SBSB using rate- $\frac{1}{3}$  SBCs does indeed reach to the top right corner of the EXIT chart at  $(I_A, I_E) = (1, 1)$  and hence results in an infinitesimally low BER. By contrast,

<sup>3</sup>For the sake of using a unified terminology, we refer to the scheme using no SBC as the rate-1 SBC.

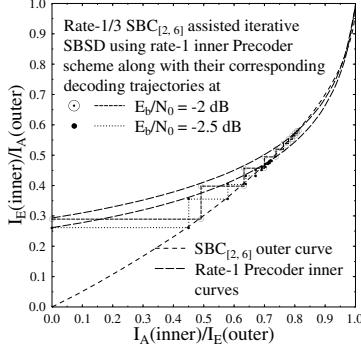


Fig. 3. The EXIT chart and decoding trajectories of the  $\text{SBC}_{[2, 6]}$  scheme.

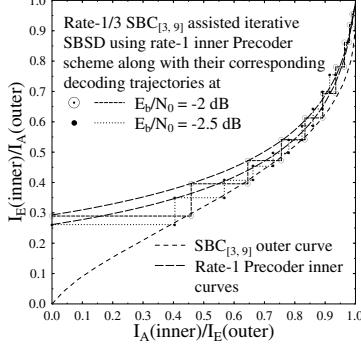


Fig. 4. The EXIT chart and decoding trajectories of the  $\text{SBC}_{[3, 9]}$  scheme.

the SBSD scheme using a rate-1 SBC, i.e. no SBC fails to do so. The actual decoding trajectories of the various error protection schemes employing the different rate- $\frac{1}{3}$  outer SBCs as well as using the rate-1 inner precoder detailed in Table II were recorded both at  $E_b/N_0 = -2 \text{ dB}$  and  $-2.5 \text{ dB}$ , as portrayed in Figures 3, 4, 5, and 6, respectively. These trajectories were recorded by acquiring the mutual information at the input and output of both the inner and outer decoder during the bit-by-bit Monte-Carlo simulation of the iterative soft-bit source and channel decoding algorithm. It may be analysed from the EXIT trajectories of Figures 3, 4, 5, and 6 that as expected, the convergence behaviour of the SBCs improves upon increasing  $d_{H,\min}$ .

## V. SYSTEM PERFORMANCE RESULTS

In this section we present our performance results characterising the proposed system. A 45-frame "Akiyo" video sequence [3] represented in (176x144)-pixel Quarter Common Intermediate Format (QCIF) and encoded using the H.264/AVC JM 13.2 reference video codec at 15 frames-per-second ( $\text{fps}$ ) at the target bitrate of  $64 \text{ kbps}$  was used as our test sequence. Each QCIF frame was partitioned into 9 slices and each slice was composed of 11 MBs. In order to reduce error propagation each intra-coded 'I' frame is followed by 44 predicted 'P' frames, corresponding to 3 seconds lag between two consecutive 'I' frames at  $15 \text{ fps}$ . Additionally, we incorporated error resilience features, such as DP and intra-frame coded MB updates of three randomly dispersed MBs per frame, to control the effects of error propagation. The insertion of 'B' pictures results in an unacceptable loss of lip-synchronisation as a result of the corresponding delay incurred due to the bi-directionally predicted video coding operations [12], and hence was avoided. The remaining system parameters of our experimental setup are listed in Table III.

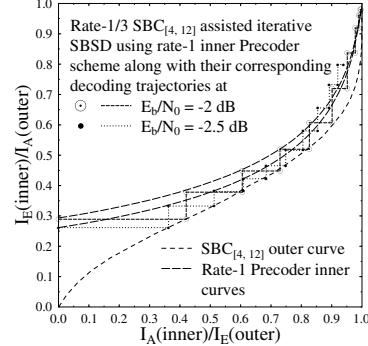


Fig. 5. The EXIT chart and decoding trajectories of the  $\text{SBC}_{[4, 12]}$  scheme.

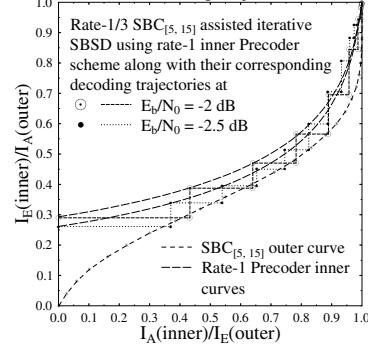


Fig. 6. The EXIT chart and decoding trajectories of the  $\text{SBC}_{[5, 15]}$  scheme.

Keeping in view the videophone scenario, error resilient en-

TABLE III  
SYSTEMS PARAMETERS

System Parameters	Value	System Parameters	Value
Source Coding	H.264/AVC	No of MB's/Slice	11
Bit Rate (Kbps)	64	Intra-frame MB update/frame	3
Frame Rate (fps)	15		
No of Slices/frame	9		
Inner Code	Rate-1 Precoder	Over-all Code Rate	1/3
Modulation Scheme	QPSK	Channel	Correlated Rayleigh Fading
Number of Transmitters, $N_t$	1	Normalised Doppler Frequency	0.01
Number of Receivers, $N_r$	1		
Interleaver Length	$\approx (64000/15)$		

coding techniques, such as Flexible Macro-block Ordering (FMO) [12] and the employment of multiple reference frames for inter-frame motion compensation were turned off, because despite their substantially increased complexity they typically result in modest video performance improvements in low-motion head-and-shoulders video sequences, such as the "Akiyo" clip. Additionally, only the immediately preceding frame was used for motion search, which results in a reduced computational complexity compared to using multiple reference frames. Moreover, due to the limited residual redundancy inherent in the source encoded bit-stream and for the sake of reducing the computational complexity imposed, we limited the number of iterations between the RSC and SBSD decoders to  $I_t = 5$ , when using a rate-1 SBC – i.e. no SBC. By contrast we used  $I_t = 10$  iterations, when applying SBCs having a rate below unity. For the sake of increasing the confidence in our results, we repeated each 45-frame experiment 160 times and averaged the generated results. Figure 7 presents the performance of the various rate- $\frac{1}{3}$  SBC based error protection schemes of Table I in terms of the attainable BER, while their comparison with the rate-1 SBC based schemes is offered

in Figures 9. Finally, the performance trends expressed in terms of the  $PSNR$  versus  $E_b/N_0$  curves are portrayed in Figures 8 and 10. It may be observed in Figure 8 that the SBC<sub>[5, 15]</sub> scheme having  $d_{H,min} = 6$  provides the best  $PSNR$  performance among the four different SBC schemes of Table II across the entire  $E_b/N_0$  region considered. Its also observed in Figure 10 that upon using SBSD in conjunction with the rate-1 outer SBC and rate- $\frac{1}{3}$  inner RSC results in a worse  $PSNR$  performance than the employment of rate- $\frac{1}{3}$  outer SBCs combined with the rate-1 inner precoder, as mentioned in Table II, although they have the same over-all code rate. Quantitatively, using the rate- $\frac{1}{3}$  SBCs of Table II, an additional  $E_b/N_0$  gain of upto 27 dB may be achieved over the identical-rate benchmarker scheme dispensing with SBC.

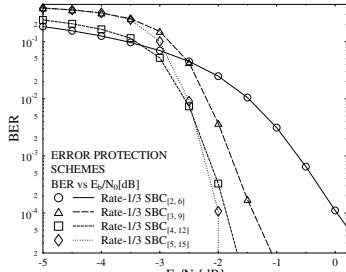


Fig. 7. BER performance of the various error protection schemes.

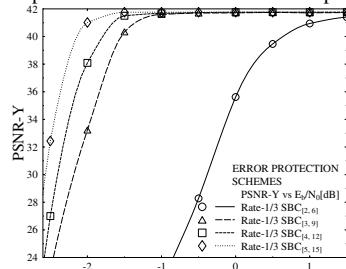


Fig. 8. PSNR-Y performance of the various error protection schemes.

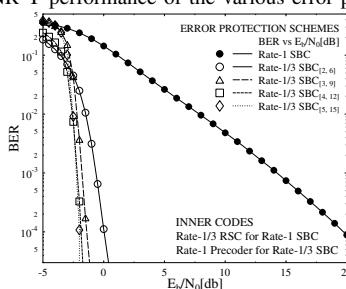


Fig. 9. BER vs  $E_b/N_0$  performance of the various error protection schemes.

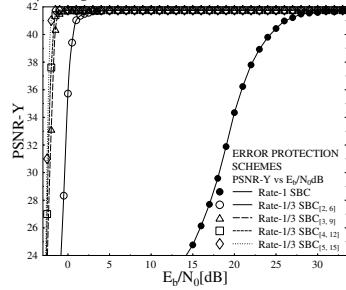


Fig. 10. PSNR-Y performance of various error protection schemes.

## VI. CONCLUSIONS

In this paper a generic low-complexity SBC coding scheme is proposed which satisfies the *necessary and sufficient* condition of  $d_{H,min} = 2$  for iterative detection convergence for

arbitrary SBSD-aided multimedia source codecs. We applied different error protection schemes considering the transmission of DP aided H.264/AVC coded video using carefully selected SBCs with diverse  $d_{H,min}$ . It was demonstrated that the bit-error correction capability of the ISCD scheme was significantly improved with the advent of rate- $\frac{1}{3}$  SBC scheme owing to the deliberate increase in redundancy of the source coded bit-stream. Additionally, the convergence behaviour of different SBC schemes in the ISCD paradigm was analysed using EXIT charts. The H.264/SBC/rate-1 precoder design example exhibited an  $E_b/N_0$  gain of 3 dB at the  $PSNR$  degradation point of 1 dB, when using SBCs having  $d_{H,min} = 6$  compared to the identical-rate SBCs having  $d_{H,min} = 3$ . Moreover, an  $E_b/N_0$  gain of 27 dB was attained using iterative soft-bit source and channel decoding with the aid of rate- $\frac{1}{3}$  SBCs relative to the identical-rate benchmarker.

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