

On the Design of Turbo Packet Combining Schemes for Relay-Assisted Systems over Multi-Antenna Broadband Channels

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Abstract—This paper focuses on turbo packet combining strategies for multi-relay-assisted systems operating over multiple-input–multiple-output (MIMO) broadband channel. We propose a frequency domain minimum mean square (MMSE)-based turbo packet combining scheme where all slots received signals and their corresponding channel frequency responses (CFRs) are used to decode the data packet. We also provide an efficient recursive implementation way for the proposed scheme, and show that both its computational complexity and memory requirements are quite insensitive to the number of relays in the system. For the special case of cooperative automatic repeat request (ARQ) systems, we introduce an adaptive packet combining algorithm that enable to reduce the receiver implementation cost. Block error rate (BLER) performance are provided to demonstrate the gains offered by the proposed combining scheme over the conventional soft information-based combining.

Index Terms—Cooperative relaying, multiple-antenna systems, turbo equalization, packet combining.

I. INTRODUCTION

Relaying is an efficient diversity technique that allows to combat multi-path fading in wireless communications [1], [2]. In this transmission mechanism, one or more relays assist the communication between the source and destination to form a multiple- input–multiple-output (MIMO) system and therefore build up space–time diversity branches that are exploited at the destination. Several interesting relaying schemes have been proposed, among which are two basic modes: amplify-and-forward (AF), and decode-and-forward (DF). The AF strategy represents the simplest way that a relay may cooperate with the source and the destination. Under this scheme, the relay simply amplifies the received signal and forwards it towards the destination. However, in the DF scheme, the relay first decodes the signal received from the source, re-encodes and retransmits it to the destination. This approach suffers from error propagation when the relay transmits an erroneously decoded data block [3]. Selective DF, where the relay only transmits when it can reliably decode the data packet, has been introduced as an efficient method to reduce error propagation [4].

To improve spatial diversity of a relaying system, signals received over the source–destination and the relay–destination links are combined at the receiver side. Most of the research

work in this area has focused on flat fading channels. However, in practical systems, channels connecting the source, the relay and the destination may suffer from inter-symbol interference (ISI) caused by frequency-selective fading. In [5], the authors have introduced a packet combining strategy for AF scheme operating under the so-called protocol I, where the source broadcasts the data packet to both the relay and the destination during the first slot, and both the source and the relay re-send the packet to the destination during the second slot [6]. Block equalization has been proposed in [7] for protocol III, where the broadcast nature of the channel is not considered, i.e., the source sends to the relay during the first slot, and both the source and the relay send to the destination in the second slot. In [8], a frequency-domain equalizer with diversity combining has been proposed for single antenna cooperative systems with demodulate-and-forward relaying and using the so-called protocol II. In this protocol, the operation mode during the first slot is similar to that of protocol I, while during the second slot only the relay sends the packet to the destination.

In this paper, we consider a broadband multi-relay-assisted system using selective DF scheme. We focus on systems operating under the framework of protocol II as it is widely regarded as an efficient relaying scheme for increasing the overall throughput. In this paper, inspired by the packet combining concept introduced in [9], [10] for ARQ, we propose an MMSE-based turbo packet combining scheme for broadband relaying systems. We then provide an efficient recursive implementation for the proposed scheme, and show that both its computational complexity and memory requirements are quite insensitive to the number of relays in the system. We also introduce a low complexity adaptive packet combining scheme for the special case of cooperative ARQ communication, where the feedback from the destination is exploited and the packet repetition is activated only if the destination fails to decode the data packet [11], [12]. Throughout the paper we use the following notation: $(.)^\top$ and $(.)^H$ are the transpose and the transpose conjugate of the argument, respectively. $\text{diag}\{\mathbf{x}\}$ and $\text{diag}\{\mathbf{X}_1, \dots, \mathbf{X}_m\}$ denote the diagonal matrix and the block diagonal matrix constructed from $\mathbf{x} \in \mathbb{C}^n$ and from $\mathbf{X}_1, \dots, \mathbf{X}_m \in \mathbb{C}^{n_1 \times n_2}$, respectively. For $\mathbf{x} \in \mathbb{C}^{TN}$, \mathbf{x}_f denotes the discrete Fourier transform (DFT) of \mathbf{x} , i.e.

$\mathbf{x}_f = \mathbf{U}_{T,N} \mathbf{x}$, with $\mathbf{U}_{T,N} = \mathbf{U}_T \otimes \mathbf{I}_N$, where \mathbf{I}_N is the $N \times N$ identity matrix, \mathbf{U}_T is a unitary $T \times T$ matrix whose (m, n) th element is $(\mathbf{U}_T)_{m,n} = \frac{1}{\sqrt{T}} e^{-j(2\pi mn/T)}$, $j = \sqrt{-1}$, and \otimes denotes the Kronecker product.

The remainder of the paper is organized as follows: In Section II, we introduce the relay system model. In Section III, we provide the structure of the proposed packet combining scheme as well as its efficient recursive implementation. The adaptive packet combining algorithm for cooperative ARQ systems is detailed in Section IV. The performance analysis is provided in Section V. Finally, the paper is concluded in Section VI.

II. RELAY SYSTEM MODEL

We consider a relay-assisted wireless communication system where the M_S antenna source denoted as S transmits information blocks to the M_D antenna destination denoted as D with the assistance of $K - 1$ dedicated selective DF relays denoted as $R_2, \dots, R_k, \dots, R_K$. Each relay R_k is equipped with M_{R_k} transmit and receive antennas. The source-relay ($S \rightarrow R_k$), source-destination ($S \rightarrow D$), and relay-destination ($R_k \rightarrow D$) links are assumed to be frequency selective. The channel matrices corresponding to the $A \rightarrow B$ link are $\mathbf{H}_0^{(AB)}, \dots, \mathbf{H}_{L_{AB}-1}^{(AB)} \in \mathbb{C}^{M_B \times M_A}$, where L_{AB} denotes the number of symbol-spaced taps, and $A \in \{S, R_k\}$, and $B \in \{R_k, D\}$. Their entries are zero-mean circularly symmetric complex Gaussian random variables. Cyclic prefix (CP)-aided transmission is assumed for all links. The average energies of the different links are E_{SR_k} , E_{SD} and E_{R_kD} , and take into account the path-loss and shadowing effects of each link.

In this paper, the relay-assisted system use up to K time slots for sending one information block from the source to the destination to guarantee orthogonal transmissions, where each slot spans T channel use. First, the source encodes its data blocks using a space-time bit interleaved coded modulation (STBICM) encoder. The resulting symbol vector is given by,

$$\mathbf{s} \triangleq [\mathbf{s}_0^\top, \dots, \mathbf{s}_{T-1}^\top]^\top \in \mathcal{S}^{M_S T}, \quad (1)$$

where $\mathbf{s}_i \triangleq [s_{1,i}^\top, \dots, s_{t,i}^\top, \dots, s_{M_S,i}^\top]^\top \in \mathcal{S}^{M_S}$ is the symbol vector at channel use $i = 0, \dots, T-1$, and \mathcal{S} is the symbol constellation set. During the first slot, the source inserts a CP symbol word of length $T_{CP}^{S \rightarrow D} \geq \max_{k=2, \dots, K} (L_{SR_k}, L_{SD})$, then broadcasts the resulting symbol frame to the $K - 1$ relays and the destination. After CP deletion, the baseband $M_D \times 1$ signal vector obtained at the destination is given by,

$$\mathbf{y}_i^{(1)} = \sqrt{E_{SD}} \sum_{l=0}^{L_{SD}-1} \mathbf{H}_l^{(1)} \mathbf{s}_{(i-l) \bmod T} + \mathbf{n}_i^{(1)}, \quad (2)$$

where $\mathbf{H}_l^{(1)} = \mathbf{H}_l^{(SD)}$, and $\mathbf{n}_i^{(1)} \sim \mathcal{N}(\mathbf{0}_{M_D \times 1}, \sigma^2 \mathbf{I}_{M_D})$ is the thermal noise at the destination receiver. During the following $K - 1$ slots, each relay decodes the signal received from the source, if the data packet is correctly decoded, the relay re-encodes and retransmits it to the destination during the allocated slot k , using M_S antennas, otherwise, the packet

retransmission is not activated during slot k . At each slot $k = 2, \dots, K$, if the packet retransmission is activated, the $M_D \times 1$ received signal vector at the destination side, after CP deletion, can be expressed as

$$\mathbf{y}_i^{(k)} = \sqrt{E_k} \sum_{l=0}^{L_k-1} \mathbf{H}_l^{(k)} \mathbf{s}_{(i-l) \bmod T} + \mathbf{n}_i^{(k)}. \quad (3)$$

where $\mathbf{H}_l^{(k)} = \mathbf{H}_l^{(R_k D)} \in \mathbb{C}^{M_D \times M_S}$, $L_k = L_{R_k D}$, and $E_k = E_{R_k D}$.

III. FREQUENCY DOMAIN MMSE-BASED TURBO PACKET COMBINING

In this paper the data packet is decoded in iterative fashion through the exchange of extrinsic information between the proposed soft packet combiner and SISO decoder. In cooperative ARQ systems, the destination performs the data packet decoding during each time slot k , while in fixed-relay based systems, the data packet decoding didn't start before the last time slot K . First, the frequency domain block signal vector $\underline{\mathbf{y}}_f^{(k)}$ is constructed. Second, the optimal soft combiner computes the extrinsic log-likelihood ratio (LLR) about coded and interleaved bits using *a priori* information. Then, the extrinsic LLRs corresponding to coded and interleaved bits are desinterleaved, and transferred to the SISO decoder. After a preset number of iterations, the decision about the data packet is performed. In cooperative ARQ systems, If the packet is incorrectly decoded at time slot k , a NACK message is sent to the relay $k + 1$ which starts the packet retransmission process. If the packet is correctly decoded, the destination broadcast an ACK message to both source and relays to stop the relaying transmission and start the transmission of a new data packet during the next time slot. In this section, we propose an MMSE-based turbo packet combining scheme for broadband multi-relay-assisted systems. The concept performs, in the frequency domain, interference cancellation and MMSE filtering jointly for all slots by considering each slot as an additional set of virtual M_D receive antennas. We also introduce an efficient implementation scheme for systems with $K M_D > M_S$.

A. The Proposed Turbo Packet Combining Scheme

After k time slots, the system (source, $k - 1$ relays, and destination) can be viewed as a point to point MIMO link with M_S transmit and $k M_D$ receive antennas. Using the “virtual antennas” concept, we construct the $k M_D T \times 1$ block received signal vector $\underline{\mathbf{y}}^{(k)}$ after k slots as,

$$\underline{\mathbf{y}}^{(k)} \triangleq [\underline{\mathbf{y}}_0^{(k)\top}, \dots, \underline{\mathbf{y}}_{T-1}^{(k)\top}]^\top \in \mathbb{C}^{k M_D T}, \quad (4)$$

where

$$\underline{\mathbf{y}}_i^{(k)} \triangleq [\mathbf{y}_i^{(1)\top}, \dots, \mathbf{y}_i^{(k)\top}]^\top \in \mathbb{C}^{k M_D} \quad (5)$$

is the signal received over the $k M_D$ virtual antennas corresponding to reception over k consecutive slots using M_D receive antennas. The block communication model corresponding to this k -slot scheme is given by,

$$\mathbf{y}^{(k)} = \underline{\mathbf{H}}^{(k)} \mathbf{s} + \underline{\mathbf{n}}^{(k)}, \quad (6)$$

where $\underline{\mathcal{H}}^{(k)} \in \mathbb{C}^{kM_D T \times M_S T}$ is a block circulant matrix whose first $kM_D T \times M_S$ block column matrix is

$$\left[\underline{\mathbf{H}}_0^{(k)\top}, \dots, \underline{\mathbf{H}}_{T-1}^{(k)\top}, \mathbf{0}_{(T-L)kM_D \times M_S} \right]^\top, \quad (7)$$

with

$$\begin{cases} L &= \max_{k=1,\dots,K} (L_k), \\ \underline{\mathbf{H}}_l^{(k)} &\triangleq \left[\sqrt{E_1} \mathbf{H}_l^{(1)\top}, \dots, \sqrt{E_k} \mathbf{H}_l^{(k)\top} \right]^\top \in \mathbb{C}^{kM_D \times M_S}, \end{cases} \quad (8)$$

correspond to the order of the virtual MIMO channel, and $\underline{\mathbf{H}}_l^{(k)}$ is the channel matrix of the l th virtual tap. Vector

$$\underline{\mathbf{n}}^{(k)} \triangleq \left[\underline{\mathbf{n}}_0^{(k)\top}, \dots, \underline{\mathbf{n}}_{T-1}^{(k)\top} \right]^\top \in \mathbb{C}^{kM_D T}, \quad (9)$$

where

$$\underline{\mathbf{n}}_i^{(k)} \triangleq \left[\mathbf{n}_i^{(1)\top}, \dots, \mathbf{n}_i^{(k)\top} \right]^\top \quad (10)$$

$\sim \mathcal{N}(\mathbf{0}_{kM_D \times 1}, \sigma^2 \mathbf{I}_{kM_D})$ denotes the thermal noise present in the k -slot equivalent MIMO communication system. Note that the block circulant matrix $\underline{\mathcal{H}}^{(k)}$ can be block diagonalized in a Fourier basis as

$$\underline{\mathcal{H}}^{(k)} = \mathbf{U}_{T,kM_D}^H \underline{\Lambda}^{(k)} \mathbf{U}_{T,M_S}. \quad (11)$$

Therefore, applying the DFT \mathbf{U}_{T,kM_D} on the k -slot received block signal vector (4) yields the following frequency domain block communication model,

$$\underline{\mathbf{y}}_f^{(k)} = \underline{\Lambda}^{(k)} \mathbf{s}_f + \underline{\mathbf{n}}_f^{(k)}, \quad (12)$$

where

$$\begin{cases} \underline{\Lambda}^{(k)} \triangleq \text{diag} \left\{ \underline{\Lambda}_0^{(k)}, \dots, \underline{\Lambda}_{T-1}^{(k)} \right\} \in \mathbb{C}^{kM_D \times M_S T}, \\ \underline{\Lambda}_i^{(k)} = \sum_{l=0}^{L-1} \underline{\mathbf{H}}_l^{(k)} e^{-j(2\pi il/T)} \in \mathbb{C}^{kM_D \times M_S}. \end{cases} \quad (13)$$

In the following, we eliminate the slot index k for notation simplicity. Remember that in fixed-relay based systems, the following data packet processing is performed just one time when $k = K$, while in cooperative ARQ, it is performed each time slot k . Let $\tilde{\mathbf{s}}$ denote the conditional estimate of \mathbf{s} , and $\sigma_{t,i}^2$ the conditional variance of $s_{t,i}$. With the aid of multi-slot block communication model (12), the MMSE estimate \mathbf{z}_f about \mathbf{s}_f , can be expressed according to the following forward-backward filtering structure,

$$\mathbf{z}_f = \Phi \underline{\mathbf{y}}_f - \Psi \tilde{\mathbf{s}}_f, \quad (14)$$

where $\Phi = \text{diag} \{ \Phi_0, \dots, \Phi_{T-1} \}$ is the multi-slot forward MMSE filter given by,

$$\begin{cases} \Phi_i \triangleq \underline{\Lambda}_i^H \mathbf{B}_i^{-1}, \\ \mathbf{B}_i = \sigma^2 \mathbf{I}_{kM_D} + \underline{\Lambda}_i \tilde{\Xi} \underline{\Lambda}_i^H. \end{cases} \quad (15)$$

$\tilde{\Xi}$ is an unconditional covariance computed as the time average of conditional covariance matrices Ξ_i defined as,

$$\Xi_i \triangleq \text{diag} \{ \sigma_{1,i}^2, \dots, \sigma_{M_S,i}^2 \}, \quad (16)$$

and $\Psi = \text{diag} \{ \Psi_0, \dots, \Psi_{T-1} \}$ is the multi-slot backward MMSE filter given by,

$$\begin{cases} \Psi_i \triangleq \Phi_i \underline{\Lambda}_i - \Upsilon, \\ \Upsilon = \frac{1}{T} \sum_{i=0}^{T-1} \Phi_i \underline{\Lambda}_i. \end{cases} \quad (17)$$

After computing (14), the inverse DFT (IDFT) is then applied to \mathbf{z}_f to obtain the equalized time domain sequence,

$$\mathbf{z} = \mathbf{U}_{T,M}^H \mathbf{z}_f. \quad (18)$$

The MMSE estimate $z_{t,i}$ corresponding to antenna t and channel use i can be simply extracted from \mathbf{z} as $z_{t,i} = \mathbf{e}_{t,i}^H \mathbf{z}$, with $\mathbf{e}_{t,i}$ denotes the $(M_S i + t)$ th vector of the canonical basis. At the n th iteration, The extrinsic LLRs values $\phi_{t,i,m,n}^{(e)}$ corresponding to coded and interleaved bits $b_{t,i,m}$ are then produced using the max-log simplification,

$$\begin{aligned} \phi_{t,i,m,n}^{(e)} &= \min_{s \in \mathcal{S}_0^m} \left\{ \frac{|z_{t,i} - g_{t,i}s|^2}{\theta_{t,i}^2} - \sum_{j \neq m} \phi_{t,i,j,n}^{(a)} \lambda_j \{s\} \right\} \\ &\quad - \min_{s \in \mathcal{S}_1^m} \left\{ \frac{|z_{t,i} - g_{t,i}s|^2}{\theta_{t,i}^2} - \sum_{j \neq m} \phi_{t,i,j,n}^{(a)} \lambda_j \{s\} \right\}, \end{aligned} \quad (19)$$

where $g_{t,i}$ and $\theta_{t,i}^2$ denote, respectively, the equivalent channel gain at the output of equalizer and the residual interference variance corresponding to discrete time i , and transmit antenna t , $\phi_{t,i,j,n}^{(a)}$ is the *a-priori* LLR for coded bit $b_{t,i,j}$ obtained from the decoder in the previous iteration, $\lambda_j \{s\}$ is an operator extracting the j th bit labeling the symbol $s \in \mathcal{S}$ and \mathcal{S}_β^m is the set of symbols having the m th bit set to β , i.e., $\mathcal{S}_\beta^m = \{s : \lambda_m \{s\} = \beta\}$. The calculated extrinsic LLRs are then de-interleaved and fed back to the soft-input-soft-output (SISO) decoder.

B. An Efficient Implementation Scheme For Relay-Assisted System With $KM_D > M_S$

Note that the proposed packet combining approach requires the computation of matrix inverses $\mathbf{B}_0^{-1}, \dots, \mathbf{B}_{T-1}^{-1} \in \mathbb{C}^{kM_D \times kM_D}$ at each turbo iteration, which involve a complexity order cubic against kM_D . Furthermore, the signals received at slots $1, \dots, k$ and their corresponding CFRs have to be stored at the receiver. The implementation of such a receiver could be feasible if $KM_D \leq M_S$. However, if $KM_D > M_S$, this packet combining approach could not be feasible in practice anymore, since the receiver will require a huge memory that increase linearly with the number of relays as well as the inversion of large matrices. In that case, to prevent the computation of $kM_D \times kM_D$ matrix inversions, we apply the matrix inversion lemma [13] that allow us to express the inverse of \mathbf{B}_i^{-1} as

$$\mathbf{B}_i^{-1} = \frac{1}{\sigma^2} \left\{ \mathbf{I}_{kM_D} - \underline{\Lambda}_i \mathbf{C}_i^{-1} \underline{\Lambda}_i^H \right\}, \quad (20)$$

with $\mathbf{C}_i = \sigma^2 \tilde{\Xi}^{-1} + \underline{\Lambda}_i^H \underline{\Lambda}_i \in \mathbb{C}^{M_S \times M_S}$. This reduces the implementation cost since the complexity order becomes cubic against M_S instead of kM_D . For the memory size issue, we

introduce two variables recursively computed, $\tilde{\mathbf{y}}_f^{(k)}$ and $\underline{\mathbf{D}}_i^{(k)}$, to avoid storing received signals and CFRs corresponding to all time slots. The first variable $\tilde{\mathbf{y}}_f^{(k)}$ is introduced to store the received signals and calculated using the following recursion,

$$\begin{cases} \tilde{\mathbf{y}}_f^{(k)} = \tilde{\mathbf{y}}_f^{(k-1)} + \Lambda^{(k)^H} \mathbf{y}_f^{(k)}, \\ \underline{\mathbf{D}}_i^{(0)} = \mathbf{0}_{TM_S \times 1}. \end{cases} \quad (21)$$

The second variable $\underline{\mathbf{D}}_i^{(k)}$ is used to store the channel frequency responses and calculated as,

$$\begin{cases} \underline{\mathbf{D}}_i^{(k)} = \underline{\mathbf{D}}_i^{(k-1)} + \Lambda_i^{(k)^H} \Lambda_i^{(k)}, \\ \underline{\mathbf{D}}_i^{(0)} = \mathbf{0}_{M_S \times M_S}. \end{cases} \quad (22)$$

Note that the storage requirements become linear in term of transmit antennas and insensitive to the receive antennas and the number of relays. By using the matrix inversion lemma, we re-write the expression of soft MMSE packet combining as,

$$\mathbf{z}_f = \Gamma \tilde{\mathbf{y}}_f^{(k)} - \Omega \tilde{\mathbf{s}}_f, \quad (23)$$

where $\Gamma = \text{diag}\{\Gamma_0, \dots, \Gamma_{T-1}\} \in \mathbb{C}^{TM_S \times TM_S}$, and $\Omega = \text{diag}\{\Omega_0, \dots, \Omega_{T-1}\} \in \mathbb{C}^{TM_S \times TM_S}$ denote the new forward and backward filters, respectively, and are given by,

$$\begin{cases} \Gamma_i \triangleq \frac{1}{\sigma^2} \left\{ \mathbf{I}_{M_S} - \underline{\mathbf{D}}_i^{(k)} \mathbf{C}_i^{-1} \right\}, \\ \mathbf{C}_i = \sigma^2 \tilde{\boldsymbol{\Xi}}^{-1} + \underline{\mathbf{D}}_i^{(k)}, \end{cases} \quad (24)$$

$$\begin{cases} \Omega_i \triangleq \Gamma_i \underline{\mathbf{D}}_i^{(k)} - \Upsilon, \\ \Upsilon = \frac{1}{T} \sum_{i=0}^{T-1} \Gamma_i \underline{\mathbf{D}}_i^{(k)}. \end{cases} \quad (25)$$

IV. ADAPTIVE PACKET COMBINING ALGORITHM FOR COOPERATIVE ARQ SYSTEMS

In cooperative ARQ systems, where the data packet decoding is performed each time slot k , the use of the efficient packet combining scheme, proposed in Sub-Section III-B, is not always the best solution to reduce the implementation cost. As we showed before, the proposed packet combining scheme involve the inversion of matrices, and depending on the implementation scheme, this matrices inversion introduces a computational complexity cubic in term of number of transmit antennas or virtual receive antennas. Therefore, if the relay-assisted system has the number of receive antennas less than transmit antennas, i.e. $M_D < M_S$, the use of the first packet combining scheme, proposed in Sub-Section III-A, during the first slots, enable to reduce the computational complexity of the receiver. However, when the receiver fails to correctly decode the data packet before the number of virtual receive antennas, in the next slot k , becomes greater than transmit antennas, i.e. $kM_D > M_S$, the first packet combining scheme is not the best implementation way to reduce the computational complexity anymore. In that case, the efficient packet combining scheme, proposed in Sub-Section III-B, becomes the best solution. To take advantage of both packet combining scheme proposed in this paper, we introduce an adaptive turbo packet combining algorithm for cooperative ARQ that we summarize in Table I.

Table I
SUMMARY OF THE ADAPTIVE TURBO PACKET COMBINING ALGORITHM
FOR COOPERATIVE ARQ SYSTEM

-
- 0. At each round k**
Compare kM_D and M_S .
1. If $kM_D \leq M_S$
 - 1.1.** Construct $\tilde{\mathbf{y}}_0^{(k)}, \dots, \tilde{\mathbf{y}}_{T-1}^{(k)}$ and $\underline{\mathbf{H}}_0^{(k)}, \dots, \underline{\mathbf{H}}_{L-1}^{(k)}$ using (5) and (8), respectively.
 - 1.2.** Compute the DFT of the virtual received signal and the CFRs at slot k , i.e. $\tilde{\mathbf{y}}_f^{(k)}$ and $\underline{\Lambda}^{(k)}$, respectively.
 - 1.3.** For each iteration,
 - 1.3.1.** Compute the forward and backward filters using (15) and (17).
 - 1.3.2.** Compute the MMSE estimate of \mathbf{x}_f using (14).
 - 1.3.3.** Compute the extrinsic LLRs.
 - 1.3.4.** Perform SISO decoding.
 - 1.4.** end 1.3.
 - 1.5.** Frame error test
 - 1.5.1.** If “correct frame” then send “ACK” and empty the memory buffers.
Otherwise, compare $(k+1)M_D$ and M_S .
If $(k+1)M_D > M_S$, compute
 $\tilde{\mathbf{y}}_f^{(k)} = \underline{\Lambda}^{(k)^H} \mathbf{y}_f^{(k)}$ and $\underline{\mathbf{D}}_i^{(k)} = \underline{\Lambda}^{(k)^H} \underline{\Lambda}^{(k)}$,
empty the memory buffers of the old setting,
and replace it by $\tilde{\mathbf{y}}_f^{(k)}$ and $\underline{\mathbf{D}}_i^{(k)}$.
 - 1.5.2.** Go to 0.
 - 2. If** $kM_D > M_S$
 - 2.1.** Update $\tilde{\mathbf{y}}_f^{(k)}$ and $\underline{\mathbf{D}}_i^{(k)}$ using recursions (21) and (22), respectively.
 - 2.2.** For each iteration,
 - 2.2.1.** Compute the forward and backward filters using (24) and (25).
 - 2.2.2.** Compute the MMSE estimate of \mathbf{x}_f using (23).
 - 2.2.3.** Compute the extrinsic LLRs.
 - 2.2.4.** Perform SISO decoding.
 - 2.3.** end 2.2.
 - 2.4.** Frame error test
 - 2.4.1.** If “correct frame” then send “ACK” and empty the memory buffers.
Otherwise, send “NACK”.
 - 2.5.** Go to 0.
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V. PERFORMANCE EVALUATION

In this section, we evaluate the block error rate (BLER) performance of the proposed packet combining scheme. We use conventional LLR-level packet combining as a reference. In this basic combining scheme, the turbo equalization is performed separately for each slot, and before SISO decoding, the extrinsic LLRs are simply added together with those obtained at the last iteration of previous slot. In all simulations, we consider a STBICM scheme where the encoder is a 16 state convolutional code with polynomial generators $(35, 23)_8$, and the modulation scheme is quadrature phase shift keying (QPSK). The length of the code frame is 2048 bits including tails, and the CP length is $T_{CP} = 3$. We use the Max-Log-MAP algorithm for SISO decoding, and the iterative MMSE receiver at the destination runs three turbo iterations. We consider relay-assisted systems with one, two, and three relays. The SNR_{SD} appearing in all figures is the S → D link signal-to-noise ratio per useful bit per receive antenna. For simplicity, we consider a homogeneous case in which the

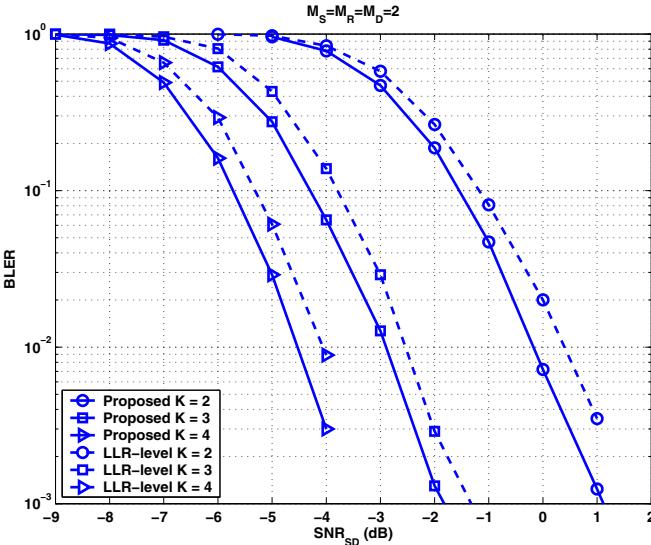


Figure 1. BLER performance for CC $(35, 23)_8$, QPSK, $M_S = M_R = M_D = 2$, $L = 3$ equal energy paths, $l_{SR} = 0.3$ and the path loss exponent $\kappa = 3$.

distance between the source and the relay l_{SR} , the relay and the destination l_{RD} , and the source and the destination l_{SD} are normalized as $l_{SR} + l_{RD} = l_{SD} = 1$. We assume that all relays are at the same distance to the source, i.e. $l_{SR} = 0.3$ and the destination, i.e. $l_{RD} = 0.7$. All links have the same frequency-selective fading channel profile, i.e., $L = 3$ equal power paths with the same path loss exponent $\kappa = 3$. The link average energy is assumed to be $E_{AB} = (l_{AB})^{-\kappa}$ with $A = S$ or R , and $B = R$ or D . Our main concern is to show the superior performance of the proposed combining strategy compared to LLR-level combining. First, we consider in Fig. 1 a relaying system with the same number of transmit and receive antennas $M_S = M_R = M_D = 2$. We observe that the proposed combining strategy clearly outperforms LLR-level combining. However, the performance gap, at 10^{-2} BLER, is less than 1dB for systems with one, two and three relays. Fig. 2 shows the performance of an overloaded system where $M_S = M_R = 2$ and $M_D = 1$. In this case, the proposed combining strategy significantly outperforms LLR-level combining, i.e., the performance gap is more than 2dB at 10^{-2} BLER for systems with one relay and 1.5dB for systems with three relays.

VI. CONCLUSION

In this paper, we have proposed turbo packet combining strategies for multi-relay-assisted systems operating over MIMO broadband channel. Using the “virtual antennas” concept, we have introduced a frequency domain MMSE-based turbo packet combining scheme where all slots received signals and their corresponding CFRs are jointly used to decode the data packet. Then, we have provided an efficient recursive implementation for the proposed scheme, and have shown that both its computational complexity and memory requirements are quite insensitive to the number of relays in the system. We have also introduced an adaptive packet combining algorithm that enable to reduce the receiver implementation cost

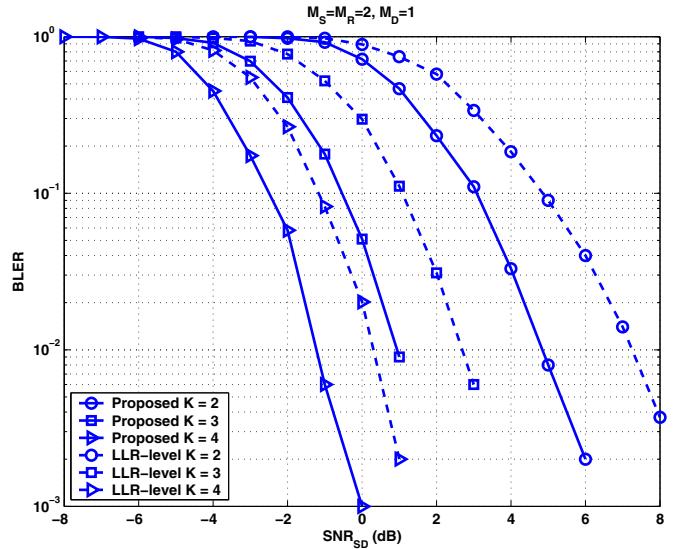


Figure 2. BLER performance for CC $(35, 23)_8$, QPSK, $M_S = M_R = 2$, $M_D = 1$ $L = 3$ equal energy paths, $l_{SR} = 0.3$ and the path loss exponent $\kappa = 3$.

for systems using cooperative ARQ transmission technique. Simulations results have shown that the proposed combining strategy provides better BLER performance than conventional LLR-level combining.

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