

Message from the Tutorial Co-Chairs



Welcome to the 33rd International Conference on VLSI Design (VLSID 2020) concurrently with 19th International Conference on Embedded Systems (ES 2020), happening in Leela Palace Hotel, Bangalore, India. The conference features six full-day tutorials to be held on Jan 4th and Jan 5th, 2020. The tutorials are organized and presented by academic experts on six different themes that we believe are very relevant to current research directions and application areas.

Below is the list of Tutorials (Speakers):

1. Open-Source EDA and Machine Learning for IC Design - A Live Update
(*Abdelrahman Hosny, Brown University, Andrew B. Kahng, UC, San Diego*)
2. Hardware Platforms for Artificial Intelligence
(*Manish Pandey, Synopsys, Swagath Venkataramani, IBM, Yu Wang, Tsinghua University*)
3. Advances in Power Management for Secure IoT and Efficient Mobile Applications
(*Qadeer Khan, IIT Madras, Shreyas Sen, Purdue University*)
4. Invisible Computing: Embedded Systems
(*Luca Carloni, Columbia University, Andreas Gerstlauer, UT, Austin, Tulika Mitra, NUS, Sri Parameswaran, University of New South Wales*)
5. Secure Circuits and Systems
(*Shivam Bhasin, Nanyang Technical University, Makoto Ikeda, University of Tokyo, Makoto Nagata, Kobe University, Simha Sethumadhavan, Columbia University*)
6. Resiliency and Testability: Key Drivers for Self-Driving Vehicles
(*J. A. Abraham, University of Texas, Austin, S. Blanton, Carnegie Mellon University, A. Chatterjee, Georgia Tech, N. Saxena, NVidia Corp*)

Sincere thanks to all the tutorial speakers for their contributions and making this a world-class program. We sincerely hope that you will enjoy the tutorial program at VLSID & ES 2020!

Amir Aminifar & Shabbir Batterywala

Tutorial Program Co-Chairs, VLSID 2020