Execute

Execute is implemented in a manner very similar to quad input. The string to be executed is placed in the input buffer and TYPEIN is called, resulting in a block of code followed by a pointer to the symbol to the left of the execute symbol. The code is then executed and a return is made to that symbol. Note that both quad input and execute may be nested to any depth, as long as there is space to store the generated code.

Saved Workspaces

Upon a) save sommand, portions of tables and various pointers are written on a binary file. If a file by the specified name exists, it will be used. Otherwise, an attempt is made to create a two link permanent file. Two links equals 7680-36 bit words, enough to hold a full workspace. If unsuccessful, because the user has not been allocated sufficient file space, a temporary file is defined. Temporary files are released when the terminal session is ended. However, the user may transfer the data to a permanent file prior to logging off. Saved files may be less than two links by creating the files in advance of the APL session.

An On-Line Proof Checker Operating under APL/360, with Educational Applications in Logic, Mathematics, and Computer Science.

P. D. Page Campus Computing Network UCLA

One of the long standing problems of Computer Science is the development of programs capable of checking, correcting, and possibly even prompting or suggesting logical reasoning in various areas of human activity. It is a problem with a good chance of solution. The obvious application for such a reasoning program is formal logic itself, but other application areas include reasoning about everyday real world problems, proving theorems of mathematics, and verifying correctness of procedures for solving problems, i.e., "programs" in one sense or another.

There are several valid starting points of attack on this problem. We have started by developing a proof checking program that will check the correctness of logical deductions written in a modern system of formal logic (D. Kalish and R. Montague, "Logic, Techniques of Formal Reasoning," Harcourt, Brace and World, Inc. 1964). This program has an additional theoremproving capability and can construct formal proofs for all of the standard theorems of propositional and predicate logic. This facility allows the user to make routine "jumps" in his chain of reasoning without the program making trivial complaints about incorrectness.

At present, the facilities available with this program are as follows:

The proof checking program operates in an on-line interactive mode under the APL/360 System. A proof to be checked is entered line by line at the terminal. As each line is entered, the program either accepts the logical statement as a valid consequence of prior proof lines, or rejects the statement as invalid. When a statement is accepted, it is numbered, formatted and typed out by the program for later reference. When a statement is rejected, a message is typed stating the reason for rejection.

The proof checking program automatically determines when a proof is complete and the assertion has been proved. A message is typed as soon as proof completion has occurred, and checking terminates.

As indicated previously, this program checks proofs of predicate logic with equality formulated according to the system of Kalish and Montague. All of the necessary inference rules of the

Kalish-Montague system have been implemented. The basic mode of program operation requires an annotation, i.e., inference rule name and line references, to accompany each new logical statement. If the statement entered does not follow from the referenced lines by the given inference rule, the statement is not accpted.

In accordance with the formalization of the Kalish-Montague System, a new subproof or derivation may be started at any point in a proof. This subproof may make use of any logical statement already acepted as part of the larger proof. Subproofs may also contain subproofs; nesting of subproofs may be continued indefinitely. The proof checking program keeps track of these subproofs and provides an indication when each is completed. This subproof completion message identifies the line containing the assertion which has been proved.

The basic mode of operation is augmented by a number of special problem oriented features. One of the most convenient features is the ability to enter an annotation without an accompanying logical statement. In this case, the program automatically deduces and displays all logical inferences following from the given annotation.

The proof checking program also has the capability of maintaining a list of axioms and theorems for use in constructing proofs. An axiom or theorem from this list may be recalled and entered as a line of the proof by an appropriate annotation.

The final and most powerful feature is an integrated theorem proving mode. A logical statement entered without annotation causes the proof checking program to invoke the theorem proving mode. This theorem prover is currently powerful enough to generate derivations for all elementary theorems of the propositional and predicate logic. It can easily verify statements which follow intuitively from previous logical statements, and thus eliminates the effort involved in leading the checking program step by step to the same logical consequence.

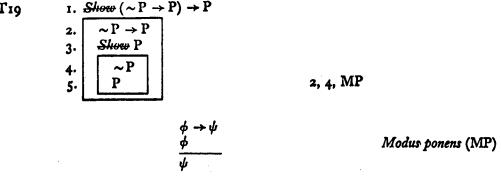
Though a complete set of inference rules is currently implemented, it will in the future, be convenient to be able to expand this list to include more powerful derived rules of inference. At times it may also be convenient to have inference rules which are especially well suited to a particular theory. The current proof checking program provides a means by which such derived rules of inference may be easily added.

At present, the input function has been tailored to accept a natural input language, i.e., mathematically oriented notation, so as to provide a system for checking proofs in mathematics (algebra and number theory). Eventually the formal system will be extended so that proofs of correctness of fairly simple computer programs can be generated and checked.

The program as it now stands has some obvious educational applications. In particular, it is expected that the current program will be used as an instructional aid by students of symbolic logic for performing homework problems and other exercises.

Example of Kalish-Montague Formal Logic Proof





SIGPLAN Notices

F

| | gram Response START | | User Input |
|--|---|---|--|
| PROOF | CHECKER VERSION 1 | 1 1 A APRTT. 1971 | - |
| | | toth ALAID 19/1 | SHOW (~P→P)→P |
| 1 | SHOW (~P+P)+P | | |
| | $\sim((\sim P+P)+P)$ | ASSUMP | |
| 3. | ~ <i>P</i> + <i>P</i> | ASSUMP | |
| +. | ~P | ASSUMP | |
| _ | P | | 3,4, <i>MP</i> |
| 5. PROOF (| P Completed, Checking 1 | 3,4, <i>MP</i> TERMINATED. | |
| Proof Ge | enerated by the APL Theor | cem Proving Functions | |
| | PROVE '(~P+P)+P' | | |
| | SHOW $(\sim P \rightarrow P) \rightarrow P$ | | |
| | $\sim ((\sim P + P) + P)$ | ASSUMP | |
| | ~ ₽₽ | ASSUMP | |
| 4. 5. | ~P (~P→P)∧~P | ASSUMP | |
| | PVP | 2, <i>NC</i> 3, <i>CD</i> | |
| n - | - · · - | | |
| | Р | | |
| 7. 8. PROO | P ~~P F COMPLETED SUCCESSF(F USED 2.92 SECONDS (| 4,3, <i>MP</i> 4,3, <i>MT</i> <i>ULLY</i> . | |
| 7 . 8 . PROO PROO | ~~P F COMPLETED SUCCESSF(| 4,3,MP 4,3,MT ULLY. OF CPU TIME. | Checker |
| 7. 8. PROO PROO | ~~P F COMPLETED SUCCESSF F USED 2.92 SECONDS of Proof Exhibiting Various RT - | 4,3, <i>MP</i> 4,3, <i>MT</i> <i>ULLY</i> . <i>OF CPU TIME</i> . Features of the Proof C <u>APL funct</u> | |
| 7. 8. PROO PROO | ~~P F COMPLETED SUCCESSF F USED 2.92 SECONDS of Proof Exhibiting Various | 4,3, <i>MP</i> 4,3, <i>MT</i> <i>ULLY</i> . <i>OF CPU TIME</i> . Features of the Proof C <u>APL funct</u> | ion which invokes checkin |
| 7. 8. PROO PROO STA OF CIIE | ~~P F COMPLETED SUCCESSF F USED 2.92 SECONDS (Proof Exhibiting Various RT VERSION 1.1. | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C <u>APL funct</u> A APRIL 1971 | ion which invokes checkin SHOW $(P+Q) \leftrightarrow (P \land \sim Q)$ |
| 7. 8. PROO PROO STA OF CHE SHO | $\sim\sim P$ F COMPLETED SUCCESSFT F USED 2.92 SECONDS (Proof Exhibiting Various RT VERSION 1.1. W (P+Q)++~(PA~Q) | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C <u>APL funct</u> A APRIL 1971 Assertion | ion which invokes checkin |
| 7. 8. PROO PROO STA OF CHE SHO | ~~P F COMPLETED SUCCESSF F USED 2.92 SECONDS (Proof Exhibiting Various RT VERSION 1.1. | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C <u>APL funct</u> A APRIL 1971 | to be proved |
| 7. 8. PROO PROO PROO STA OF CHE SHO ~(| $\sim\sim P$ F COMPLETED SUCCESSFT F USED 2.92 SECONDS (Proof Exhibiting Various RT VERSION 1.1. W (P+Q)++~(PA~Q) | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C <u>APL funct</u> A APRIL 1971 <u>Assertion</u> | ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW |
| 7. 8. PROO PROO PROO STA OF CHE SHO ~(SH ~ | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various RT | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C APL funct A APRIL 1971 Assertion ASSUMP An obviou the pr | ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW |
| 7. 8. PROO PROO PROO STA OF CHE SHO ~(SH ? P | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various RT VERSION 1.1. W (P+Q)++~(PA~Q) (P+Q)+~(PA~Q)) OW (P+Q)+~(PA~Q) ((P+Q)+~(PA~Q)) +Q | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C <u>APL funct</u> A APRIL 1971 <u>ASSUMP</u> An obviou the pr ASSUMP | ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW Is assertion is supplied by |
| 7. 8. PROO PROO PROO STA OF CHE SHO ~(SH ? P | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various RT | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C APL funct A APRIL 1971 Assertion ASSUMP An obviou the pr | tion which invokes checkin $SHOW (P+Q) \leftrightarrow (P \land \sim Q)$ to be proved SHOW is assertion is supplied by poof checker |
| 7. 8. PROO PROO PROO STA OF CIIE SHO ~(SH ? ? | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various $RT \longrightarrow$ $CKER VERSION 1.1.$ $W (P+Q)+ \rightarrow (P \land \sim Q) \longrightarrow$ $(P+Q) \leftrightarrow \sim (P \land \sim Q))$ $OW (P+Q) \leftrightarrow \sim (P \land \sim Q) \longrightarrow$ $((P+Q) \leftrightarrow \sim (P \land \sim Q)) \longrightarrow$ $((P+Q) \leftrightarrow \sim (P \land \sim Q)) \longrightarrow$ | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C APRIL 1971 ASSUMP ASSUMP ASSUMP ASSUMP | ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW Is assertion is supplied by |
| 7. 8. PROO PROO PROO STA OF CIIE SHO ~(SH ? ? | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various RT VERSION 1.1. W (P+Q)++~(PA~Q) (P+Q)+~(PA~Q)) OW (P+Q)+~(PA~Q) ((P+Q)+~(PA~Q)) +Q | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C <u>APL funct</u> A APRIL 1971 <u>ASSUMP</u> An obviou the pr ASSUMP | <pre>ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW s assertion is supplied by oof checker 6,DN</pre> |
| 7. 8. PROO PROO PROO STA OF CIIE SHO ~(SH ? ? | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various $RT \longrightarrow$ $CKER VERSION 1.1.$ $W (P+Q)+ \rightarrow (P \land \sim Q) \longrightarrow$ $(P+Q) \leftrightarrow (P \land \sim Q))$ $OW (P+Q) \leftrightarrow (P \land \sim Q) \longrightarrow$ $((P+Q) \leftrightarrow (P \land \sim Q)) \rightarrow$ $((P \land \sim Q)) \rightarrow$ | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C APL funct A APRIL 1971 Assertion ASSUMP ASSUMP ASSUMP 6,DN | tion which invokes checkin $SHOW (P+Q) \leftrightarrow (P \land \sim Q)$ to be proved SHOW is assertion is supplied by oof checker 6,DN 7,S \leftarrow Input in the |
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| 7. 8. PROO PROO PROO STA OF CHE SHO ~(SH P ~ P P | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various $RT \longrightarrow CKER VERSION 1.1.$ $W (P+Q)++\sim (P \wedge \sim Q) \longrightarrow (P+Q) + \sim (P \wedge \sim Q))$ $OW (P+Q)+\sim (P \wedge \sim Q) \longrightarrow ((P+Q)+\sim (P \wedge \sim Q)) \longrightarrow ((P \wedge \sim Q)) \longrightarrow (P \wedge \sim Q))$ $\sim Q$ Q | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C APL funct A APRIL 1971 Assertion ASSUMP ASSUMP ASSUMP ASSUMP 6,DN 7,S | <pre>ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW s assertion is supplied by oof checker 6,DN</pre> |
| 7. 8. PROO PROO PROO STA OF CHE SHO ~(SH P ~ P P | $\sim\sim P$ F COMPLETED SUCCESSFI F USED 2.92 SECONDS (Proof Exhibiting Various $RT \longrightarrow$ $CKER \longrightarrow VERSION 1.1.$ $W (P+Q) \leftrightarrow (P \land \sim Q) \longrightarrow$ $(P+Q) \leftrightarrow (P \land \sim Q))$ $OW (P+Q) \leftrightarrow (P \land \sim Q) \longrightarrow$ $((P+Q) \leftrightarrow (P \land \sim Q)) \rightarrow$ $((P+Q) \leftrightarrow (P \land \sim Q)) \rightarrow$ $((P \land \sim Q)) \rightarrow$ | 4,3,MP 4,3,MT ULLY. OF CPU TIME. Features of the Proof C A APRIL 1971 ASSUMP ASSUMP ASSUMP ASSUMP 6,DN 7.S 7,S | <pre>ion which invokes checkin SHOW (P+Q)+→~(P∧~Q) to be proved SHOW s assertion is supplied by oof checker 6,DN 7,S ← Input in the abbreviate form</pre> |

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•

| | | 5 | SHOW ~(P^~Q+(P+Q) |
|-------------|---|-----------------------------|--|
| SYNTA | CERROR IN INPUT, OR IMPROPE | R LINE -Syntax error | |
| 11. | SHOW ~(P^~Q)+(P+Q)- | Beginning of | |
| 12. | $\sim (\sim (P \land \sim Q) + (P + Q))$ | ASSUMP | |
| | ~(P^~Q) | ASSUMP Subproof is | indented |
| 14. | ~(P+Q) | ASSUMP | |
| | | | |
| 15. | SIIOW P+Q < | | ver is invoked to obtain us contradiction |
| 16. 17. | ~(P*~~)~~(P+~) P*~Q | 14, <i>NC</i> | |
| 1 /• | SUB-PROOF AT LINE 15 COME | - | |
| | SUB-PROOF AT LINE 11 COMPL | | |
| | | | 3,1, Correction of |
| | | | input (APL) |
| | | 3,11, <i>CB</i> | 1,08 |
| | (P+Q)++~(P^~Q) ~(P^~Q)++(P+Q) | 3,11, <i>CB</i> | |
| 19. 19. | COMPLETED, CHECKING TERMINA | | tion is detected |
| 1 11001 | | | |
| | | | |
| | ~~¢ ¢ | Double negation (DN) | |
| | \$ ~~\$ | | |
| | , , , , , , , , , , , , , , , , , , , | | |
| | $\frac{\phi \wedge \psi}{\phi} \frac{\phi \wedge \psi}{\psi}$ | Simplification (S) | |
| | φ <u>ψ</u> | | |
| | | | |
| | $ \begin{array}{c} \phi \rightarrow \psi \\ \phi \\ \psi \end{array} $ | | |
| | <u>φ</u> | Modus ponens (MP) | |
| | ψ | | |
| | | | |
| | $\phi \rightarrow \psi$ | | |
| | $\psi \rightarrow \phi$ | Conditional-biconditional (| (CB) |
| | $\phi \leftrightarrow \psi$ | | |
| | | | |
| | Example of a Pi | redicate Logic Proof | |
| | | | |
| | | | |
| | Proof Generated Interactiv | vely using the APL Pro | of Checker |
| | | | Hanna Tarant |
| | Program Response | | User Input |
| | START | | |
| PROOF | CHECKER VERSION 1.1A | <i>IPRIL</i> 1971 | |
| | | 2 | SHOW $\Delta[X](F[X]+G[X])+$ |
| 1. | SHOW $\Delta[X](F[X]+G[X])+(\Delta[X])$ | '[X]+Δ[X]G[X]) / | $(\Delta[X]F[$ |
| 2. | $\sim (\Delta[X](F[X] + G[X]) + (\Delta[X]F[X])$ | ASSUMP | 7 L |
| 3. | $\Delta[X](F[X] \rightarrow G[X])$ ~($\Delta[X]F[X] \rightarrow \Delta[X]G[X])$ | ASSIIMP | |
| 4. | ~(864] 2 64] 7 86 4] 7 66 4] 7 | | SHOW $\Delta[X]F[X] + \Delta[X]G[X]$ |
| 5. | SHOW A[X]F[X]+A[X]G[X] | | |
| 5. | $\Delta[X]F[X]$ | ASSUMP | |
| 7. | ~^[X]G[X] | ASSUMP | |
| | | | |
| | | | |

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| | | SHOW $\Delta[X]G[X]$ | |
|----------|--|------------------------------|--|
| 8. 9. | SHOW A[X]G[X] ~G[X] | ASSUMP | |
| 10. | F[X] | X 6,UI 6,UI | |
| 11. | <i>F</i> [<i>X</i>]+ <i>G</i> [<i>X</i>] | X 3, <i>UI</i> | |
| 12. | <i>G</i> [<i>X</i>] | 10,11, <i>MP</i> | |
| PROOF | SUB-PROOF AT LINE 8 SUB-PROOF AT LINE 5 COMPLETED, CHECKING T | COMPLETED. Notation: | |
| | Kalish-Montague | _ | |
| T201 | 1. Show $\Lambda x(Fx \to Gx) \to (\Lambda xFx)$ 2. $\Lambda x(Fx \to Gx)$ 3. Show $\Lambda xFx \to \Lambda xGx$ 4. ΛxFx 5. ΛxFx 6. Fx 7. $Fx \to Gx$ 8. Gx | | |
| | Aap 4 | Universal instantiation (UI) | |
| | $\frac{\phi \rightarrow \psi}{\phi}$ | Modus ponens (MP) | |

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A LANGUAGE MACHINE

Rodnay Zaks Center for Research in Management Science University of California, Berkeley

THE META-APL TIME-SHARING SYSTEM

META-APL is a multiprocessor time-sharing system developed at the University of California, Berkeley, for interactive real-time APL service. Its conceptual design has deliberately been kept simple and an expeptionally high performance to cost ratio has been obtained. Most of the functions traditionally performed by software have been pushed into the hardware-firmware execution unit. The core memory, although fast (750 nsec) is viewed by the processor strictly as an IO device.

The system was developed in two steps: completion of an APL processor and development of an operating-system processor, both processors being micro-programmed and